

General Description

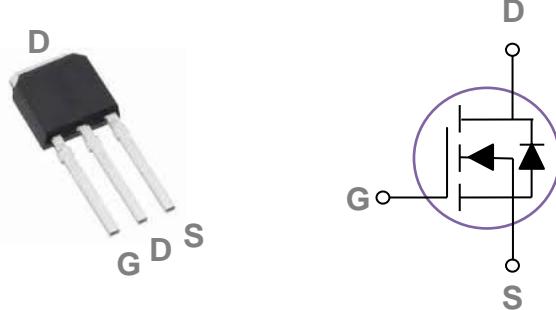
These N-Channel enhancement mode power field effect transistors are using Super Junction technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

BVDSS	RDS(ON)	ID
650V	360mΩ	11A

Features

- 650V, 11A, RDS(ON) = 360mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO251 Pin Configuration



Applications

- PFC Power Supply Stages
- Motor Control
- DC-DC Converters
- Adapter

Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	650	V
V _{Gs}	Gate-Source Voltage	±30	V
I _D	Drain Current – Continuous (T _c =25°C)	11	A
	Drain Current – Continuous (T _c =100°C)	6.9	A
I _{DM}	Drain Current – Pulsed ¹	44	A
EAS	Single Pulse Avalanche Energy	210	mJ
P _D	Power Dissipation (T _c =25°C)	75	W
	Power Dissipation – Derate above 25°C	0.6	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62.5	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	1.65	°C/W



650V N-Channel MOSFETs

PJR11N65N

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=1\text{mA}$	650	---	---	V
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=650\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=25^\circ\text{C}$	---	---	1	μA
		$\text{V}_{\text{DS}}=520\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 30\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=4\text{A}$	---	320	360	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D = 250\mu\text{A}$	2	3	4	V

Dynamic and switching Characteristics

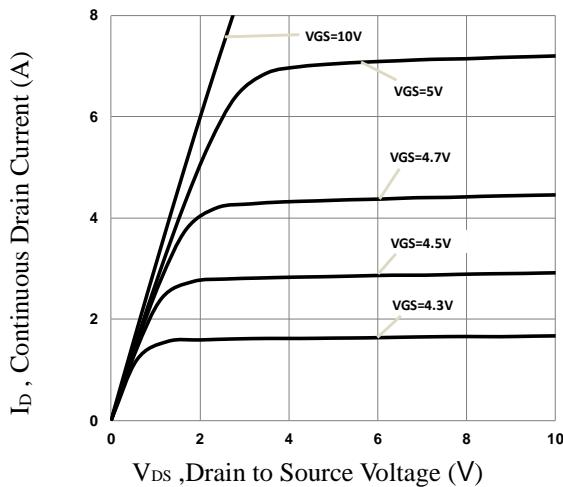
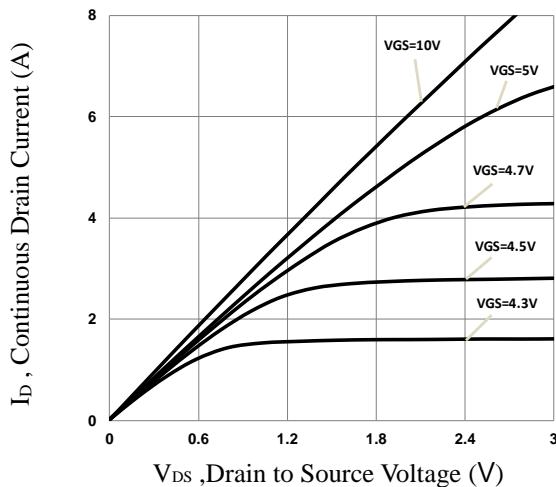
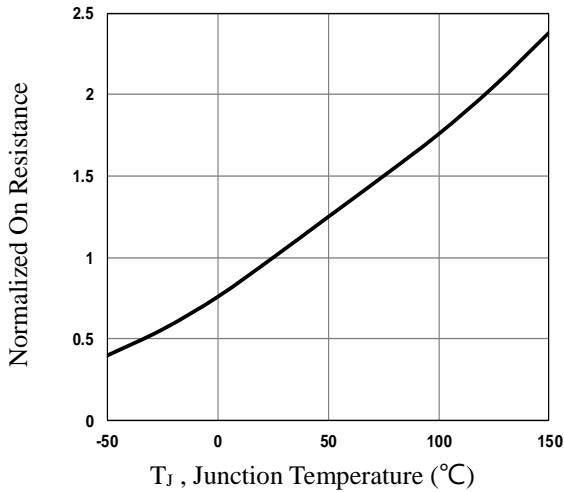
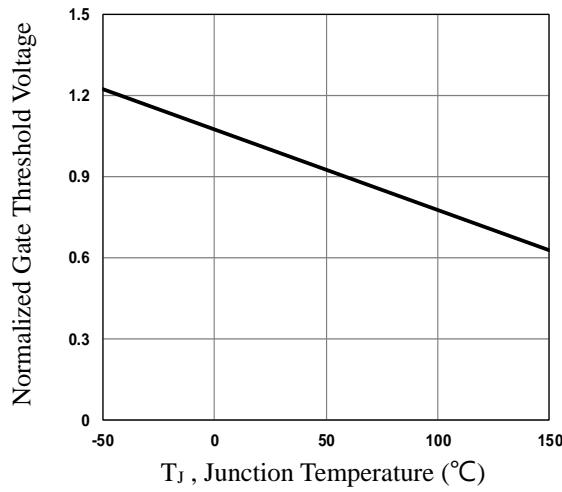
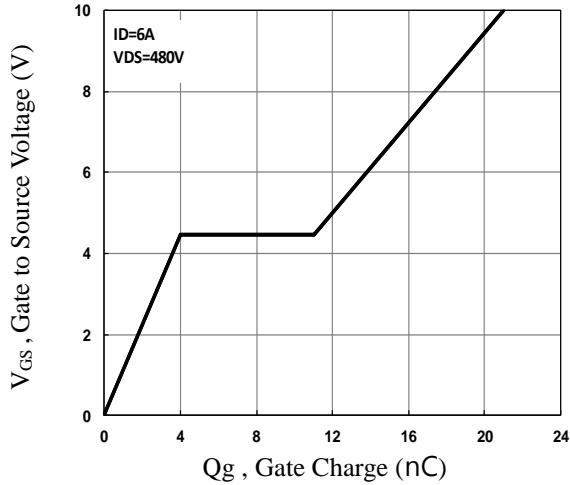
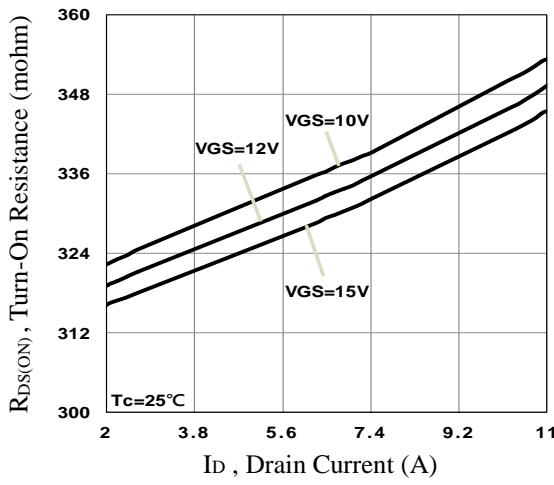
Q_g	Total Gate Charge ^{2,3}	$\text{V}_{\text{DS}}=480\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=6\text{A}$	---	21	32	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	4	6	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	7	11	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$\text{V}_{\text{DD}}=480\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{R}_G=25\Omega$ $\text{I}_D=6\text{A}$	---	18	27	ns
T_r	Rise Time ^{2,3}		---	41	62	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	110	165	
T_f	Fall Time ^{2,3}		---	39	59	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=100\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{F}=1\text{MHz}$	---	670	1005	pF
C_{oss}	Output Capacitance		---	30	45	
C_{rss}	Reverse Transfer Capacitance		---	1.5	5.3	
R_g	Gate resistance	$\text{V}_{\text{GS}}=0\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$, $\text{F}=1\text{MHz}$	---	20	---	Ω

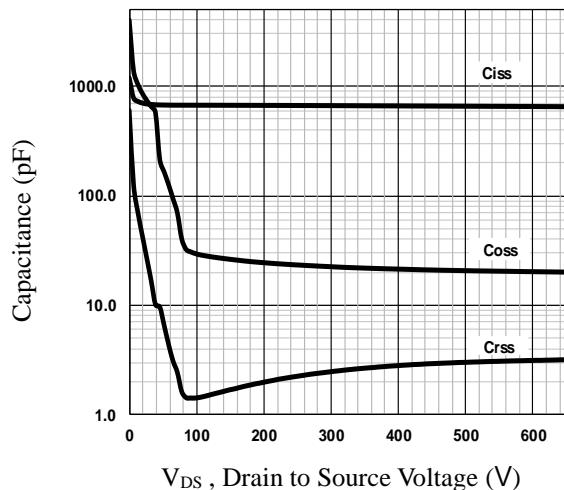
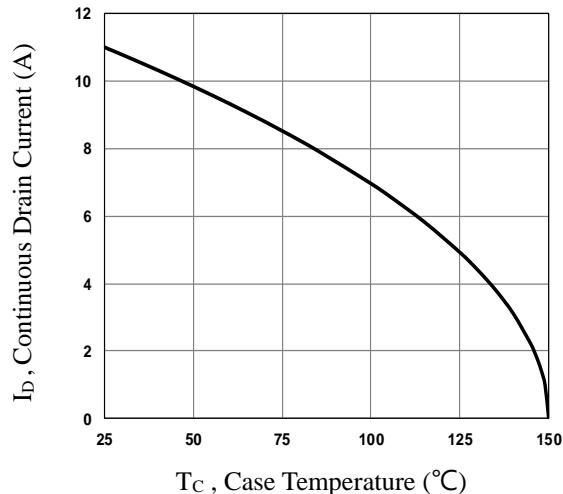
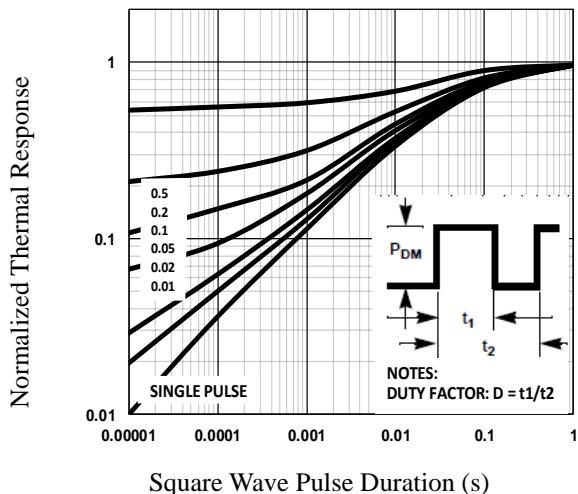
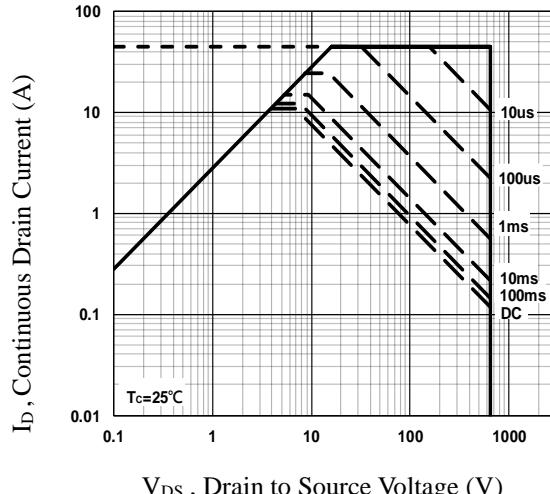
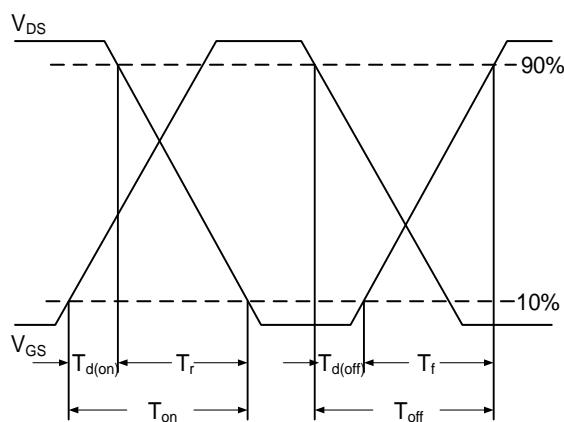
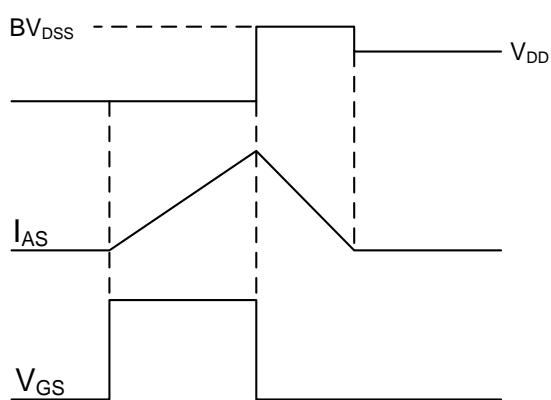
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	11	A
I_{SM}	Pulsed Source Current		---	---	22	A
V_{SD}	Diode Forward Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_s=6\text{A}$, $\text{T}_J=25^\circ\text{C}$	---	---	1.4	V
t_{rr}	Reverse Recovery Time ²	$\text{V}_R=400\text{V}$, $\text{I}_s=10\text{A}$, $d\text{I}/dt=100\text{A}/\mu\text{s}$, $\text{T}_J=25^\circ\text{C}$	---	320	---	ns
Q_{rr}	Reverse Recovery Charge ²		---	3.8	---	μC

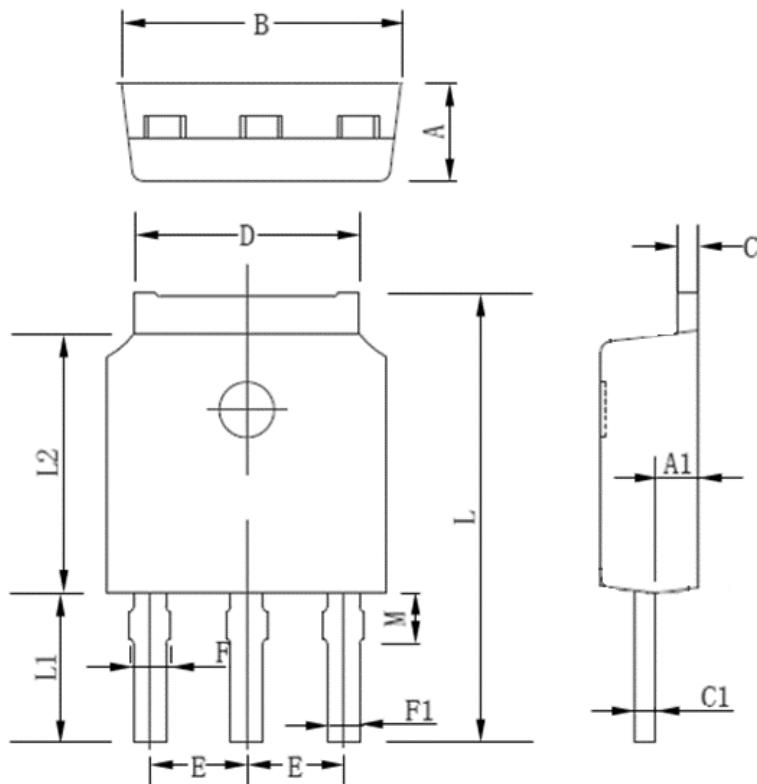
Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.


Fig.1 Typical Output Characteristics

Fig.2 Typical Output Characteristics

Fig.3 Normalized RDSON vs. T_J

Fig.4 Normalized V_{th} vs. T_J

Fig.5 Gate Charge Characteristics

Fig.6 Turn-On Resistance vs. I_D


Fig.7 Capacitance Characteristics

Fig.8 Continuous Drain Current vs. T_c

Fig.9 Normalized Transient Impedance

Fig.10 Maximum Safe Operation Area

Fig.11 Switching Time Waveform

Fig.12 EAS Waveform

TO251 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.500	2.100	0.098	0.083
A1	1.250	0.900	0.049	0.035
B	6.800	6.400	0.268	0.252
C	0.580	0.420	0.023	0.017
C1	0.580	0.420	0.023	0.017
D	5.530	5.000	0.218	0.197
E	2.400	2.000	0.094	0.079
F	1.050	0.600	0.041	0.024
F1	0.900	0.500	0.035	0.020
L	12.400	9.600	0.488	0.378
L1	5.300	3.800	0.209	0.150
L2	6.400	5.700	0.252	0.224