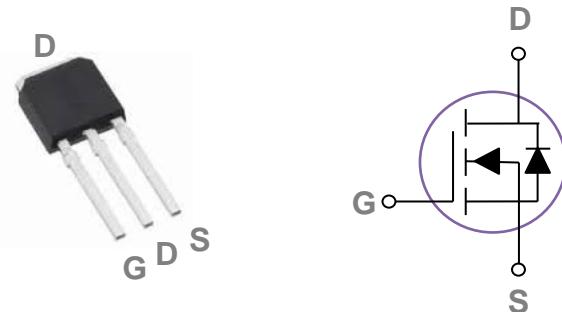


General Description

These N-Channel enhancement mode power field effect transistors are using super junction MOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO251 Pin Configuration



BVDSS	RDS(ON)	ID
650V	580mΩ	8A

Features

- 650V,8A, $RDS(ON) = 580m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- PFC Power Supply Stages
- Motor Control
- DC-DC Converters
- Adapter

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	650	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current – Continuous ($T_c=25^\circ C$)	8	A
	Drain Current – Continuous ($T_c=100^\circ C$)	5	A
I_{DM}	Drain Current – Pulsed ¹	32	A
EAS	Single Pulse Avalanche Energy ²	170	mJ
P_D	Power Dissipation ($T_c=25^\circ C$)	65	W
	Power Dissipation – Derate above 25°C	0.52	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.93	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=1\text{mA}$	650	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=650\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=520\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=100^\circ\text{C}$	---	---	10	uA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=2\text{A}$	---	530	580	mΩ
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\text{uA}$	2	3	4	V

Dynamic and switching Characteristics

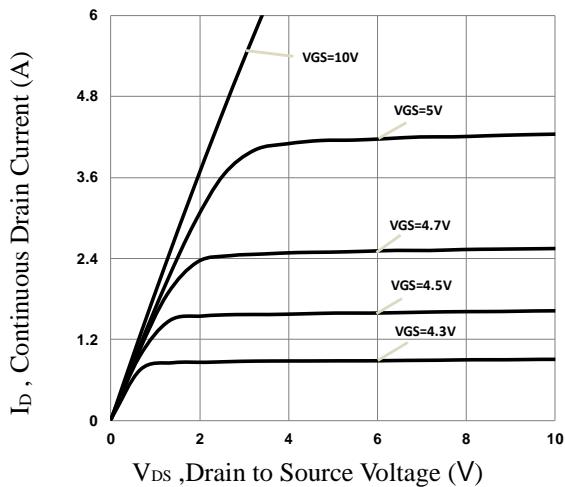
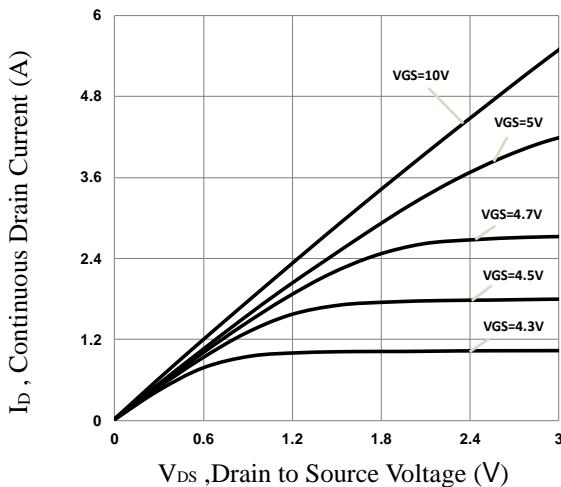
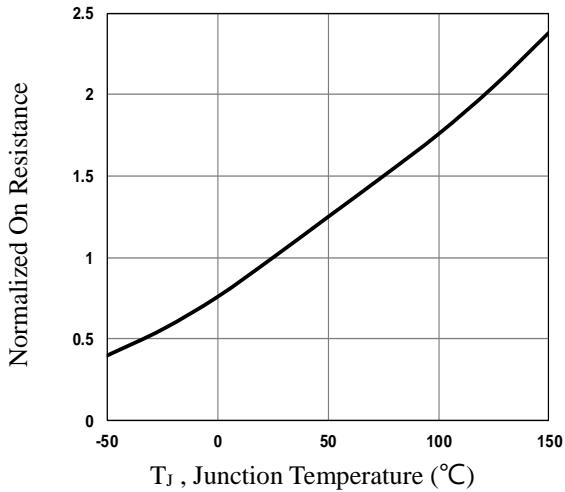
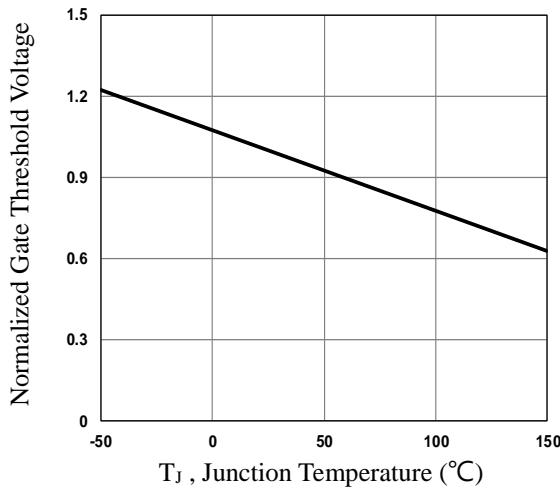
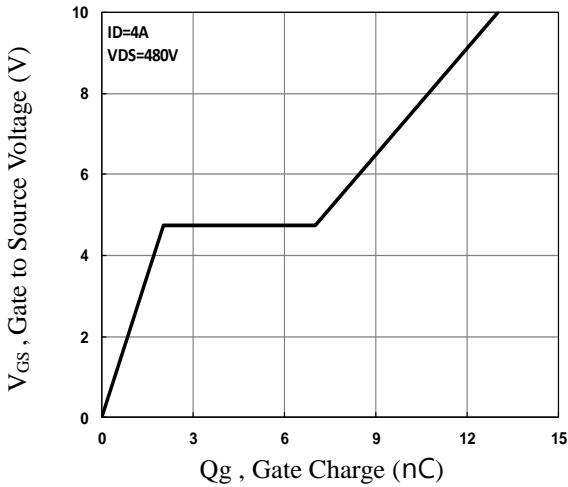
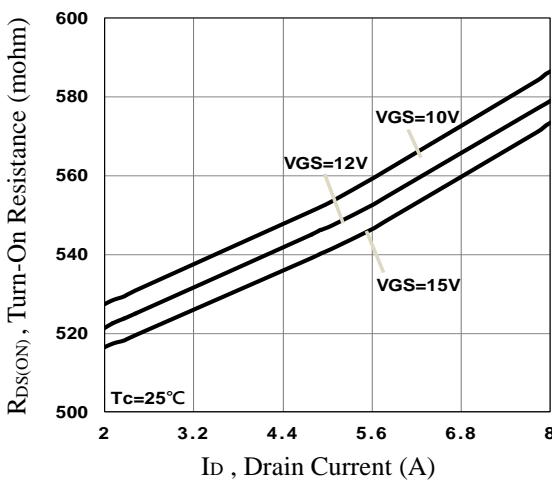
Q_g	Total Gate Charge ^{3, 4}	$V_{\text{DS}}=350\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=8\text{A}$	---	14	21	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	2	3	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	12	18	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{3, 4}	$V_{\text{DS}}=350\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_{\text{G}}=25\Omega$ $I_{\text{D}}=8\text{A}$	---	14	21	ns
T_r	Rise Time ^{3, 4}		---	34	51	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{3, 4}		---	48	72	
T_f	Fall Time ^{3, 4}		---	25	38	
C_{iss}	Input Capacitance	$V_{\text{DS}}=350\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	450	675	pF
C_{oss}	Output Capacitance		---	16	24	
C_{rss}	Reverse Transfer Capacitance		---	2	5	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	27	---	Ω

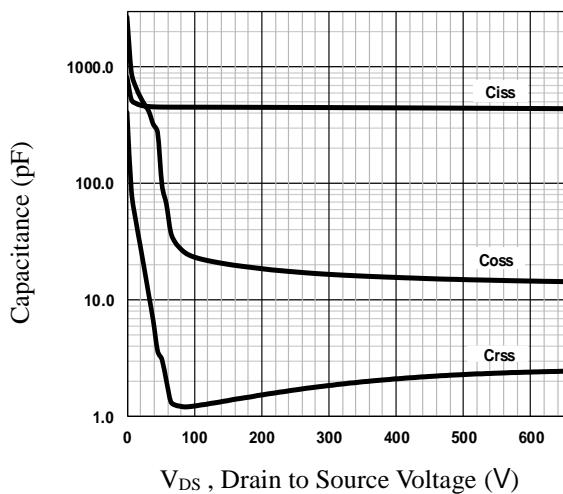
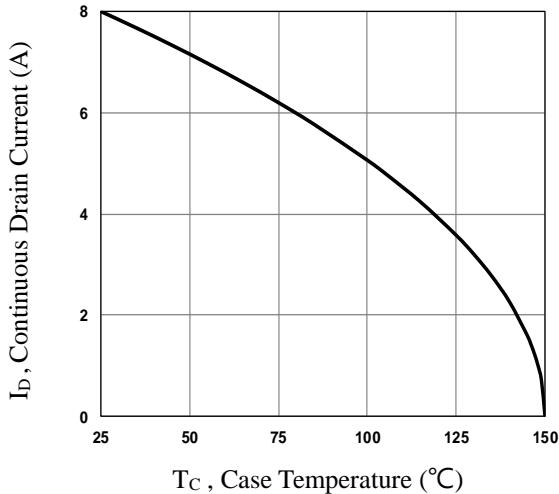
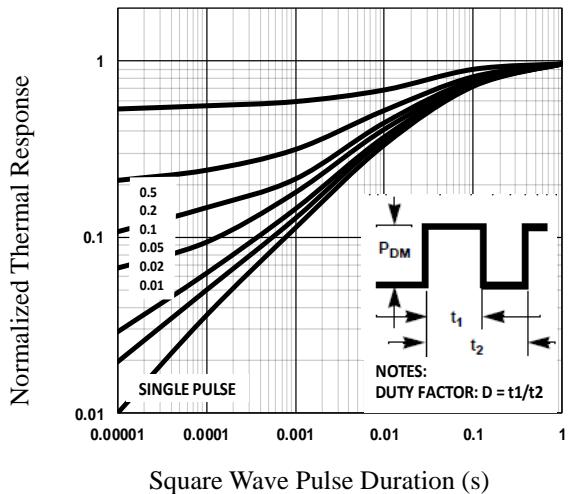
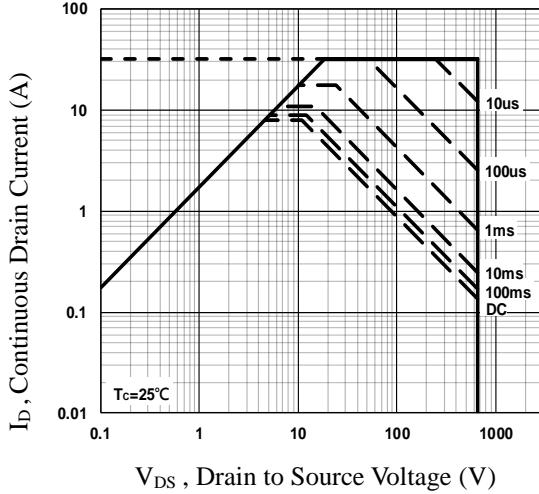
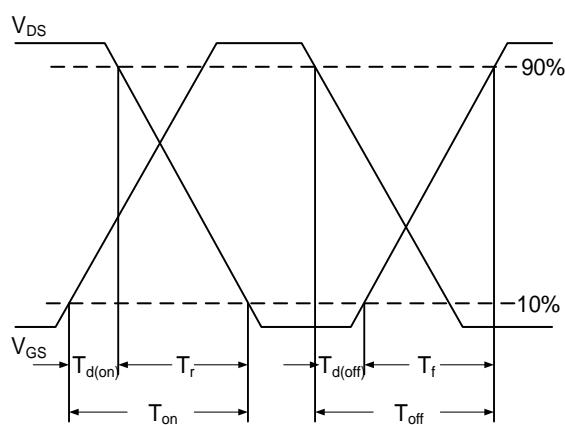
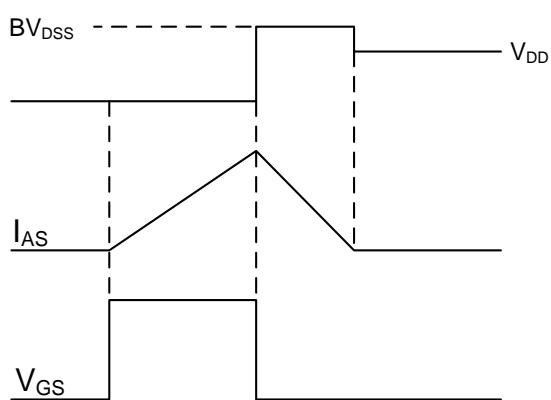
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	8	A
I_{SM}	Pulsed Source Current		---	---	16	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$V_R=400\text{V}$, $I_s=4\text{A}$ $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	230	---	ns
Q_{rr}	Reverse Recovery Charge		---	2	---	uC

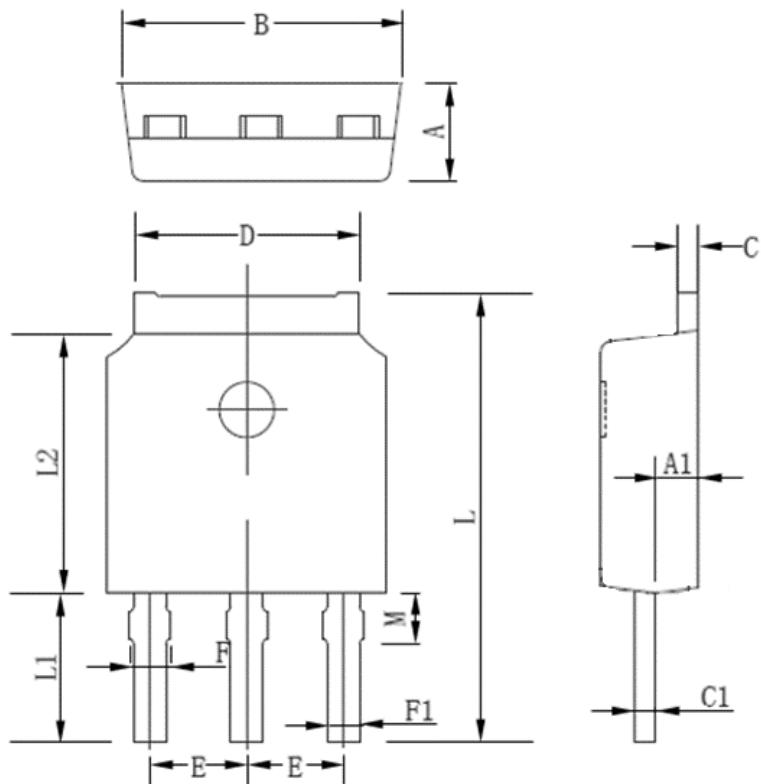
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Typical Output Characteristics

Fig.2 Typical Output Characteristics

Fig.3 Normalized RDSON vs. T_J

Fig.4 Normalized V_{th} vs. T_J

Fig.5 Gate Charge Characteristics

Fig.6 Turn-On Resistance vs. I_D


Fig.7 Capacitance Characteristics

Fig.8 Continuous Drain Current vs. T_c

Fig.9 Normalized Transient Impedance

Fig.10 Maximum Safe Operation Area

Fig.11 Switching Time Waveform

Fig.12 EAS Waveform

TO251 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.500	2.100	0.098	0.083
A1	1.250	0.900	0.049	0.035
B	6.800	6.400	0.268	0.252
C	0.580	0.420	0.023	0.017
C1	0.580	0.420	0.023	0.017
D	5.530	5.000	0.218	0.197
E	2.400	2.000	0.094	0.079
F	1.050	0.600	0.041	0.024
F1	0.900	0.500	0.035	0.020
L	12.400	9.600	0.488	0.378
L1	5.300	3.800	0.209	0.150
L2	6.400	5.700	0.252	0.224