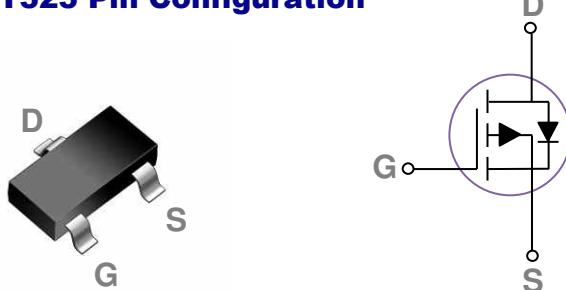


### General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT323 Pin Configuration



BVDSS	RDS(ON)	ID
-30V	90mΩ	-1.4A

### Features

- -30V, -1.4A, RDS(ON) = 90mΩ@VGS = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

### Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ\text{C}$ )	-1.4	A
	Drain Current – Continuous ( $T_A=70^\circ\text{C}$ )	-1.1	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	5.6	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	0.278	W
	Power Dissipation – Derate above 25°C	0.002	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{θJA}$	Thermal Resistance Junction to ambient	---	450	°C/W



30V P-Channel MOSFETs

PDU3915Z

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$ , $I_D=-1\text{mA}$	---	-0.02	---	$\text{V}/\text{ }^{\circ}\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-27\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{DS}=-24\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	-10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$ , $I_D=-1\text{A}$	---	75	90	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$ , $I_D=-0.8\text{A}$	---	110	140	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D = -250\mu\text{A}$	-1.2	-1.6	-2.2	V
			---	-2.8	---	$\text{mV}/\text{ }^{\circ}\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=-10\text{V}$ , $I_D=-1\text{A}$	---	3	---	S

**Dynamic and switching Characteristics**

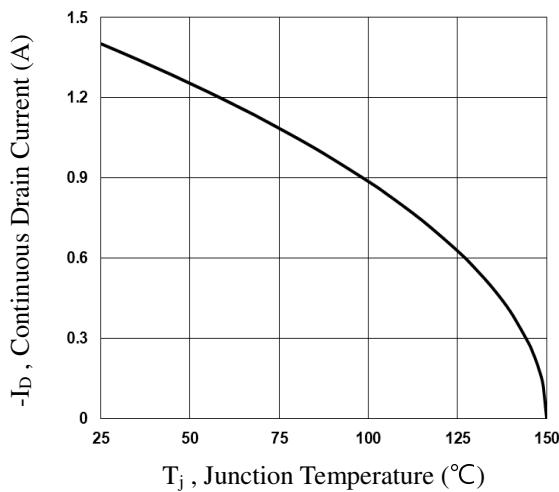
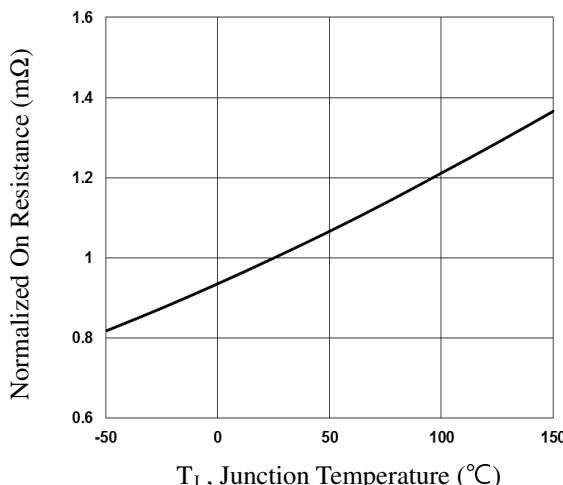
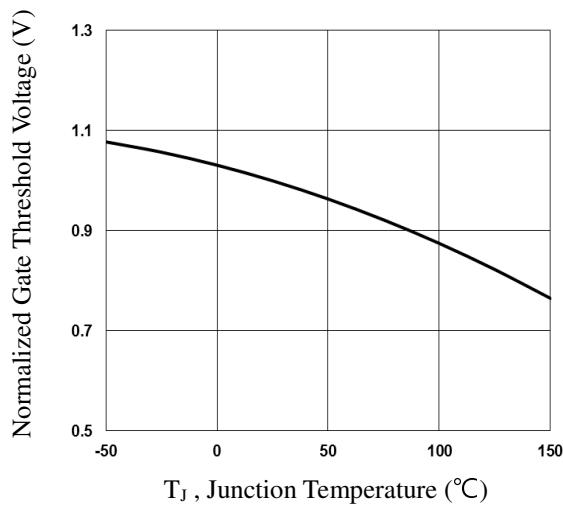
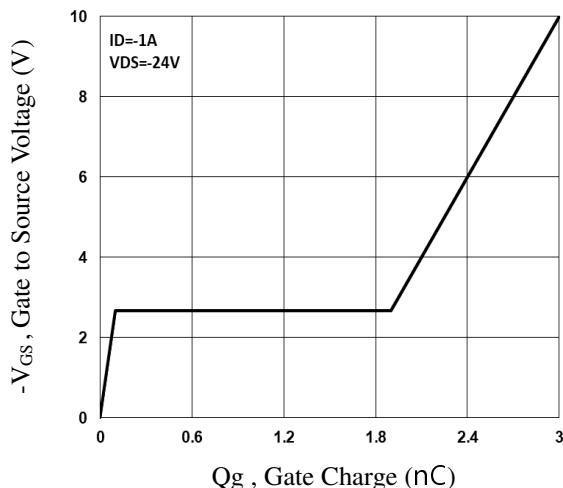
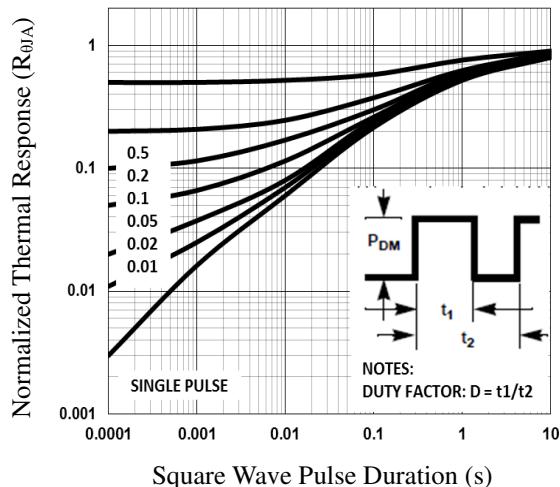
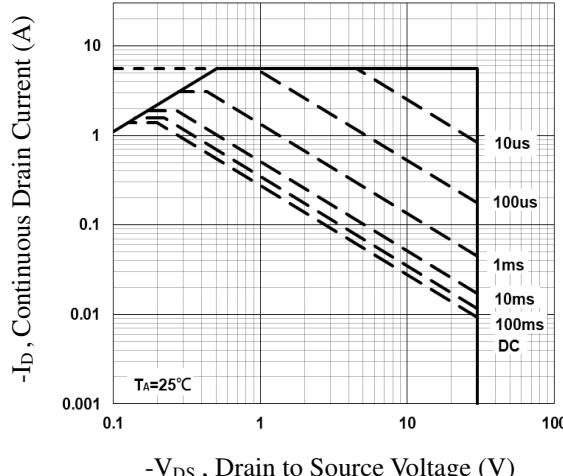
$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{DS}=-24\text{V}$ , $V_{GS}=-4.5\text{V}$ , $I_D=-1\text{A}$	---	2.5	5	nC
$Q_{gs}$	Gate-Source Charge <sup>2, 3</sup>		---	0.1	0.3	
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>		---	1.8	3.6	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{DD}=-15\text{V}$ , $V_{GS}=-10\text{V}$ , $R_G=6\Omega$ $I_D=-1\text{A}$	---	6.1	12	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	8.7	17	
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>		---	33.2	66	
$T_f$	Fall Time <sup>2, 3</sup>		---	3.7	7	
$C_{iss}$	Input Capacitance	$V_{DS}=-15\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	226	450	pF
$C_{oss}$	Output Capacitance		---	39	78	
$C_{rss}$	Reverse Transfer Capacitance		---	29	58	

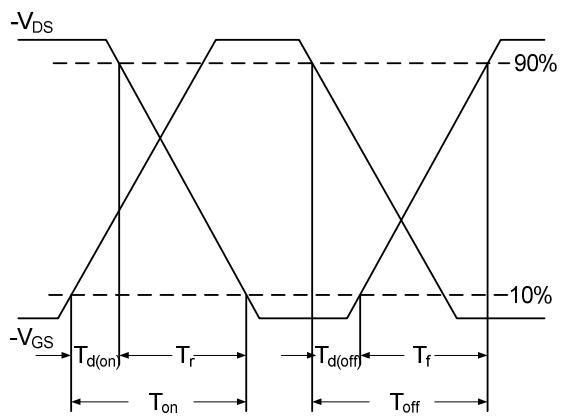
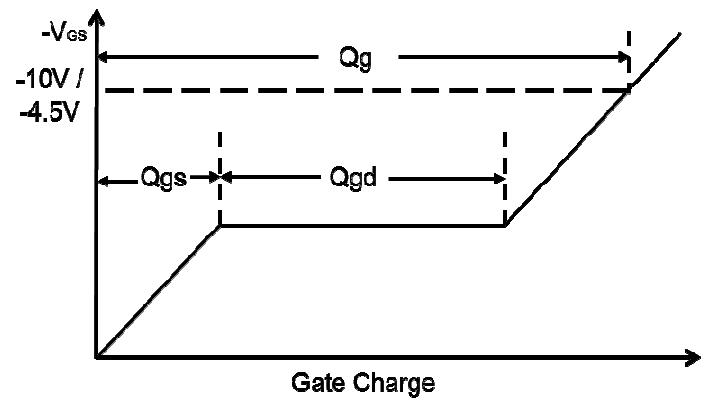
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-1.4	A
$I_{SM}$	Pulsed Source Current		---	---	-2.8	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	V

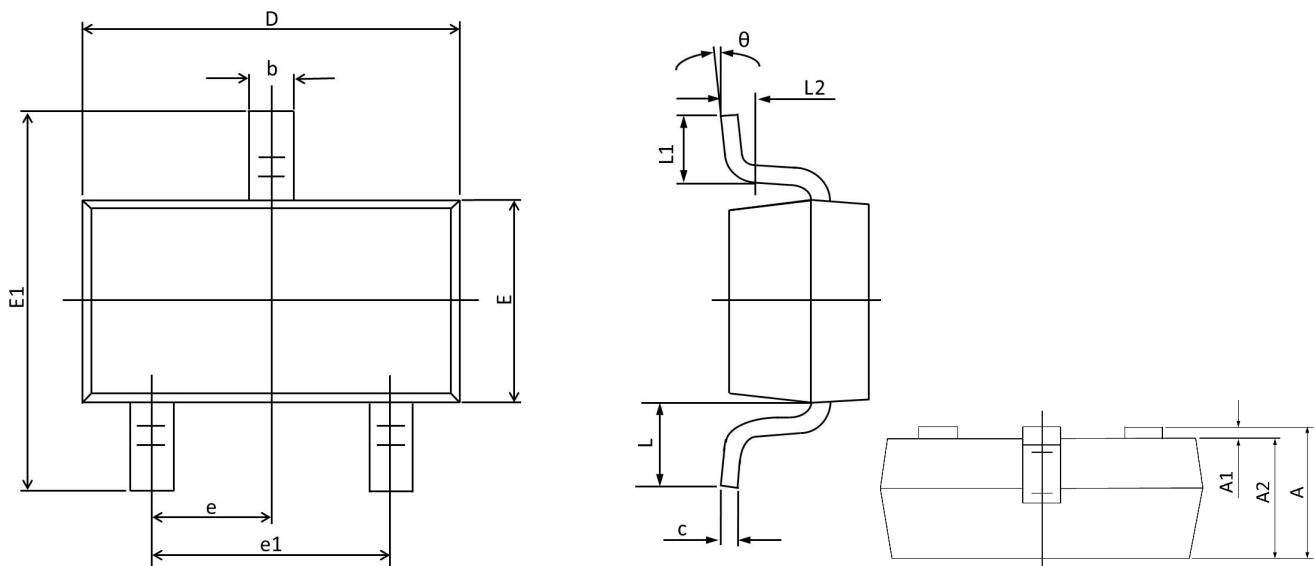
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. T<sub>j</sub>**

**Fig.2 Normalized RDSON vs. T<sub>j</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7** Switching Time Waveform

**Fig.8** Gate Charge Waveform

## SOT323 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.100	0.800	0.043	0.031
A1	0.100	0.000	0.004	0.000
A2	1.000	0.800	0.039	0.031
b	0.400	0.200	0.016	0.008
c	0.250	0.080	0.010	0.003
D	2.200	1.800	0.087	0.071
E	1.350	1.150	0.053	0.045
E1	2.450	1.800	0.096	0.071
e	0.65BSC		0.026BSC	
e1	1.400	1.200	0.055	0.047
L	0.525REF.		0.021REF.	
L1	0.460	0.150	0.018	0.006
L2	0.200	0.000	0.008	0.000
theta	8°	0°	8°	0°