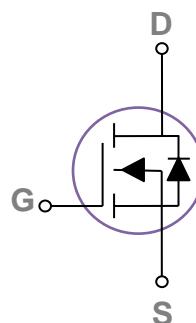
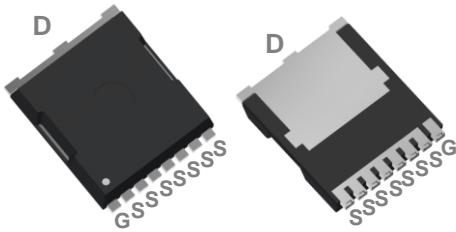


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TOLL Pin Configuration



BVDSS	RDSON	ID
150V	4.3mΩ	170A

Features

- 150V, 170A, RDS(ON) = 4.3mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	150	V
V _{Gs}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	170	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	109	A
I _{DM}	Drain Current – Pulsed ¹	680	A
EAS	Single Pulse Avalanche Energy ²	1800	mJ
I _{AS}	Single Pulse Avalanche Current ²	60	A
P _D	Power Dissipation ($T_c=25^\circ\text{C}$)	430	W
	Power Dissipation – Derate above 25°C	3.44	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	55	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	0.29	°C/W



150V N-Channel MOSFETs

PDT150N15BH**Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	150	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=120\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^\circ\text{C}$	---	---	1	μA
		$V_{DS}=120\text{V}$, $V_{GS}=0\text{V}$, $T_J=85\text{ }^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$, $I_D=30\text{A}$	---	3.6	4.3	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D = 250\mu\text{A}$	2.5	3.5	4.5	V
g_{fs}	Forward Transconductance	$V_{DS}=10\text{V}$, $I_D=3\text{A}$	---	15	---	S

Dynamic and switching Characteristics³

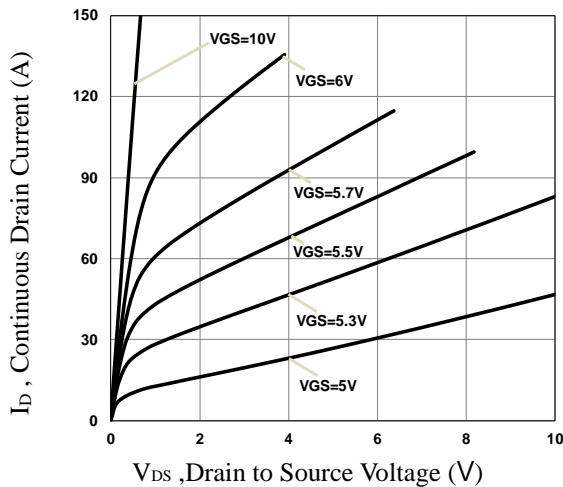
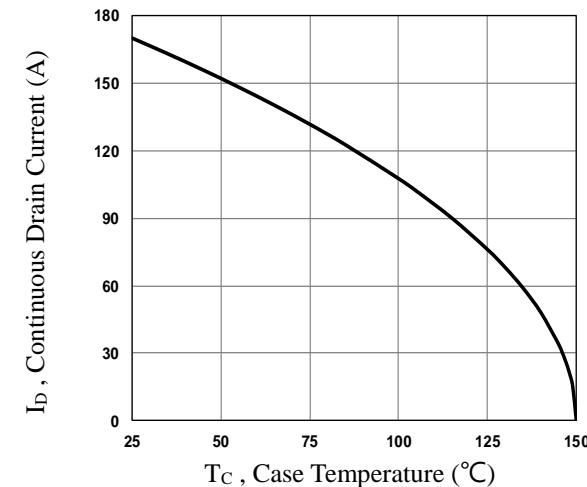
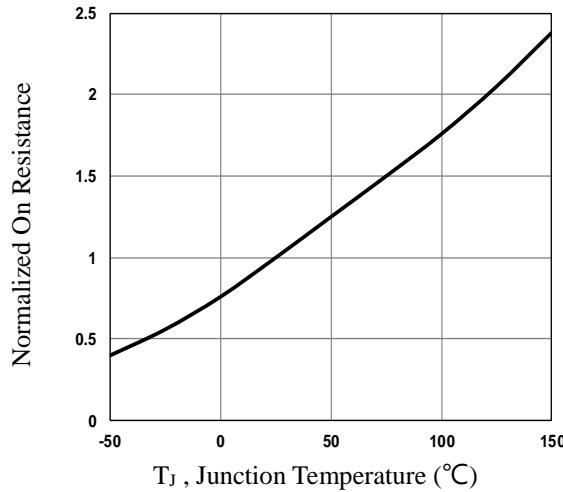
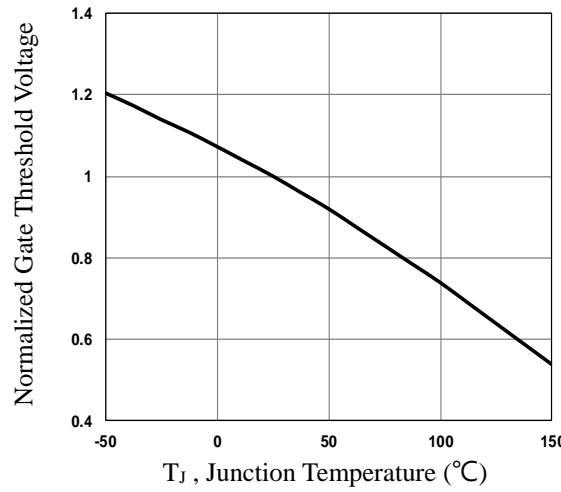
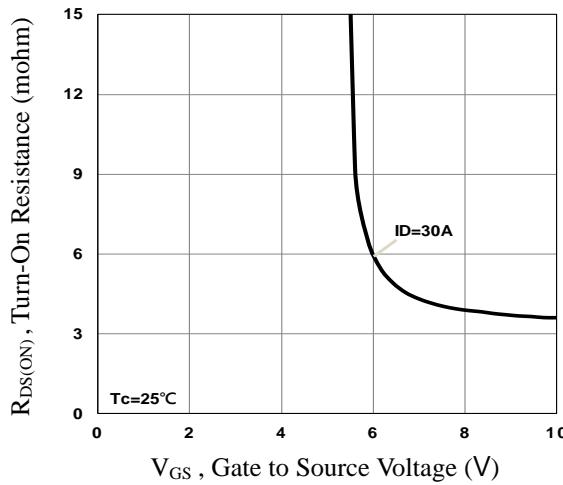
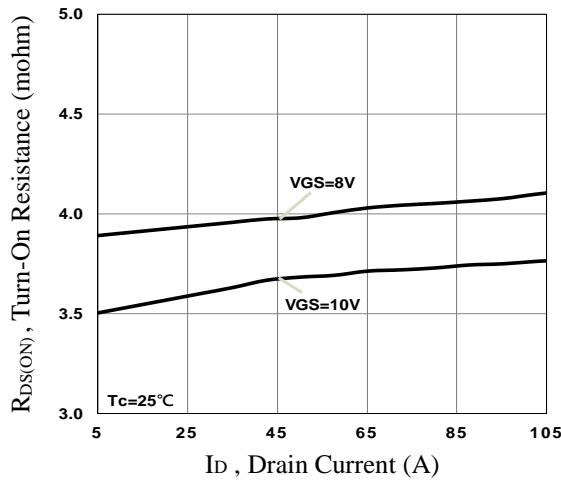
Q_g	Total Gate Charge	$V_{DS}=75\text{V}$, $V_{GS}=10\text{V}$, $I_D=80\text{A}$	---	120	180	nC
Q_{gs}	Gate-Source Charge		---	31	45	
Q_{gd}	Gate-Drain Charge		---	34	50	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=75\text{V}$, $V_{GS}=10\text{V}$, $R_G=6\Omega$ $I_D=80\text{A}$	---	30	45	ns
T_r	Rise Time		---	35	55	
$T_{d(off)}$	Turn-Off Delay Time		---	35	55	
T_f	Fall Time		---	45	70	
C_{iss}	Input Capacitance	$V_{DS}=75\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	7330	11000	pF
C_{oss}	Output Capacitance		---	680	1050	
C_{rss}	Reverse Transfer Capacitance		---	18	30	
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $F=1\text{MHz}$	---	2.7	---	Ω

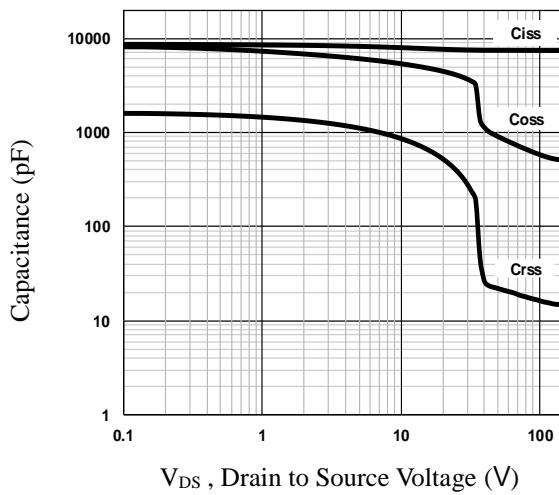
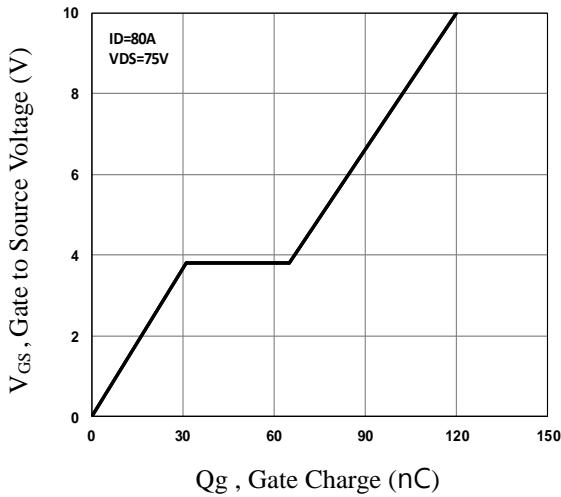
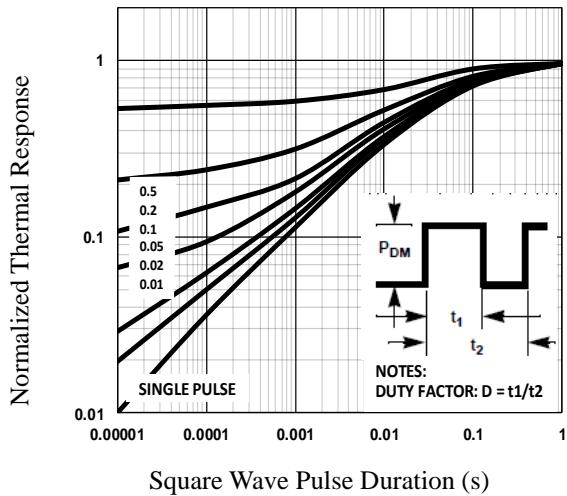
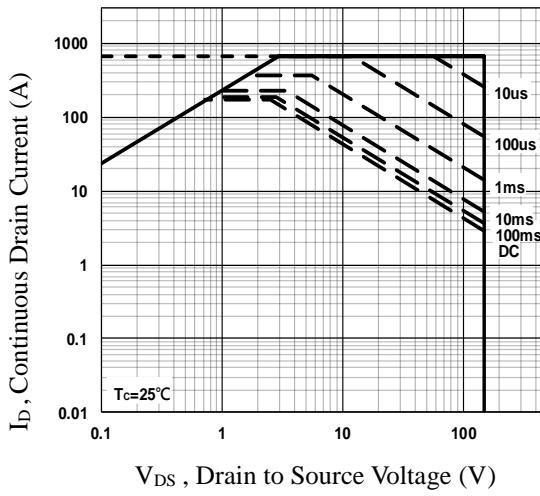
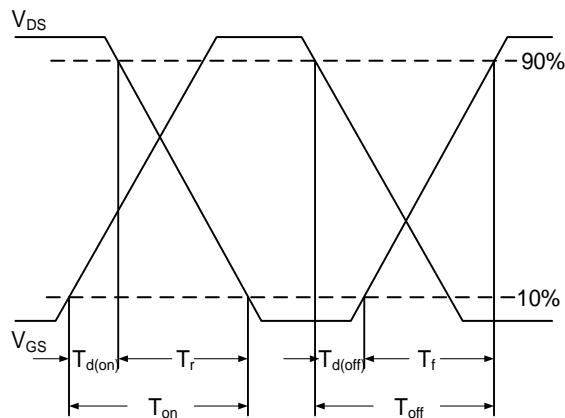
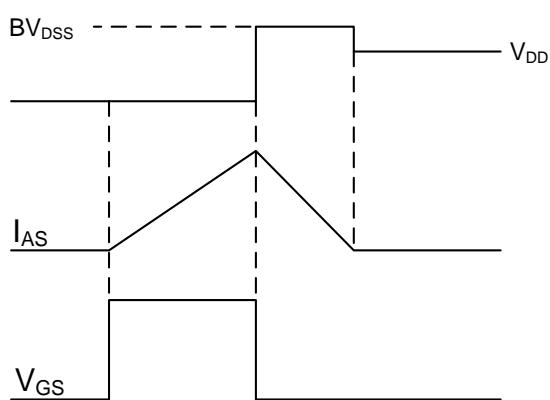
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	170	A
I_{SM}	Pulsed Source Current		---	---	340	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25\text{ }^\circ\text{C}$	---	---	1	V
t_{rr}	Reverse Recovery Time	$V_R=100\text{V}$, $I_s=20\text{A}$ $di/dt=100\text{A}/\mu\text{s}$, $T_J=25\text{ }^\circ\text{C}$	---	110	---	ns
Q_{rr}	Reverse Recovery Charge		---	520	---	nC

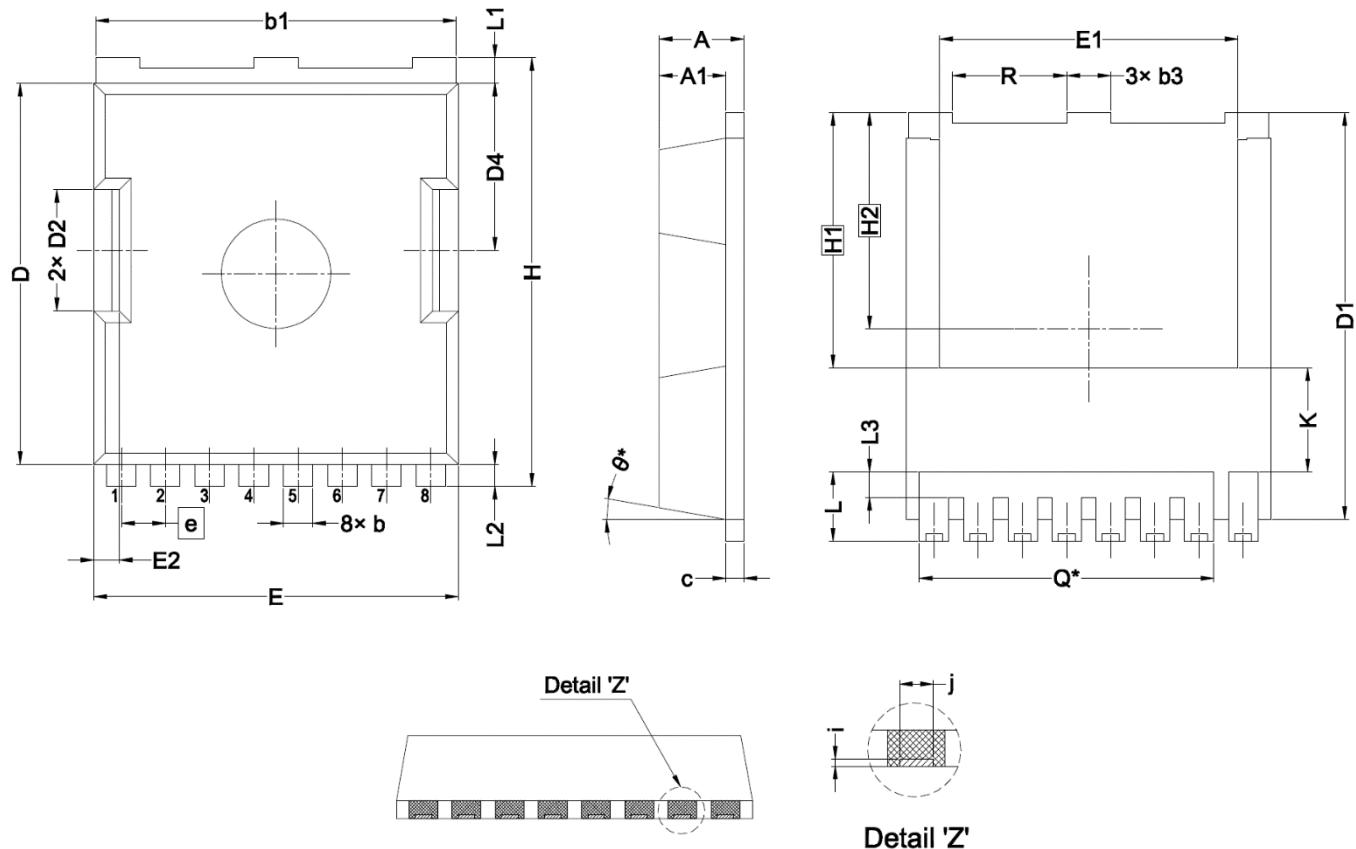
Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{DD}=50\text{V}$, $V_{GS}=10\text{V}$, $L=1\text{mH}$, $I_{AS}=60\text{A}$, $R_G=25\Omega$, Starting $T_J=25\text{ }^\circ\text{C}$.
- Essentially independent of operating temperature.


Fig.1 Typical Output Characteristics

Fig.2 Continuous Drain Current vs. T_c

Fig.3 Normalized $R_{DS(on)}$ vs. T_j

Fig.4 Normalized V_{th} vs. T_j

Fig.5 Turn-On Resistance vs. V_{GS}

Fig.6 Turn-On Resistance vs. I_D

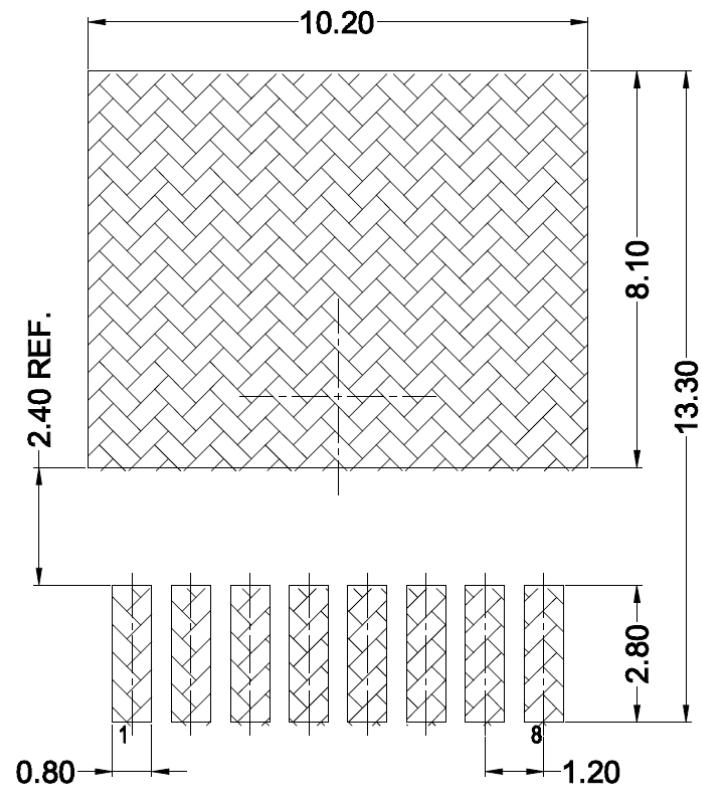

Fig.7 Capacitance Characteristics

Fig.8 Gate Charge Characteristics

Fig.9 Normalized Transient Impedance

Fig.10 Maximum Safe Operation Area

Fig.11 Switching Time Waveform

Fig.12 EAS Waveform

TOLL PACKAGE INFORMATION



SYMBOL	mm			SYMBOL	mm		
	MIN	NOM	MAX		MIN	NOM	MAX
A	2.20	2.30	2.40	H	11.58	11.68	11.78
A1	1.70	1.80	1.90	H1	6.95 BSC		
b	0.70	0.80	0.90	H2	5.89 BSC		
b1	9.70	9.80	9.90	i	0.10 REF		
b3	1.10	1.20	1.30	j	0.46 REF		
c	0.40	0.50	0.60	K	2.80 REF		
D	10.28	10.38	10.48	L	1.40	1.90	2.10
D1	10.98	11.08	11.18	L1	0.60	0.70	0.80
D2	3.20	3.30	3.40	L2	0.50	0.60	0.70
D4	4.45	4.55	4.65	L3	0.30	0.70	0.80
E	9.80	9.90	10.00	N	8		
E1	8.00	8.10	8.20	Q	8.00 REF		
E2	0.60	0.70	0.80	R	3.00	3.10	3.20
e	1.20 BSC			θ	10° REF		

TOLL RECOMMENDED LAND PATTERN



unit : mm