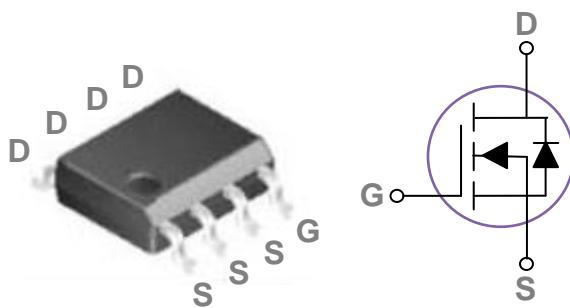


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOP8 Pin Configuration



BVDSS	RDS(ON)	ID
60V	21mΩ	6A

### Features

- 60V, 6A, RDS(ON) = 21mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	6	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	3.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	24	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	42	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	29	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	1.47	W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.0117	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	85	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	23	$^\circ\text{C/W}$

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.07	---	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=3\text{A}$	---	17	21	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=2\text{A}$	---	20	24	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = 250\mu\text{A}$	1.2	1.8	2.5	V
			---	5	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=10\text{A}$	---	9	---	S

**Dynamic and switching Characteristics**

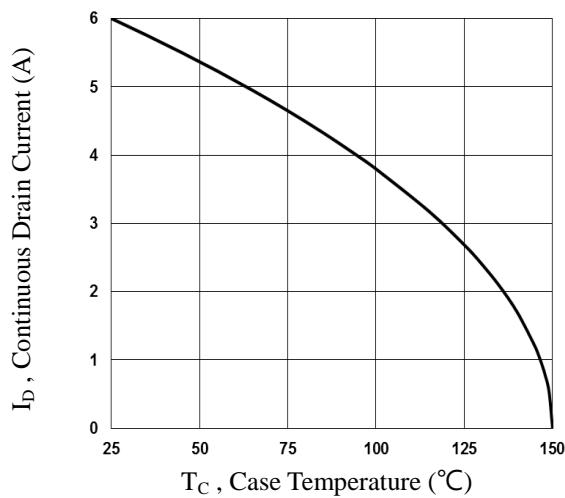
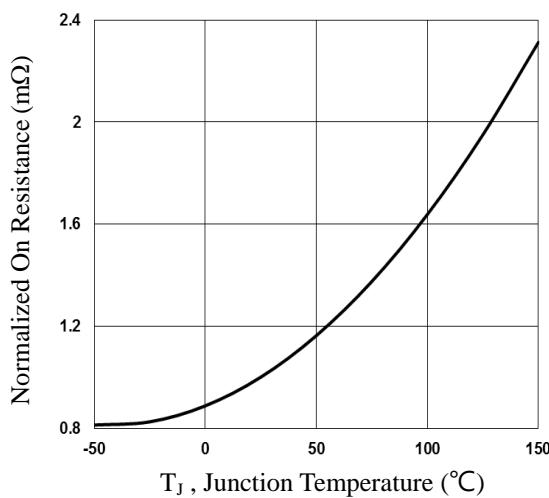
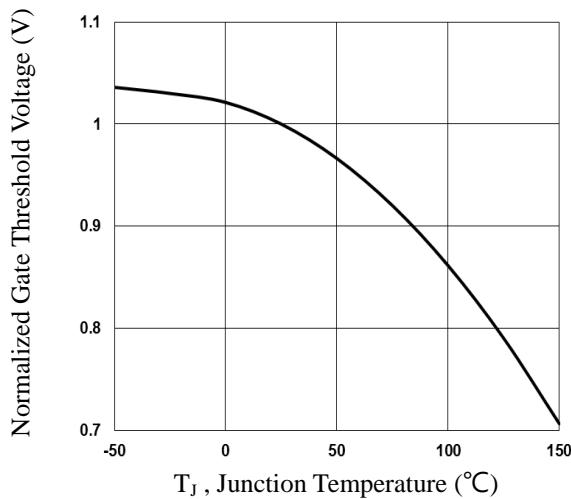
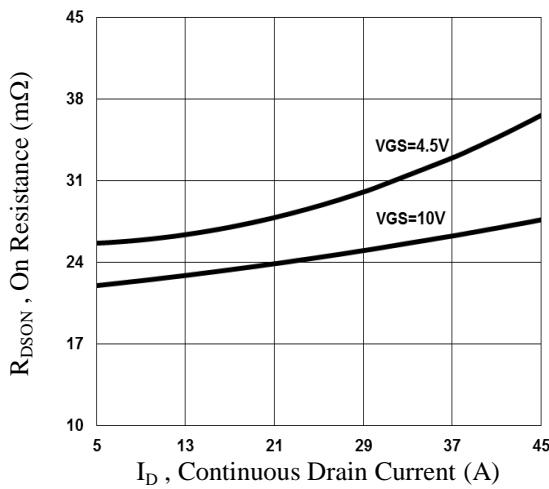
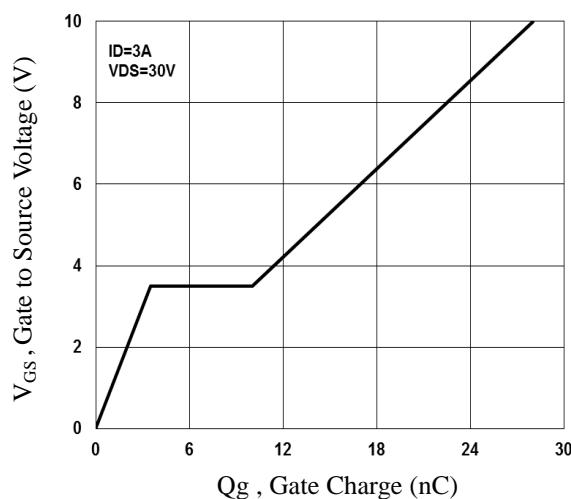
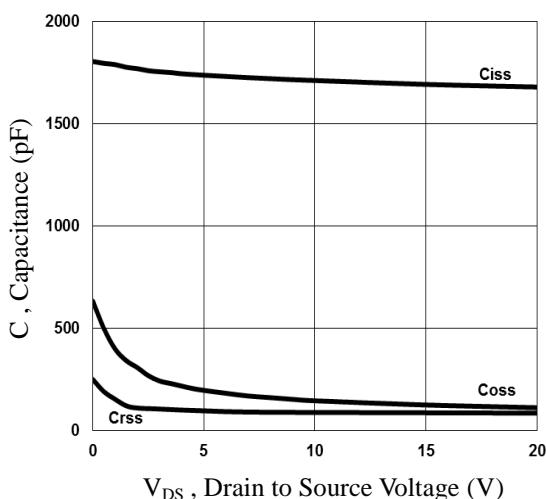
$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=3\text{A}$	---	28	42	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2,3</sup>		---	3.5	7	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2,3</sup>		---	6.5	10	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>2,3</sup>	$V_{\text{DD}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=6\Omega$	---	7.2	14	ns
$T_r$	Rise Time <sup>2,3</sup>		---	38	72	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>2,3</sup>		---	34	65	
$T_f$	Fall Time <sup>2,3</sup>		---	8.2	16	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=20\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	1680	2440	pF
$C_{\text{oss}}$	Output Capacitance		---	115	170	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	85	125	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	2.2	4.4	$\Omega$

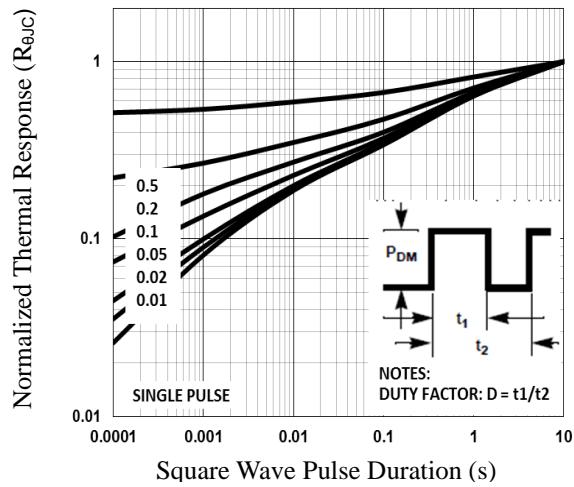
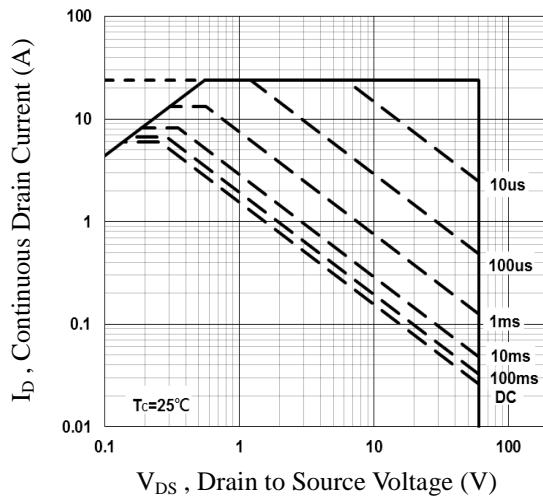
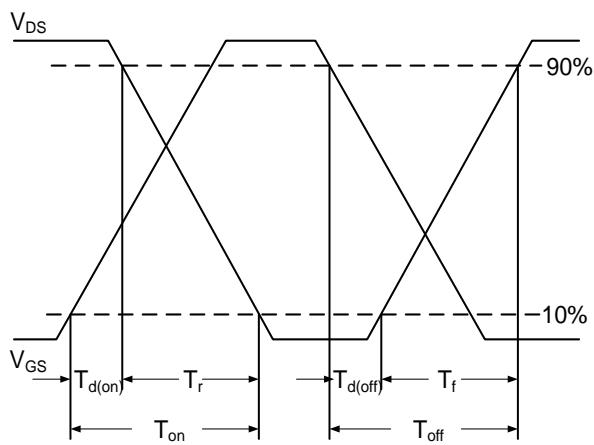
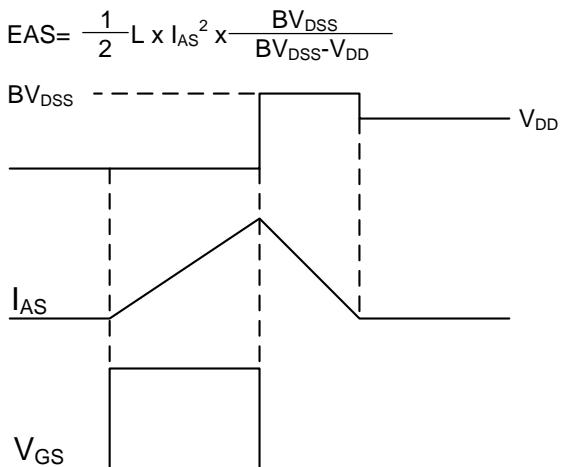
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	6	A
			---	---	12	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$t_{\text{rr}}$	Reverse Recovery Time <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	---	19.6	---	ns
		$T_J=25^\circ\text{C}$	---	14.2	---	nC

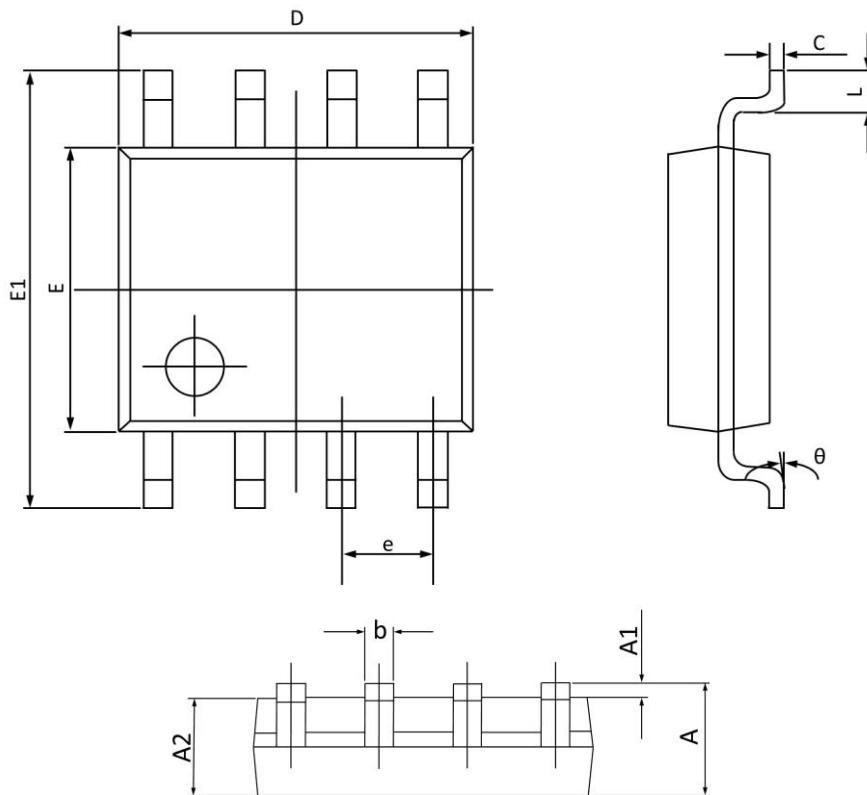
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{\text{DD}}=25\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{\text{AS}}=29\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_C$** 

**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.4  $R_{DS(on)}$  vs. Continuous Drain Current**

**Fig.5 Gate Charge Waveform**

**Fig.6 Capacitance Characteristics**


**Fig.7 Normalized Transient Impedance**

**Fig.8 Maximum Safe Operation Area**

**Fig.9 Switching Time Waveform**

**Fig.10 EAS Waveform**

## SOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.750	1.350	0.069	0.053
A1	0.250	0.100	0.010	0.004
A2	1.500	1.300	0.059	0.051
b	0.490	0.350	0.019	0.014
C	0.260	0.190	0.010	0.007
D	5.100	4.700	0.201	0.185
E	4.100	3.700	0.161	0.146
E1	6.200	5.800	0.244	0.228
e	1.27BSC		0.05BSC	
L	0.900	0.400	0.035	0.016
θ	8°	0°	8°	0°