

### General Description

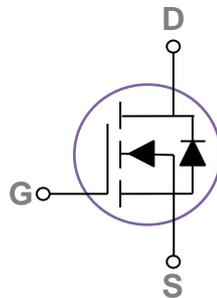
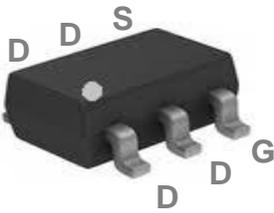
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	15mΩ	7.5A

### Features

- 30V, 7.5A,  $R_{DS(ON)} = 15m\Omega @ V_{GS} = 10V$
- Improved  $dv/dt$  capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### SOT23-6 Pin Configuration



### Applications

- MB / VGA / Vcore
- Load Switch
- Hand-Held Instrument

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ\text{C}$ )	7.5	A
	Drain Current – Continuous ( $T_A=70^\circ\text{C}$ )	6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	30	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	1.56	W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.012	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	$^\circ\text{C}/\text{W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	μA
		V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	10	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>3</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =2.5A	---	12	15	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A	---	15	20	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	1.2	1.6	2.5	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =1A	---	4	---	S

**Dynamic and switching Characteristics**

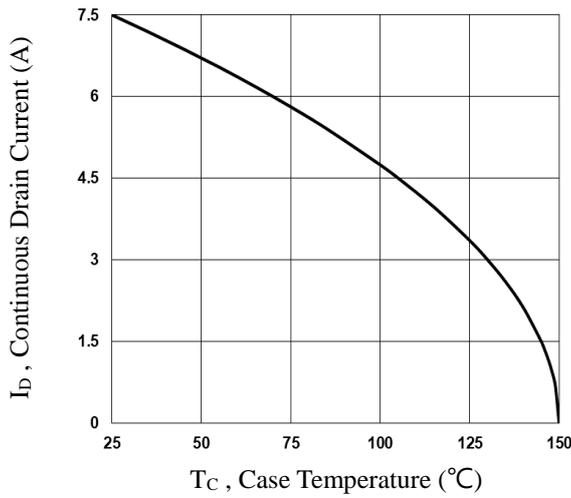
Q <sub>g</sub>	Total Gate Charge <sup>3, 4</sup>	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =4A	---	10.7	16	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3, 4</sup>		---	1.7	2.6	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3, 4</sup>		---	2.5	5	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3, 4</sup>	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω I <sub>D</sub> =4A	---	3.8	6	ns
T <sub>r</sub>	Rise Time <sup>3, 4</sup>		---	10	15	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>3, 4</sup>		---	22	33	
T <sub>f</sub>	Fall Time <sup>3, 4</sup>		---	6.6	10	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, F=1MHz	---	670	1005	pF
C <sub>oss</sub>	Output Capacitance		---	115	173	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	85	128	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	---	2.8	---	Ω

**Drain-Source Diode Characteristics and Maximum Ratings**

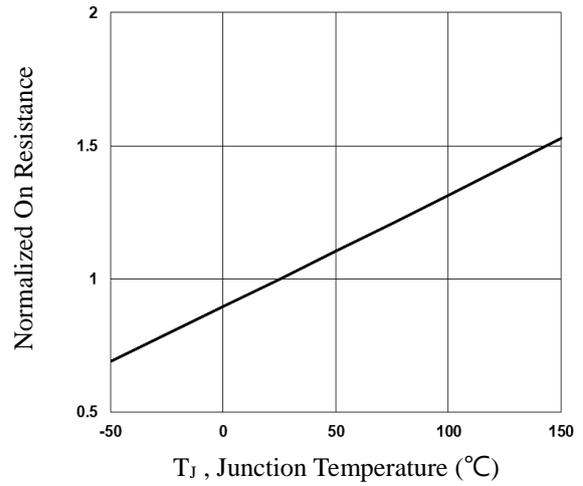
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	7.5	A
I <sub>SM</sub>	Pulsed Source Current <sup>3</sup>		---	---	15	A
V <sub>SD</sub>	Diode Forward Voltage <sup>3</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>R</sub> =30V, I <sub>S</sub> =4A	---	120	---	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs, T <sub>J</sub> =25°C	---	175	---	nC

Note :

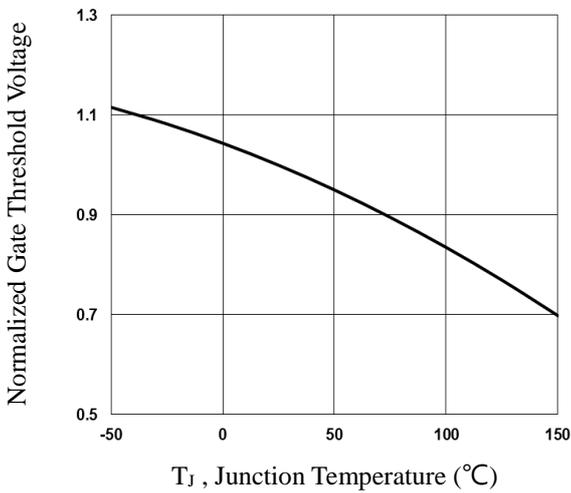
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.



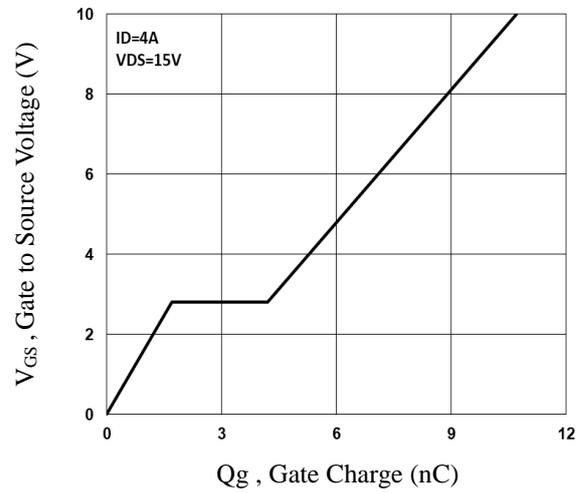
**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**



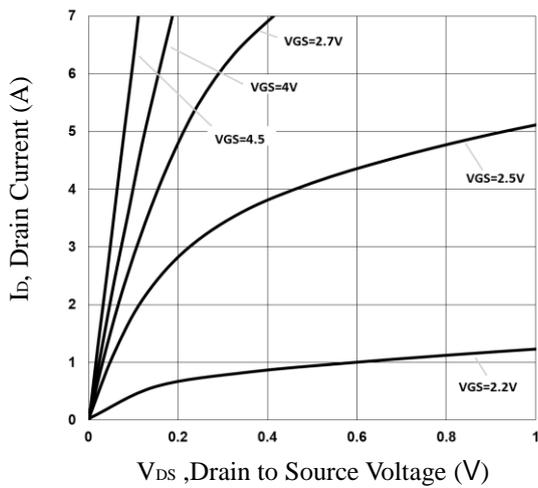
**Fig.2 Normalized R<sub>DSon</sub> vs. T<sub>j</sub>**



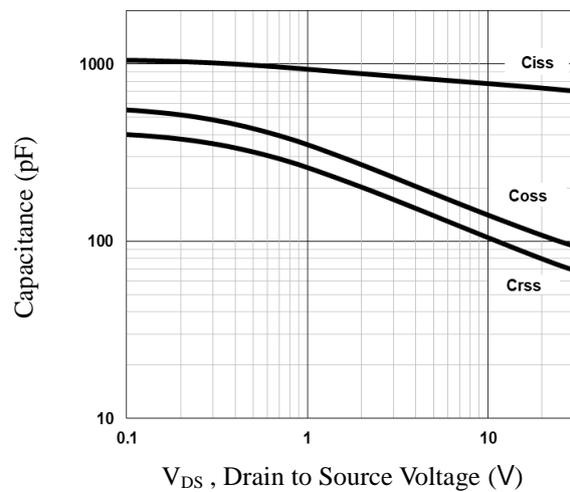
**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**



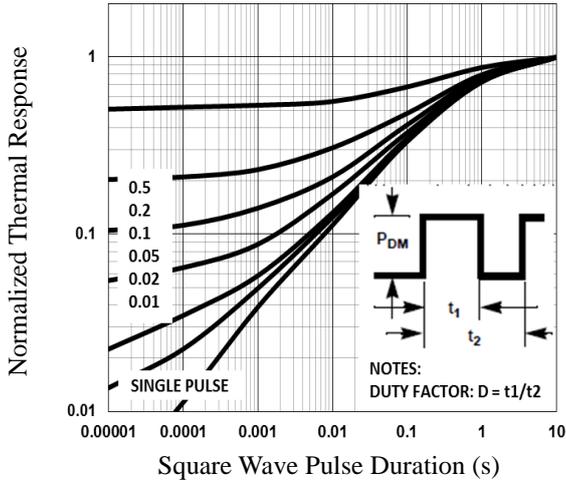
**Fig.4 Gate Charge Waveform**



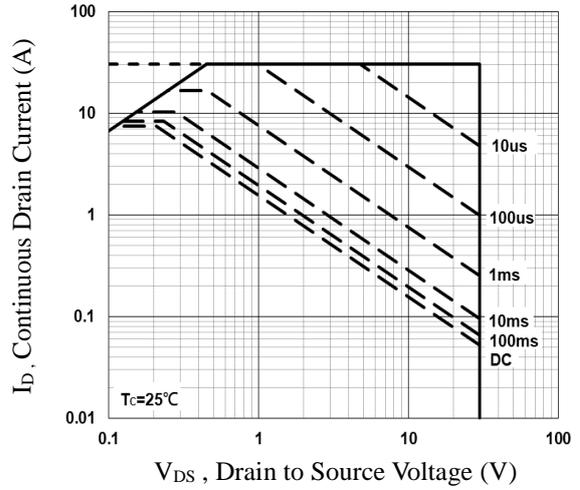
**Fig.5 Typical Output Characteristics**



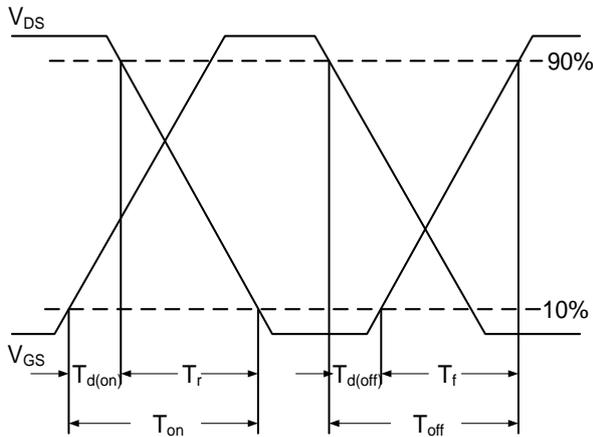
**Fig.6 Capacitance Characteristics**



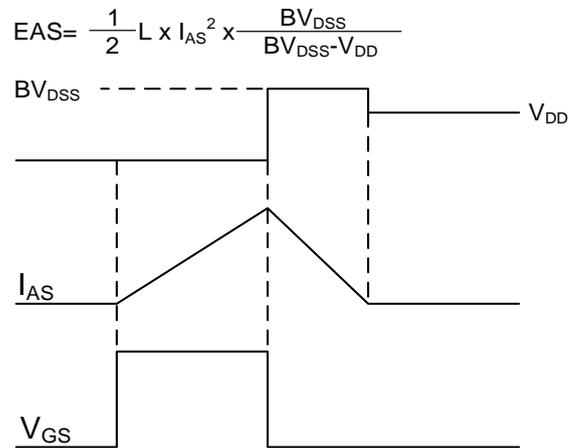
**Fig.7 Normalized Transient Response**



**Fig.8 Maximum Safe Operation Area**

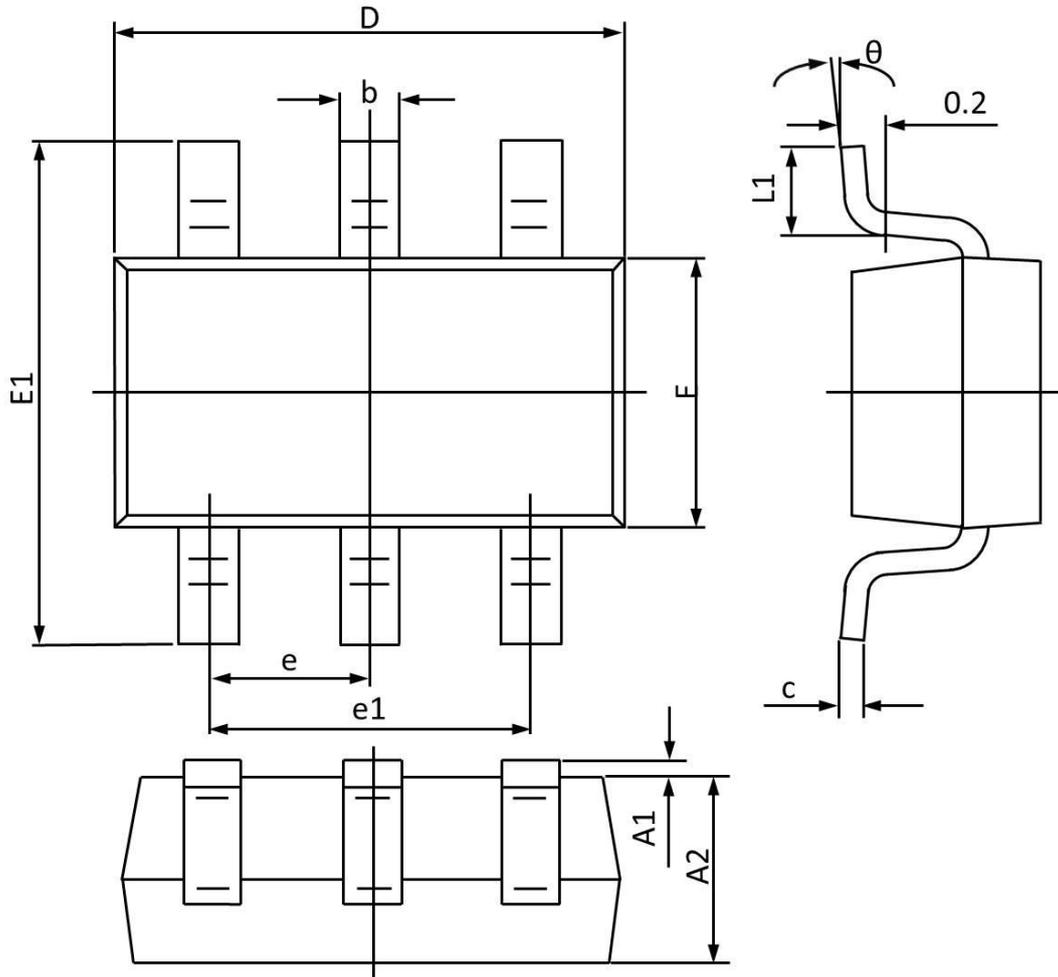


**Fig.9 Switching Time Waveform**



**Fig.10 EAS Waveform**

## SOT23-6 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A1	---	0.150	---	0.006
A2	0.900	1.300	0.035	0.051
b	0.300	0.500	0.012	0.019
c	0.100	0.200	0.004	0.008
D	2.800	3.050	0.110	0.120
E1	2.600	3.000	0.103	0.118
F	1.500	1.800	0.059	0.071
e	0.950 TYP		0.037 TYP	
e1	1.900 TYP		0.075 TYP	
L1	0.250	0.600	0.010	0.024
θ	0°	8°	0°	8°