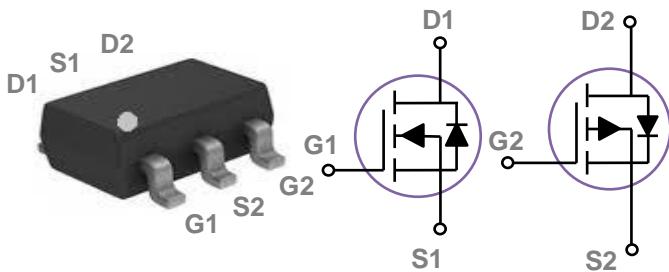


General Description

These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-6 Dual Pin Configuration



BVDSS	RDSON	ID
30V	30mΩ	4A
-30V	65mΩ	-3A

Features

- Fast switching
- Green Device Available
- Suit for 4.5V Gate Drive Applications

Applications

- DC Fan
- Motor Drive Applications
- Networking
- Half / Full Bridge Topology

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating		Units
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
I_D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	4	-3	A
	Drain Current – Continuous ($T_A=100^\circ\text{C}$)	2.5	-1.8	A
I_{DM}	Drain Current – Pulsed ¹	16	-12	A
P_D	Power Dissipation ($T_A=25^\circ\text{C}$)	2		W
	Power Dissipation – Derate above 25°C	0.016		W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150		$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150		$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62.5	$^\circ\text{C}/\text{W}$

N-CH Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^\circ\text{C}$	---	---	1	μA
		$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$, $I_D=4\text{A}$	---	22	30	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=2\text{A}$	---	28.5	37	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1.2	1.6	2.5	V
			---	-4	---	$\text{mV}/\text{ }^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=10\text{V}$, $I_D=3\text{A}$	---	6.5	---	S

Dynamic and switching Characteristics

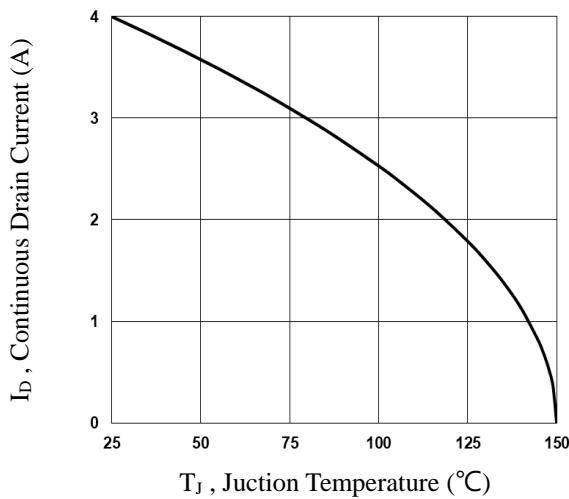
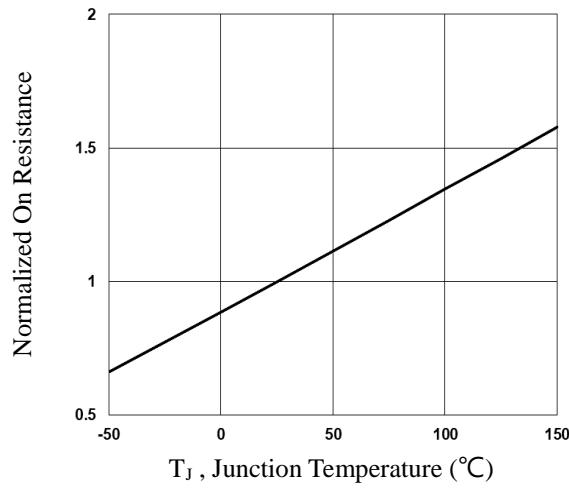
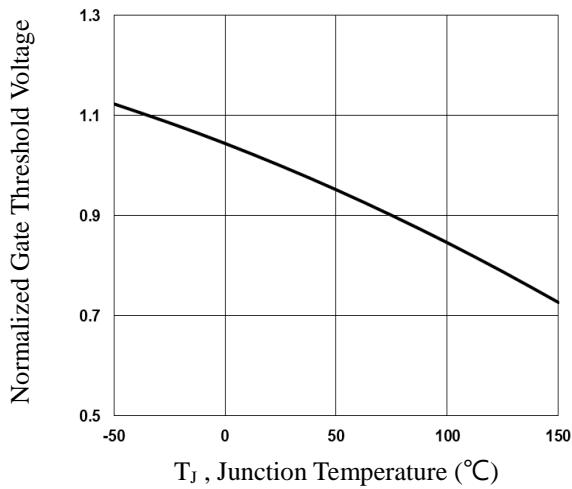
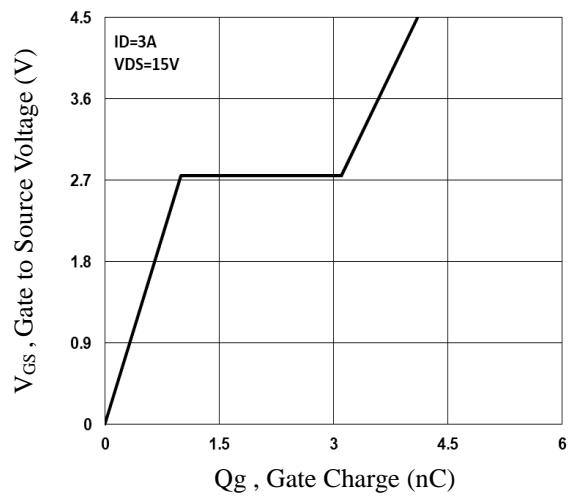
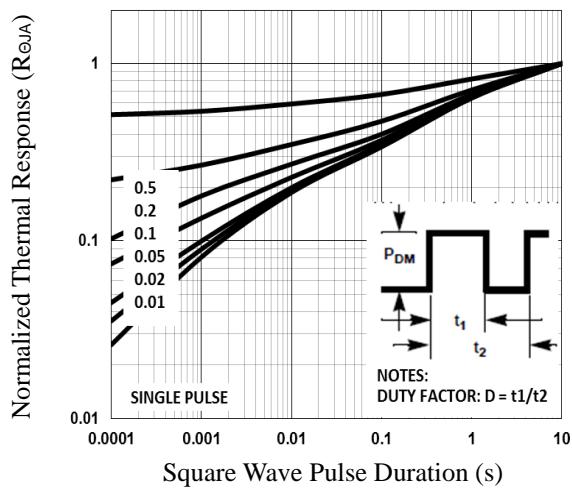
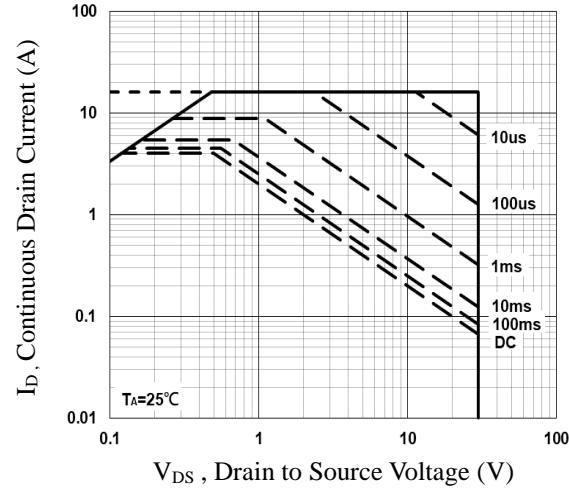
Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=15\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=3\text{A}$	---	4.1	8	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	1	2	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	2.1	4	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=15\text{V}$, $V_{GS}=10\text{V}$, $R_G=6\Omega$ $I_D=1\text{A}$	---	2.8	5	ns
T_r	Rise Time ^{3, 4}		---	7.2	14	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	15.8	30	
T_f	Fall Time ^{3, 4}		---	4.6	9	
C_{iss}	Input Capacitance	$V_{DS}=25\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	345	500	pF
C_{oss}	Output Capacitance		---	55	80	
C_{rss}	Reverse Transfer Capacitance		---	32	45	
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $F=1\text{MHz}$	---	3.2	6.4	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	4	A
			---	---	8	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25\text{ }^\circ\text{C}$	---	---	1	V

Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_J

Fig.2 Normalized RDSON vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Response

Fig.6 Maximum Safe Operation Area

P-CH Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=-1\text{mA}$	---	-0.03	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-3\text{A}$	---	54	65	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-2\text{A}$	---	74	90	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1.2	-1.6	-2.2	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	4	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_{\text{D}}=-3\text{A}$	---	3.7	---	S

Dynamic and switching Characteristics

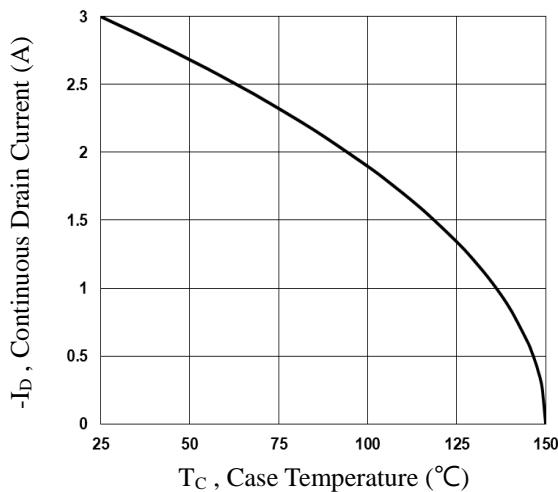
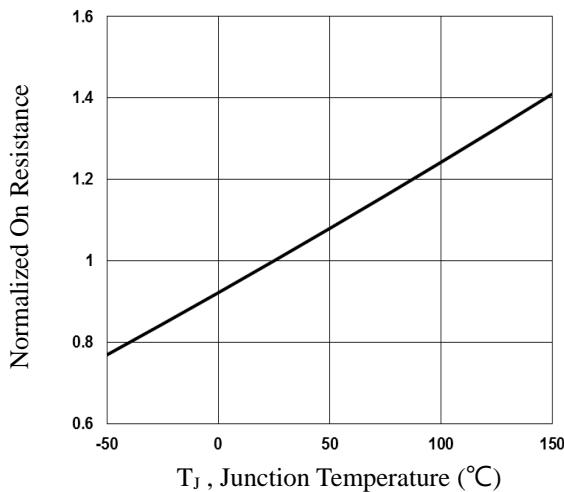
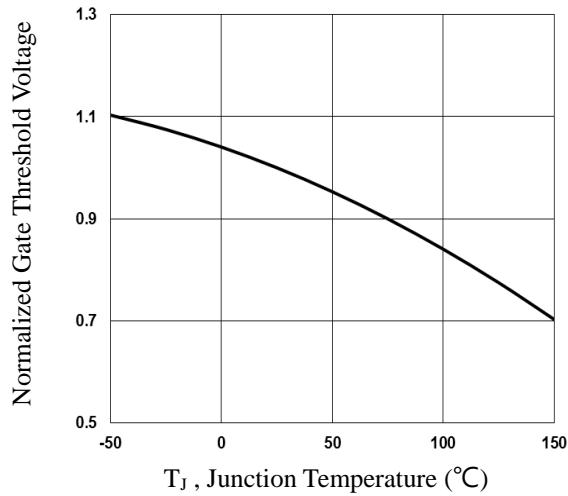
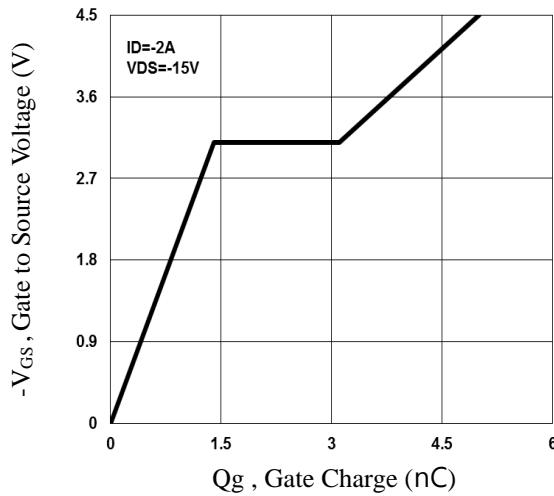
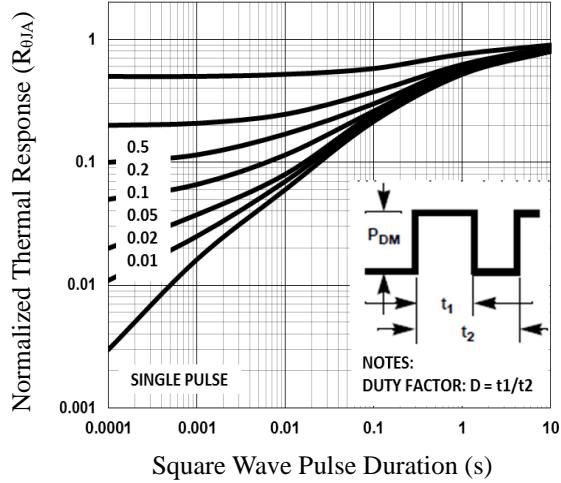
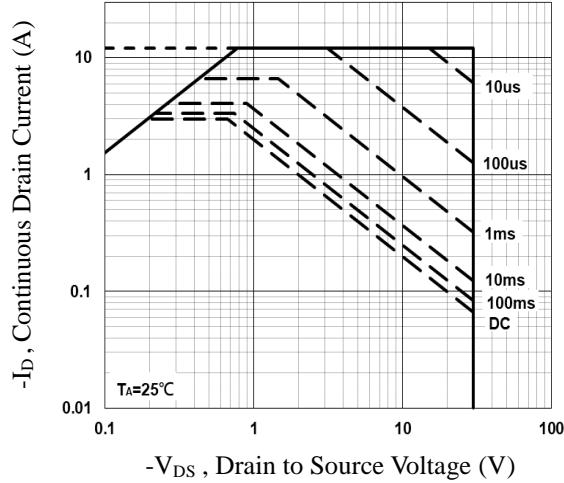
Q_g	Total Gate Charge ^{2,3}	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-2\text{A}$	---	5	8	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	1.4	3	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	1.7	4	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_{\text{G}}=6\Omega$	---	3.4	6	ns
T_r	Rise Time ^{2,3}		---	10.8	21	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	26.9	51	
T_f	Fall Time ^{2,3}		---	6.9	13	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	420	810	pF
C_{oss}	Output Capacitance		---	50	80	
C_{rss}	Reverse Transfer Capacitance		---	35	60	

Drain-Source Diode Characteristics and Maximum Ratings

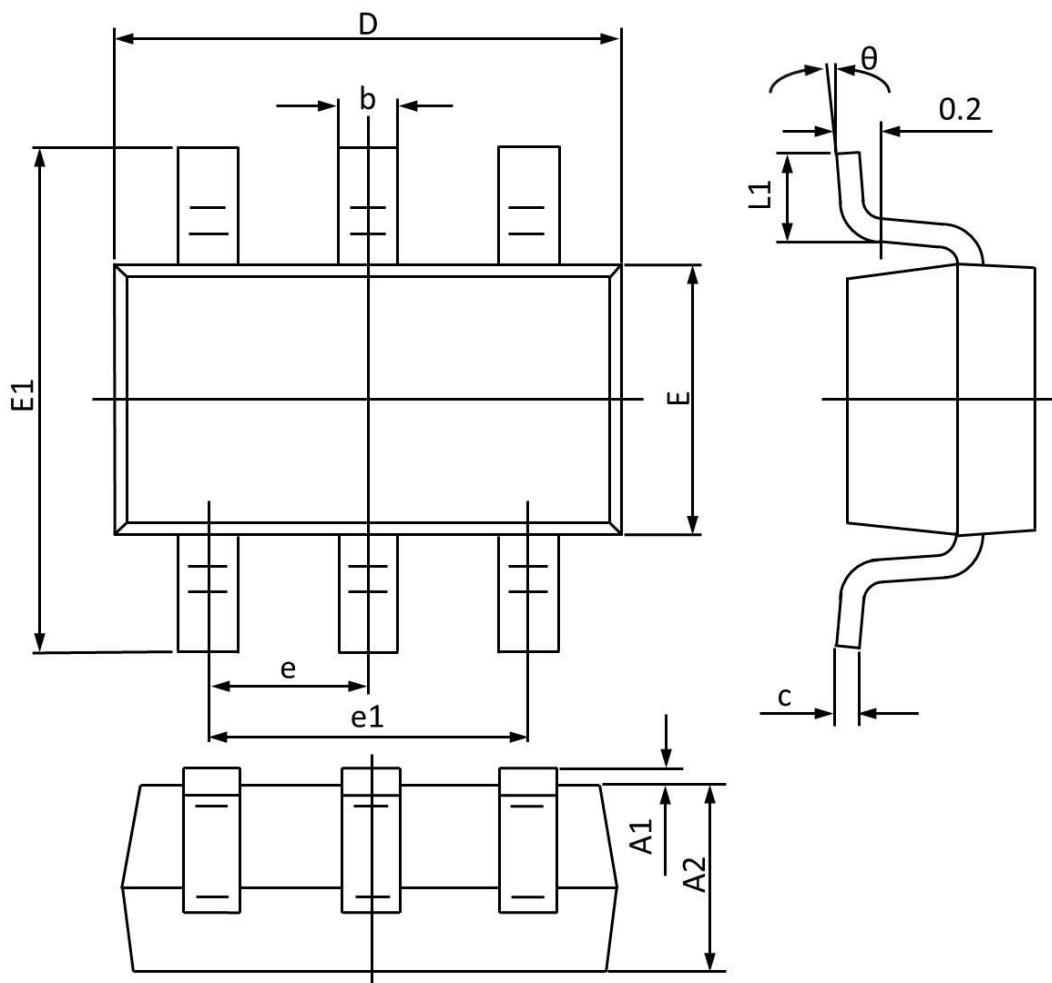
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-3	A
I_{SM}	Pulsed Source Current		---	---	-6	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

4. Repetitive Rating : Pulsed width limited by maximum junction temperature.
5. The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
6. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_C

Fig.2 Normalized RDS(on) vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area

SOT23-6 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A1	0.000	0.100	0.000	0.004
A2	1.000	1.200	0.040	0.047
b	0.300	0.500	0.012	0.019
c	0.047	0.207	0.002	0.008
D	2.800	3.000	0.110	0.118
E	1.500	1.800	0.059	0.070
E1	2.600	3.000	0.103	0.118
e	0.950 TYP		0.037 TYP	
e1	1.900 TYP		0.075 TYP	
L1	0.250	0.550	0.010	0.021
θ	0°	8°	0°	8°