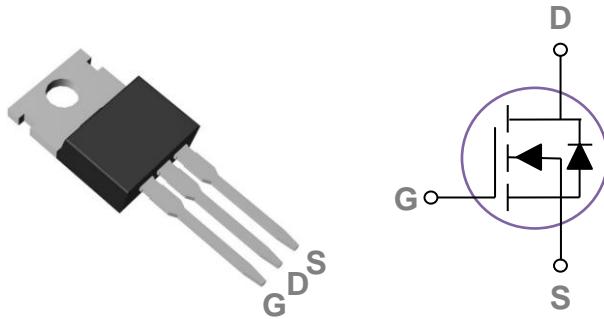


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO220 Pin Configuration



### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	80	V
$V_{GS}$	Gate-Source Voltage	+20 / -12	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	85	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	53	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	340	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	225	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	67	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	154	W
	Power Dissipation – Derate above 25°C	1.23	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.81	°C/W

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	80	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=80\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=64\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS(+)}$	Gate-Source Leakage Current	$V_{GS}=+20\text{V}$ , $V_{DS}=0\text{V}$	---	---	100	$\text{nA}$

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$ , $I_D=30\text{A}$	---	6.1	7.4	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	2.5	3.3	4	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=3\text{A}$	---	10	---	S

**Dynamic and switching Characteristics**

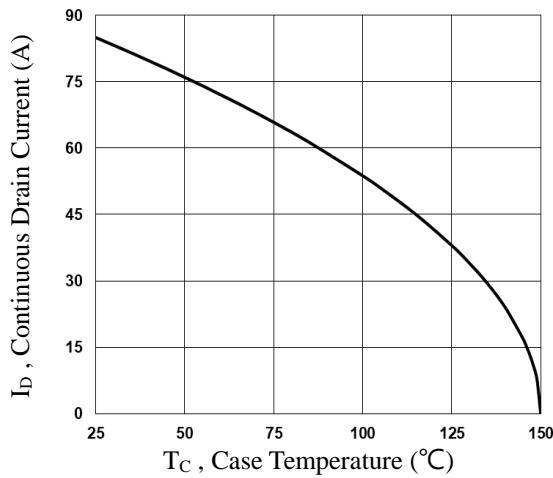
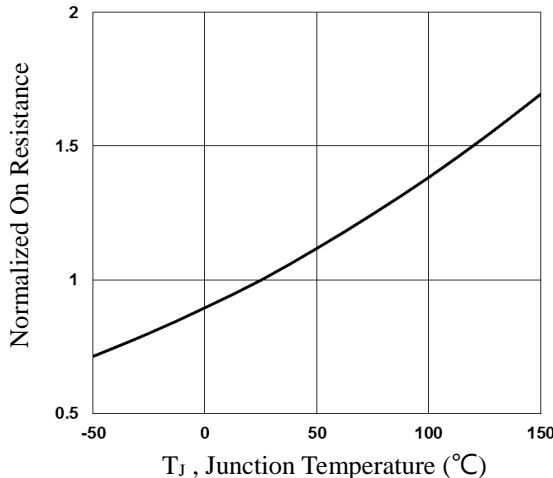
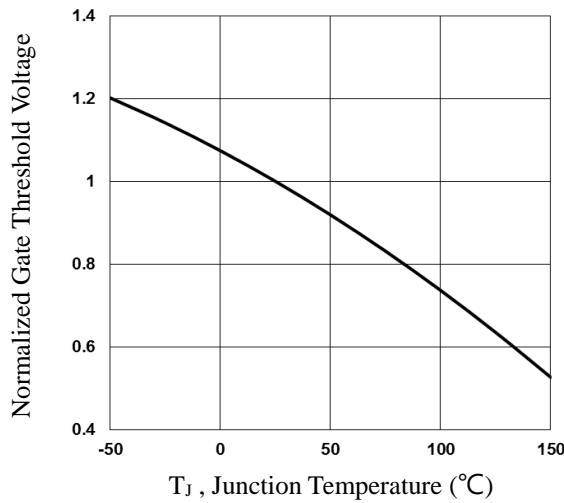
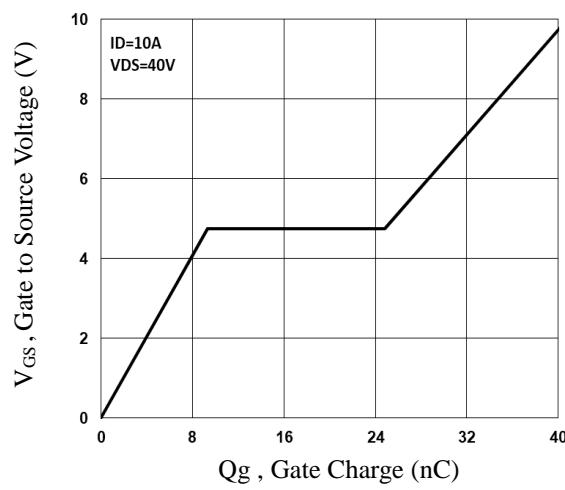
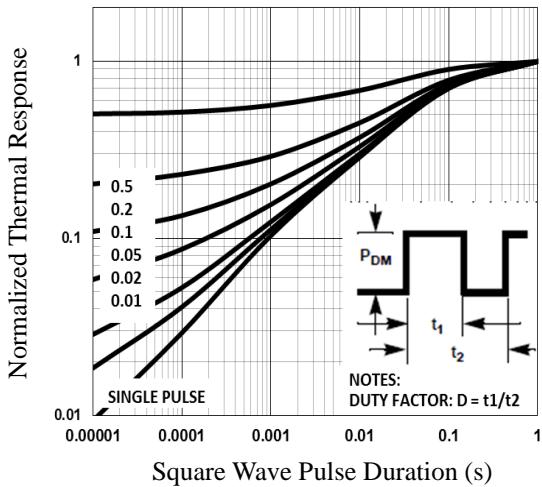
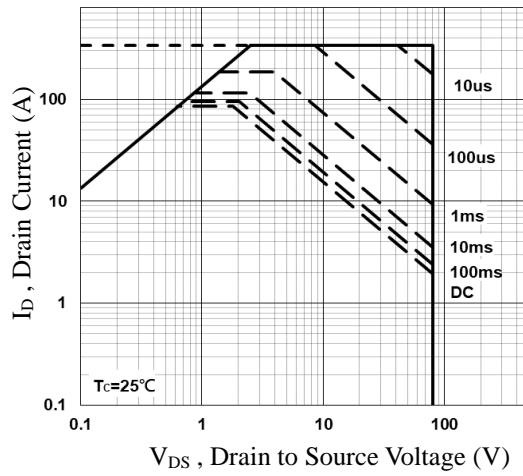
$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=40\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=10\text{A}$	---	40.8	80	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	9.3	18	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	15.5	30	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=40\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$ $I_D=1\text{A}$	---	14.6	30	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	21.5	42	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	52	108	
$T_f$	Fall Time <sup>3, 4</sup>		---	83.5	167	
$C_{iss}$	Input Capacitance	$V_{DS}=40\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	2480	4960	pF
$C_{oss}$	Output Capacitance		---	825	1650	
$C_{rss}$	Reverse Transfer Capacitance		---	48	96	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	0.84	---	$\Omega$

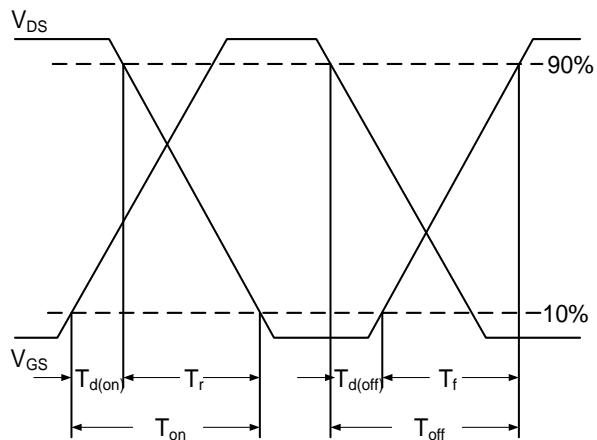
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	85	A
$I_{SM}$	Pulsed Source Current		---	---	170	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V
$T_{rr}$	Reverse Recovery Time	$V_{GS}=10\text{V}$ , $I_S=10\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ $T_J=25\text{ }^{\circ}\text{C}$	---	50	---	ns
$Q_{rr}$	Reverse Recovery Charge		---	70	---	nC

Note :

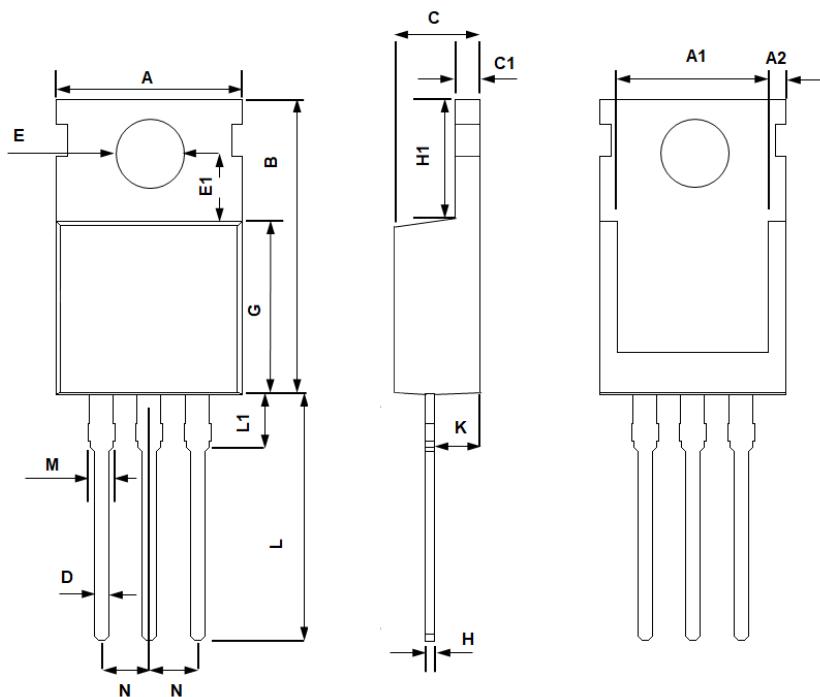
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=40\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=67\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25\text{ }^{\circ}\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. TC**

**Fig.2 Normalized RDSON vs. TJ**

**Fig.3 Normalized Vth vs. TJ**

**Fig.4 Gate Charge Characteristics**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

## TO220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.400	9.700	0.409	0.382
A1	8.900	7.400	0.350	0.291
A2	1.400	0.800	0.055	0.031
B	16.500	14.500	0.650	0.571
C	4.750	4.200	0.187	0.165
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	4.000	3.300	0.157	0.130
E1	3.800	3.400	0.150	0.134
G	9.400	8.400	0.370	0.331
H	0.600	0.200	0.024	0.008
H1	6.850	6.200	0.270	0.244
K	2.850	2.100	0.112	0.083
L	14.000	12.500	0.551	0.492
L1	4.000	2.700	0.157	0.106
M	1.750	1.100	0.069	0.043
N	2.640	2.440	0.104	0.096