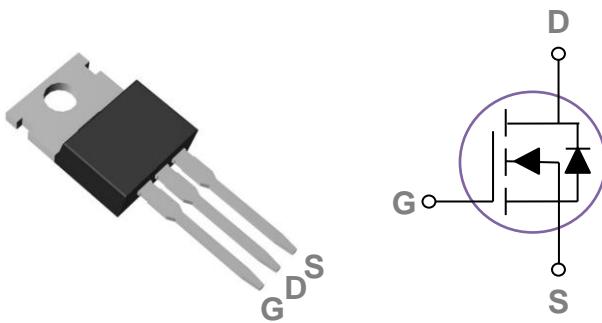


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO220 Pin Configuration



BVDSS	RDS(ON)	ID
65V	3.3mΩ	150A

### Features

- 60V, 150A,  $RDS(ON) = 3.3m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	65	V
$V_{GS}$	Gate-Source Voltage	+20/-12	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	150	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	94	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	600	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	245	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	70	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	183	W
	Power Dissipation – Derate above 25°C	1.47	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.68	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_{\text{D}}=1\text{mA}$	---	0.05	---	$\text{V}/^\circ\text{C}$
$I_{\text{DS}}^{\text{SS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=85^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	100	$\text{nA}$

**On Characteristics**

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=40\text{A}$	---	2.7	3.3	$\text{m}\Omega$
		$V_{\text{GS}}=6\text{V}$ , $I_{\text{D}}=10\text{A}$	---	4.1	5.4	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_{\text{D}}=250\mu\text{A}$	2	3	4	V
			---	-5	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_{\text{D}}=5\text{A}$	---	19	---	S

**Dynamic and switching Characteristics**

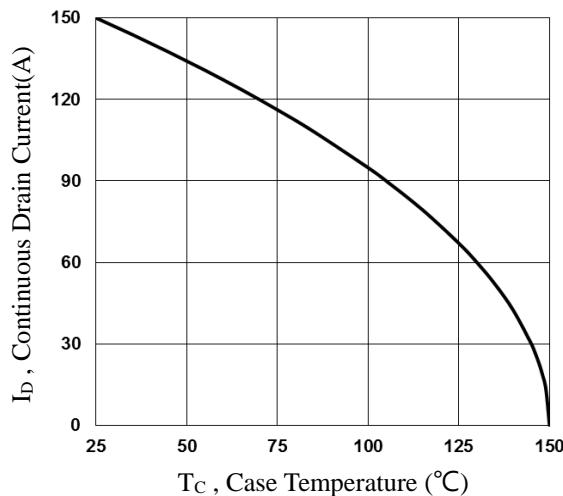
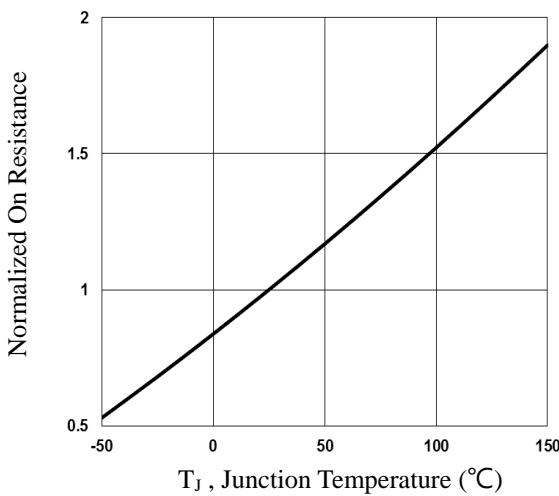
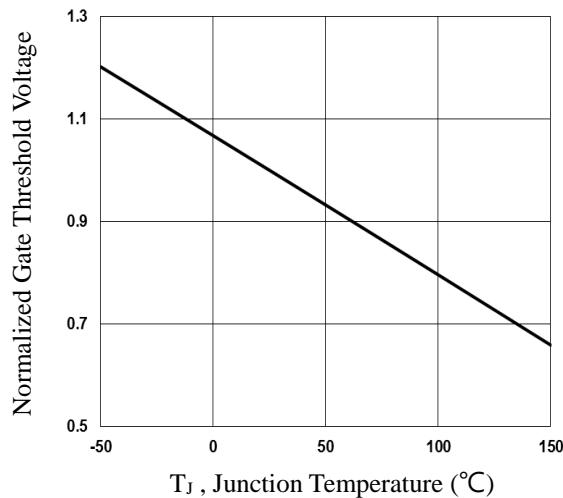
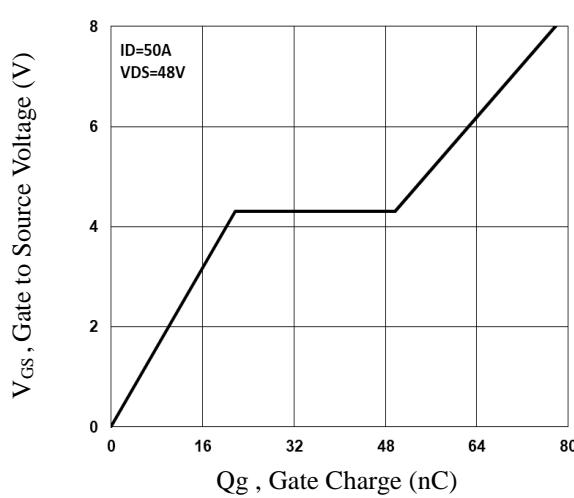
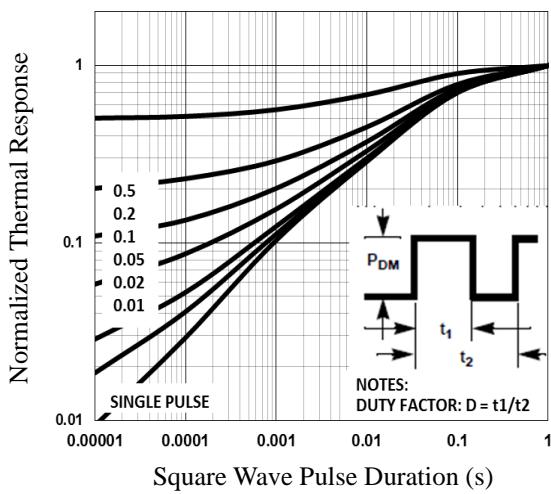
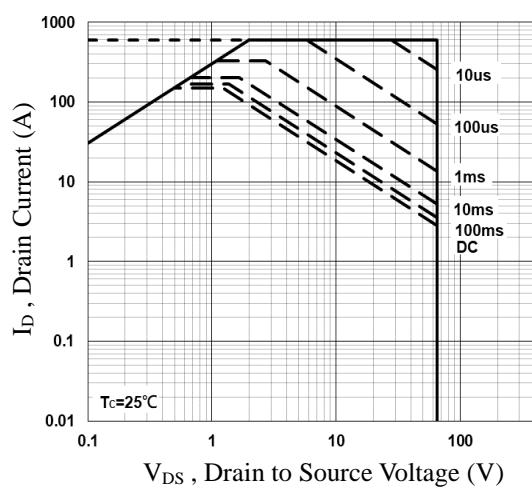
$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{\text{DS}}=48\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=10\text{A}$	---	93	---	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>3, 4</sup>		---	21.7	---	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>3, 4</sup>		---	28	---	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{\text{DD}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_{\text{G}}=6\Omega$	---	22	44	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	14	28	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>3, 4</sup>		---	40	80	
$T_f$	Fall Time <sup>3, 4</sup>		---	20	40	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	4620	---	pF
$C_{\text{oss}}$	Output Capacitance		---	780	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	50	---	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	1.8	3.6	$\Omega$

**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	150	A
			---	---	300	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}}=0\text{V}$ , $I_s=50\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	---	62	---	ns
$Q_{\text{rr}}$	Reverse Recovery Charge	$T_J=25^\circ\text{C}$	---	175	---	nC

Note :

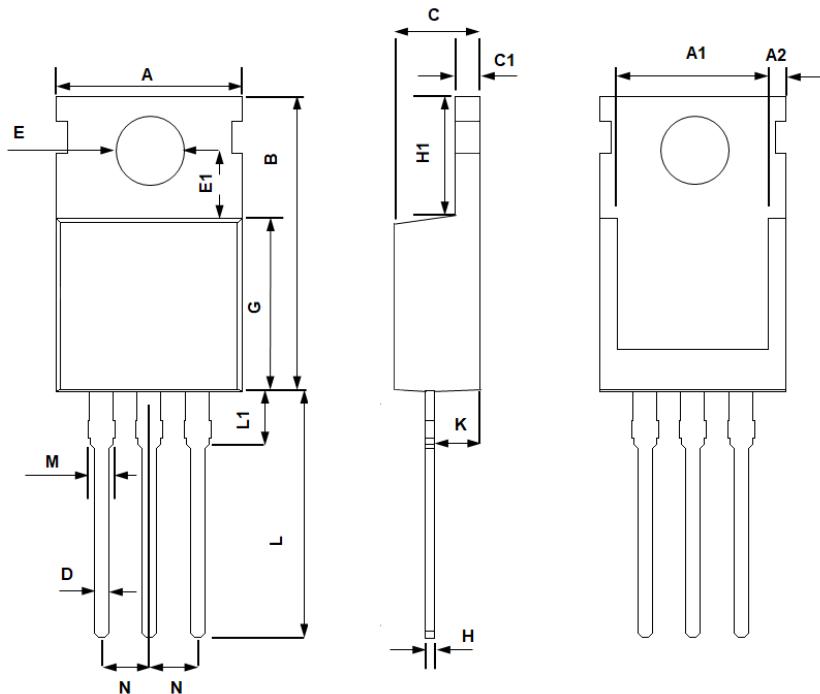
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{\text{DD}}=48\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{\text{AS}}=70\text{A}$ ,  $R_{\text{G}}=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_c$** 

**Fig.2 Normalized RD<sub>SON</sub> vs.  $T_j$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.4 Gate Charge Characteristics**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

## TO220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.400	9.700	0.409	0.382
A1	8.900	7.400	0.350	0.291
A2	1.400	0.800	0.055	0.031
B	16.500	14.500	0.650	0.571
C	4.750	4.200	0.187	0.165
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	4.000	3.300	0.157	0.130
E1	3.800	3.400	0.150	0.134
G	9.400	8.400	0.370	0.331
H	0.600	0.200	0.024	0.008
H1	6.850	6.200	0.270	0.244
K	2.850	2.100	0.112	0.083
L	14.000	12.500	0.551	0.492
L1	4.000	2.700	0.157	0.106
M	1.750	1.100	0.069	0.043
N	2.640	2.440	0.104	0.096