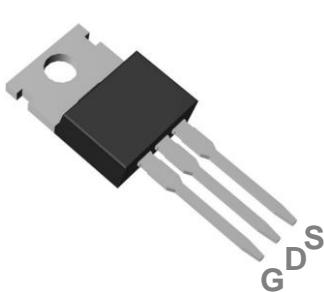


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO220 Pin Configuration



BVDSS	RDS(ON)	ID
150V	16mΩ	65A

### Features

- 150V, 65A, RDS(ON) = 16mΩ @ VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	150	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	65	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	41	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	260	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	364	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	27	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	223	W
	Power Dissipation – Derate above 25°C	1.79	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.56	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	150	---	---	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=120\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=120\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=85^\circ\text{C}$	---	---	10	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

**On Characteristics**

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=25\text{A}$	---	13	16	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	2.5	3.5	4.5	V
$\text{gfs}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=3\text{A}$	---	13	---	S

**Dynamic and switching Characteristics**

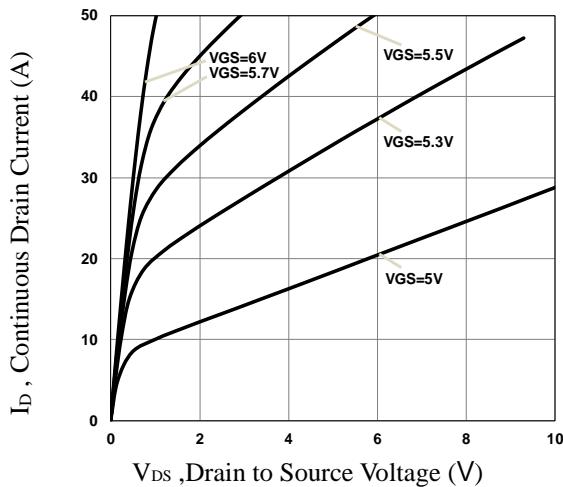
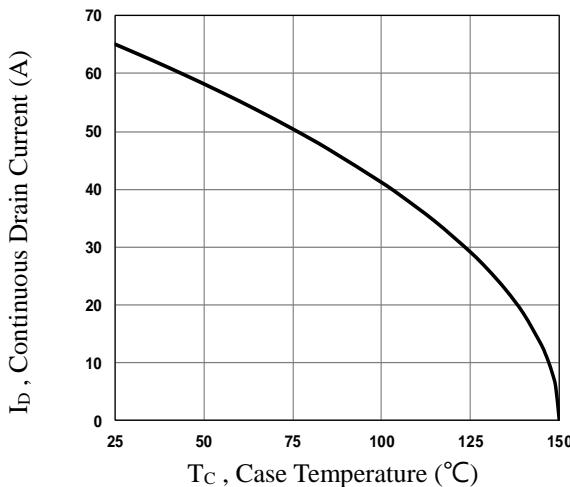
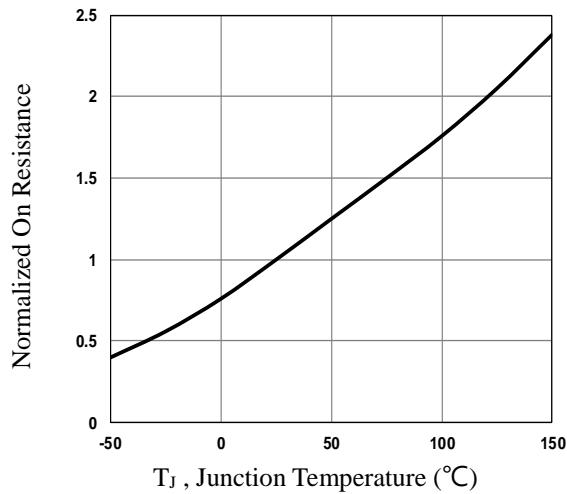
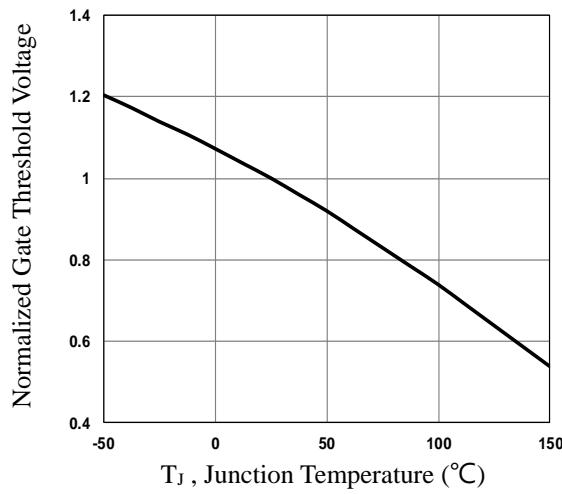
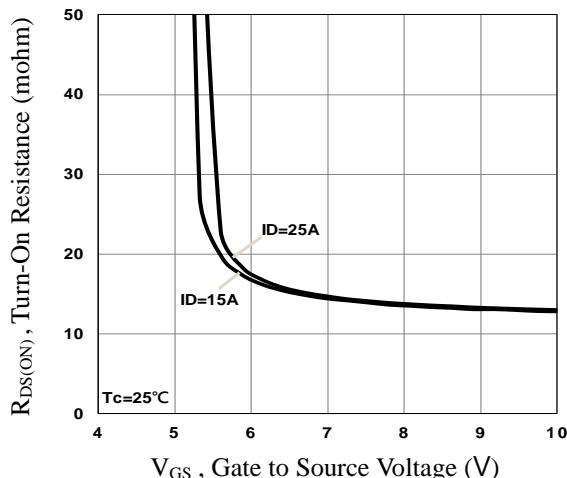
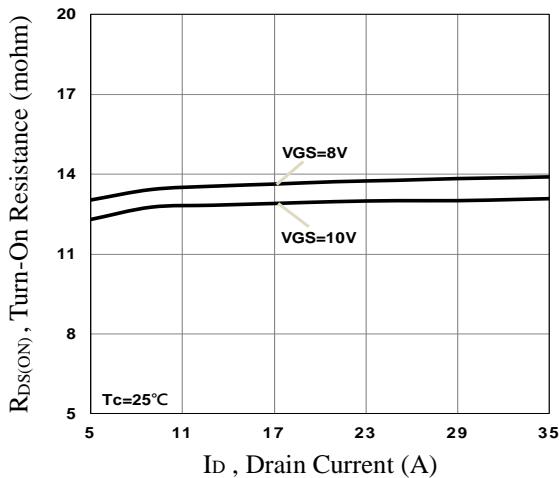
$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=30\text{A}$	---	47	75	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>3, 4</sup>		---	8	15	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>3, 4</sup>		---	17	25	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{\text{DD}}=75\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=6\Omega$ $I_D=30\text{A}$	---	15	25	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	28	40	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>3, 4</sup>		---	45	70	
$T_f$	Fall Time <sup>3, 4</sup>		---	32	50	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	2430	3650	pF
$C_{\text{oss}}$	Output Capacitance		---	207	310	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	14	25	
$R_g$	Gate Resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	1.5	---	$\Omega$

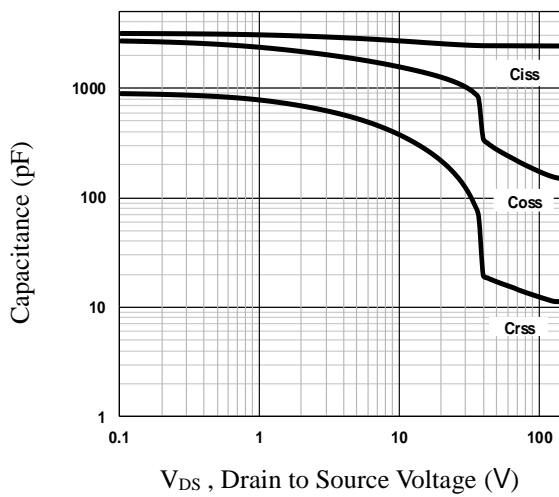
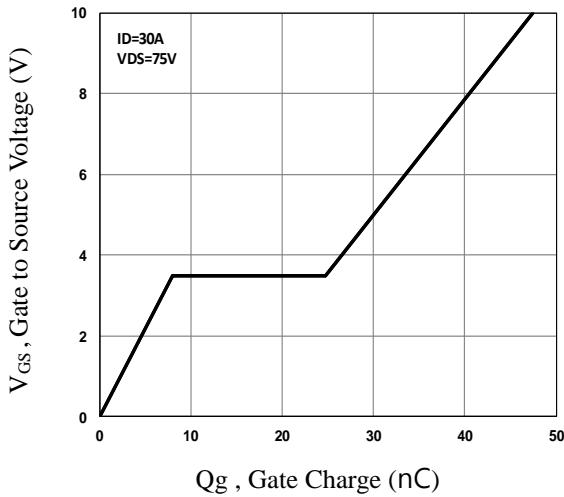
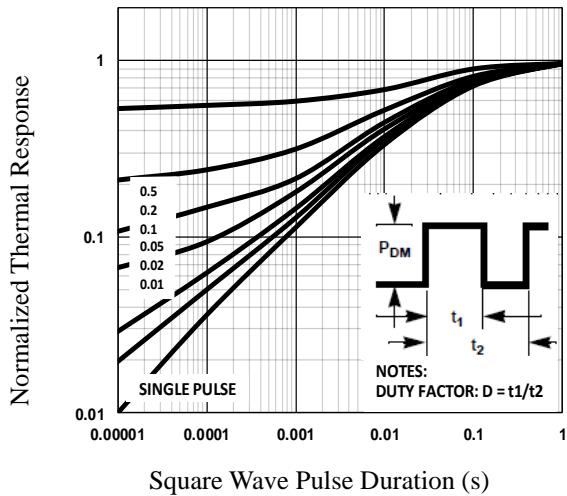
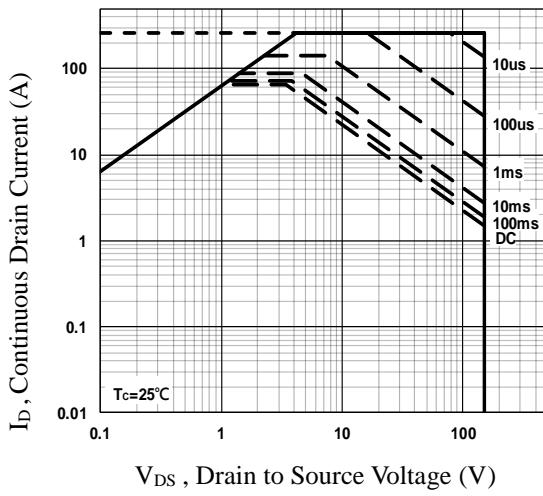
**Drain-Source Diode Characteristics and Maximum Ratings**

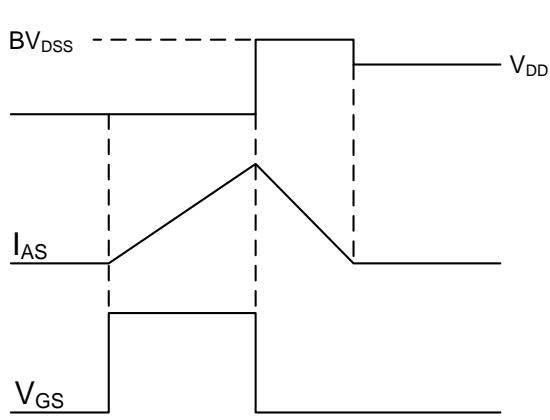
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	65	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	130	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$t_{\text{rr}}$	Reverse Recovery Time <sup>3</sup>	$V_R=100\text{V}$ , $I_s=10\text{A}$ $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	75	---	ns
$Q_{\text{rr}}$	Reverse Recovery Charge <sup>3</sup>		---	230	---	nC

Note :

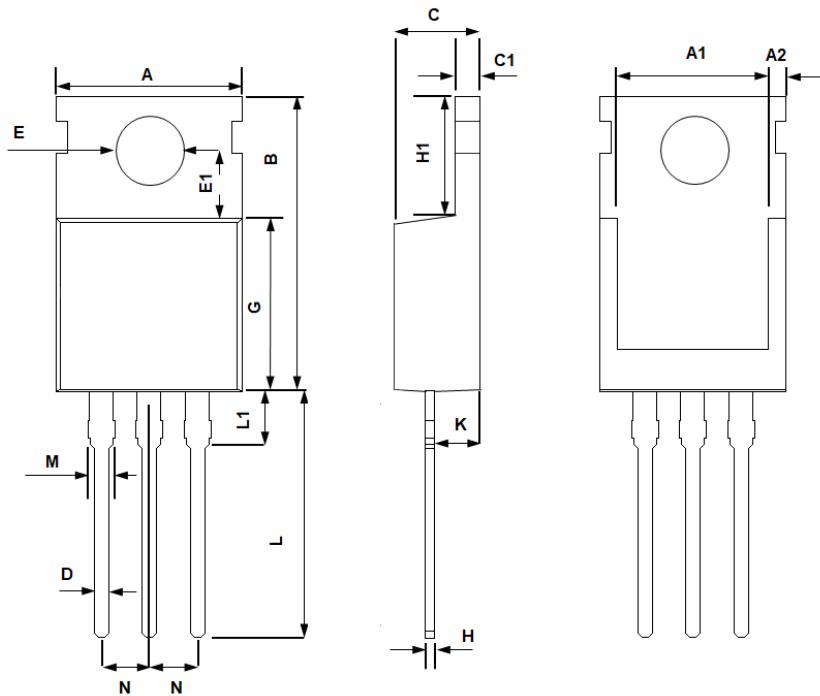
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=1\text{mH}$ ,  $I_{\text{AS}}=27\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Typical Output Characteristics**

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized  $R_{DSON}$  vs.  $T_j$** 

**Fig.4 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.5 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.6 Turn-On Resistance vs.  $I_D$**


**Fig.7 Capacitance Characteristics**

**Fig.8 Gate Charge Characteristics**

**Fig.9 Normalized Transient Impedance**

**Fig.10 Maximum Safe Operation Area**

**Fig.11 Switching Time Waveform**

**Fig.12 EAS Waveform**

## TO220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.400	9.700	0.409	0.382
A1	8.900	7.400	0.350	0.291
A2	1.400	0.800	0.055	0.031
B	16.500	14.500	0.650	0.571
C	4.750	4.200	0.187	0.165
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	4.000	3.300	0.157	0.130
E1	3.800	3.400	0.150	0.134
G	9.400	8.400	0.370	0.331
H	0.600	0.200	0.024	0.008
H1	6.850	6.200	0.270	0.244
K	2.850	2.100	0.112	0.083
L	14.000	12.500	0.551	0.492
L1	4.000	2.700	0.157	0.106
M	1.750	1.100	0.069	0.043
N	2.640	2.440	0.104	0.096