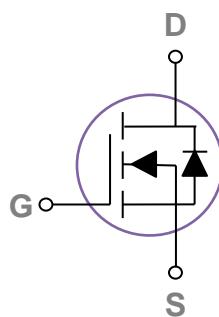
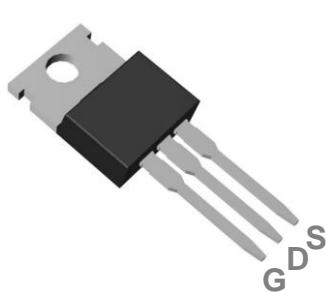


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO220 Pin Configuration



BVDSS	RDS(ON)	ID
150V	22mΩ	60A

### Features

- 150V,60A, RDS(ON) =22mΩ @VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	150	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	60	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	38	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	240	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	76	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	39	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	207	W
	Power Dissipation – Derate above $25^\circ\text{C}$	1.66	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.60	$^\circ\text{C}/\text{W}$



150V N-Channel MOSFETs

PDP60ND15BH

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	150	---	---	V
$I_{DS}$	Drain-Source Leakage Current	$V_{DS}=120\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=120\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=85\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$ , $I_D=20\text{A}$	---	18	22	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	2.5	3.5	4.5	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=3\text{A}$	---	10	---	S

**Dynamic and switching Characteristics<sup>3</sup>**

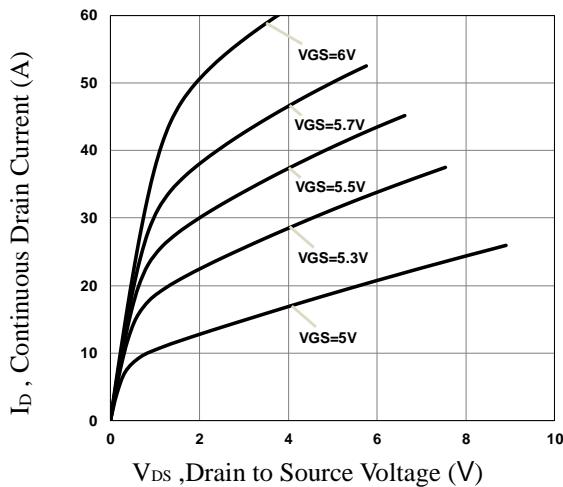
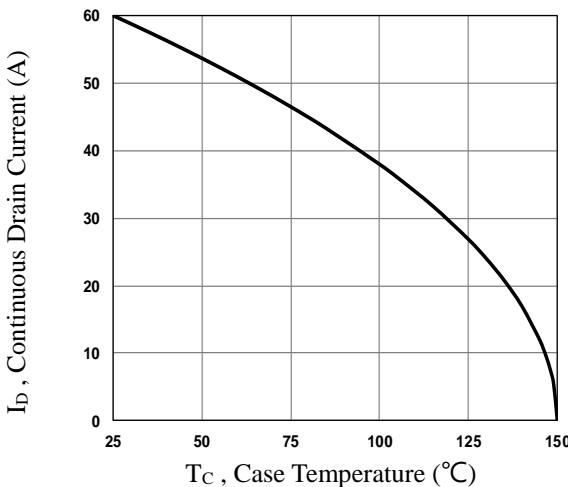
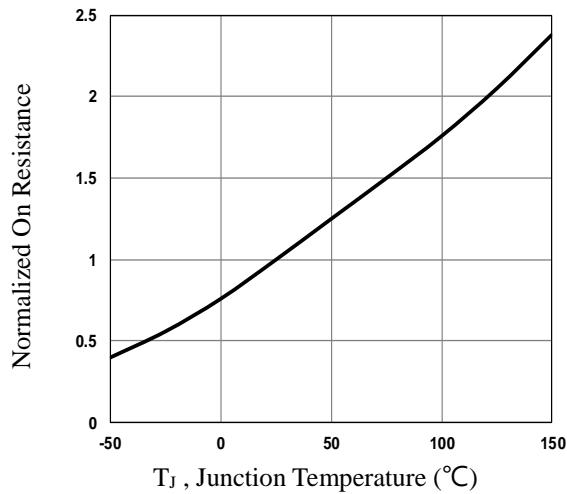
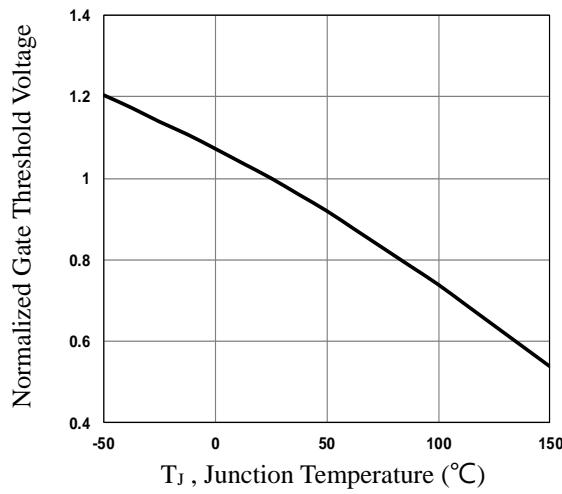
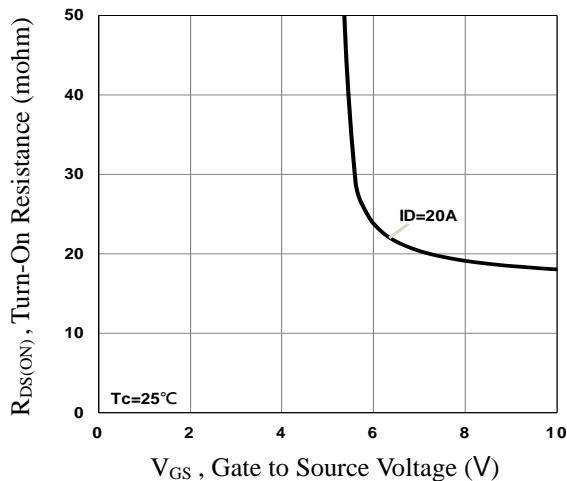
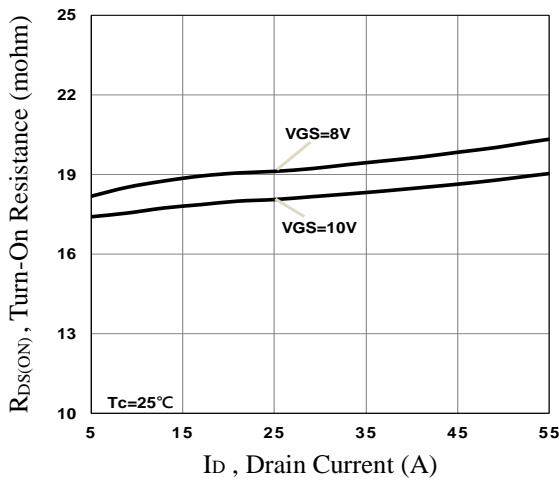
$Q_g$	Total Gate Charge	$V_{DS}=75\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=30\text{A}$	---	34	50	nC
$Q_{gs}$	Gate-Source Charge		---	5.5	9	
$Q_{gd}$	Gate-Drain Charge		---	11.5	18	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=75\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$ $I_D=30\text{A}$	---	6	10	ns
$T_r$	Rise Time		---	9	15	
$T_{d(off)}$	Turn-Off Delay Time		---	8	12	
$T_f$	Fall Time		---	12	18	
$C_{iss}$	Input Capacitance	$V_{DS}=75\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	1690	2550	pF
$C_{oss}$	Output Capacitance		---	150	230	
$C_{rss}$	Reverse Transfer Capacitance		---	7	11	
$R_g$	Gate Resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	0.5	---	$\Omega$

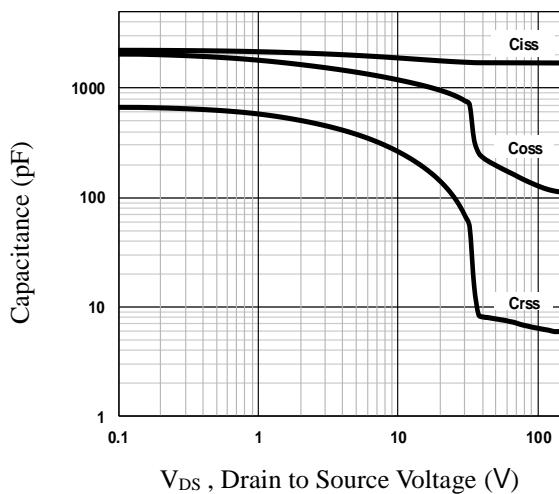
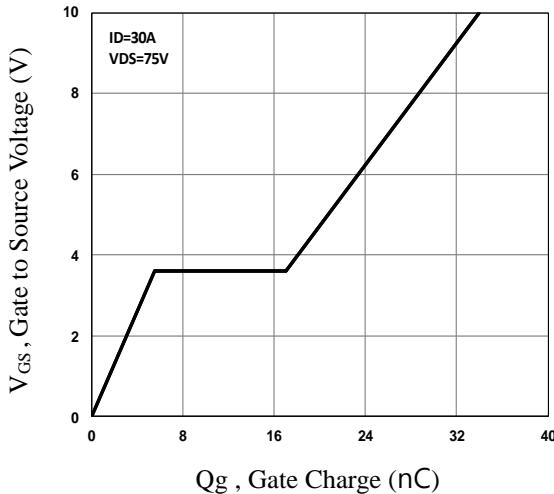
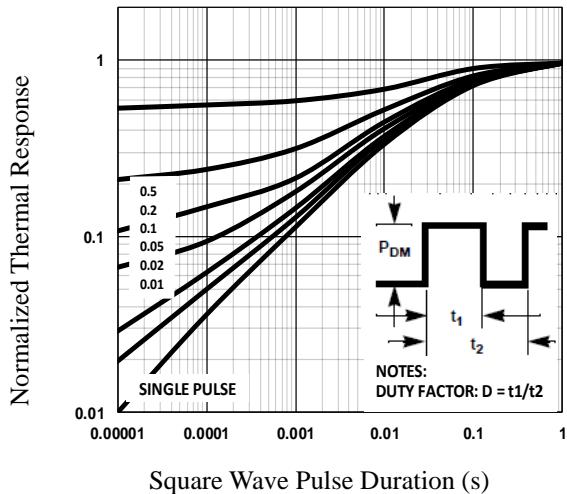
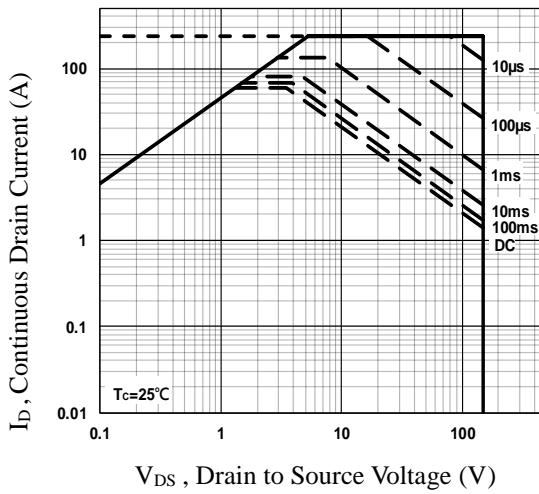
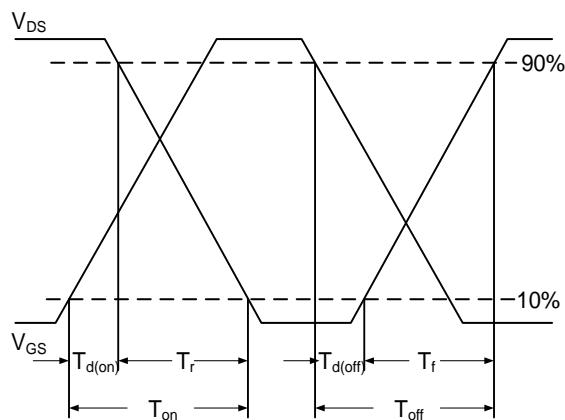
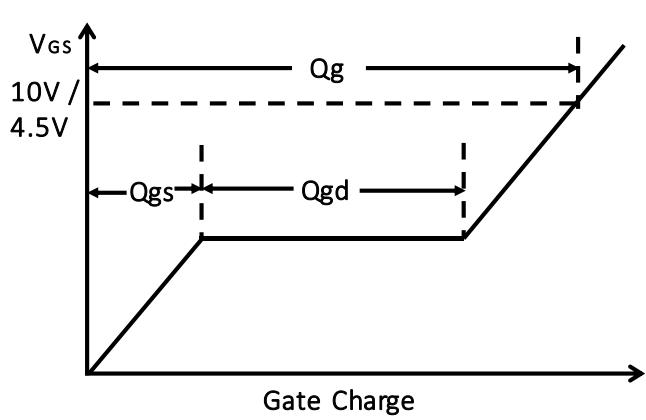
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	60	A
$I_{SM}$	Pulsed Source Current		---	---	120	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V
$t_{rr}$	Reverse Recovery Time	$V_R=100\text{V}$ , $I_s=10\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	65	---	ns
$Q_{rr}$	Reverse Recovery Charge		---	210	---	nC

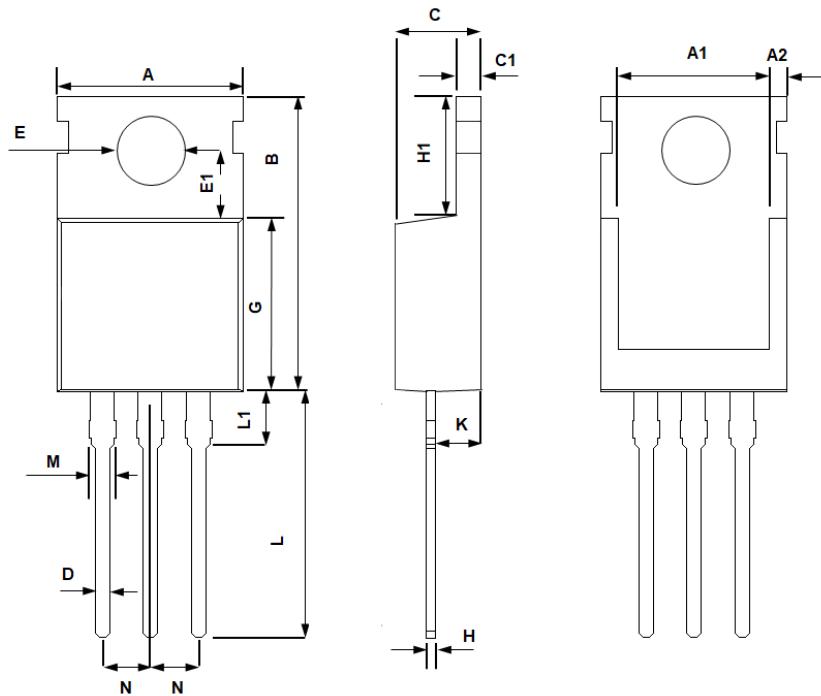
Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{DD}=50\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=39\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25\text{ }^{\circ}\text{C}$ .
- Essentially independent of operating temperature.


**Fig.1 Typical Output Characteristics**

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized  $R_{DSON}$  vs.  $T_j$** 

**Fig.4 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.5 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.6 Turn-On Resistance vs.  $I_D$**


**Fig.7 Capacitance Characteristics**

**Fig.8 Gate Charge Characteristics**

**Fig.9 Normalized Transient Impedance**

**Fig.10 Maximum Safe Operation Area**

**Fig.11 Switching Time Waveform**

**Fig.12 Gate Charge Waveform**

## TO220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.400	9.700	0.409	0.382
A1	8.900	7.400	0.350	0.291
A2	1.400	0.800	0.055	0.031
B	16.500	14.500	0.650	0.571
C	4.750	4.200	0.187	0.165
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	4.000	3.300	0.157	0.130
E1	3.800	3.400	0.150	0.134
G	9.400	8.400	0.370	0.331
H	0.600	0.200	0.024	0.008
H1	6.850	6.200	0.270	0.244
K	2.850	2.100	0.112	0.083
L	14.000	12.500	0.551	0.492
L1	4.000	2.700	0.157	0.106
M	1.750	1.100	0.069	0.043
N	2.640	2.440	0.104	0.096