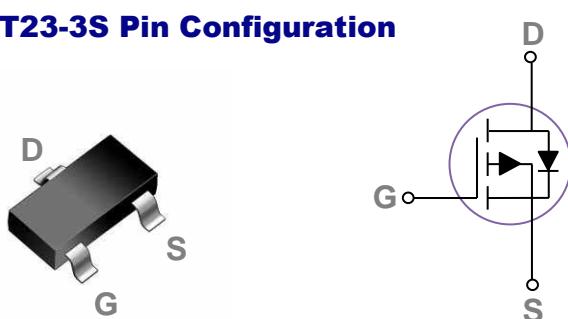


General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
-30V	32mΩ	-5.1A

Features

- -30V, -5.1A, RDS(ON) = 32mΩ@VGS = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	-5.1	A
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	-3.2	A
I_{DM}	Drain Current – Pulsed ¹	-20.4	A
EAS	Single Pulse Avalanche Energy ²	39.2	mJ
IAS	Single Pulse Avalanche Current ²	28	A
P_D	Power Dissipation ($T_A=25^\circ\text{C}$)	1.56	W
	Power Dissipation – Derate above 25°C	0.0125	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	-30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$, $I_D=-1\text{mA}$	---	-0.03	---	$\text{V}/\text{ }^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-30\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	μA
		$V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$, $I_D=-4\text{A}$	---	27	32	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-2\text{A}$	---	38	46	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu\text{A}$	-1.2	-1.6	-2.2	V
			---	4	---	$\text{mV}/\text{ }^{\circ}\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=-10\text{V}$, $I_D=-3\text{A}$	---	9	---	S

Dynamic and switching Characteristics

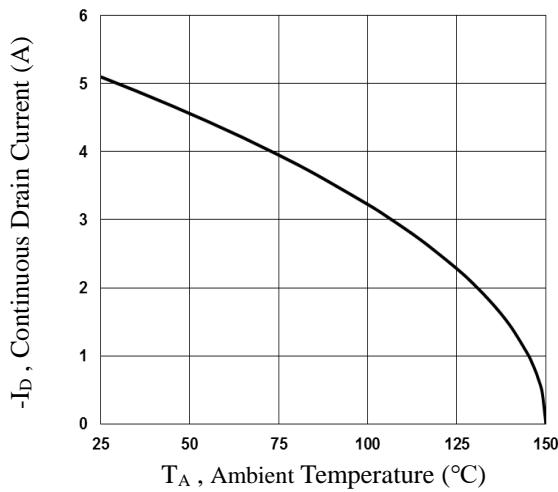
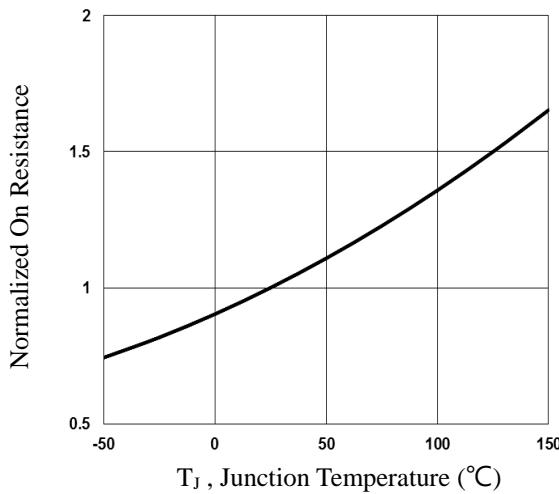
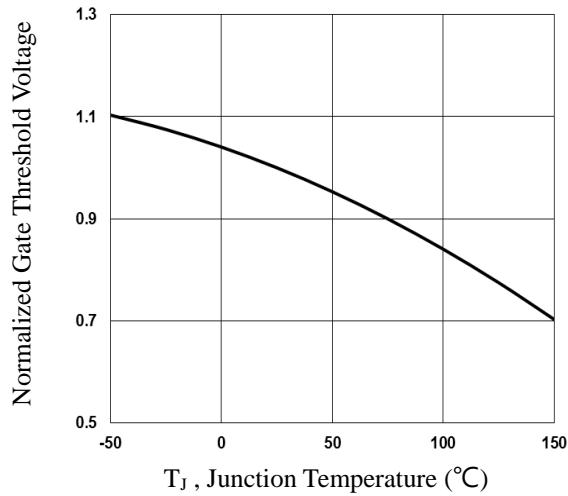
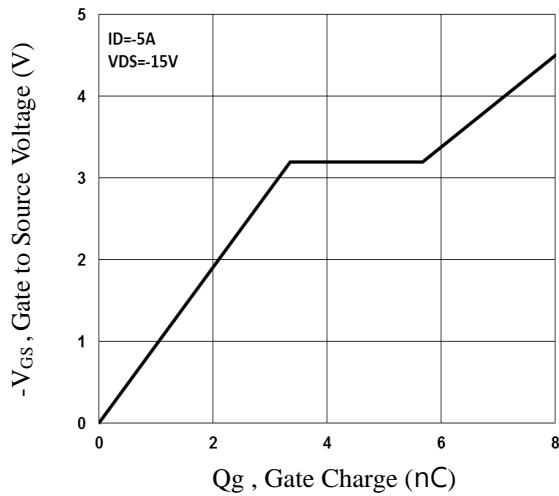
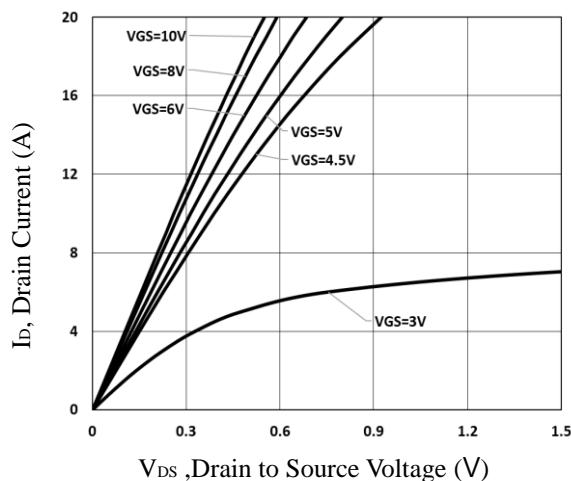
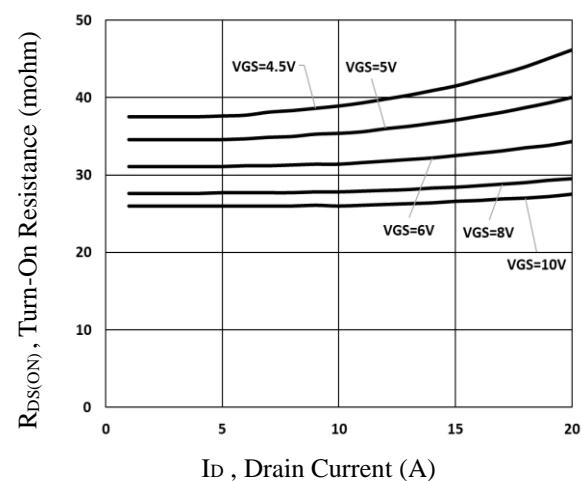
Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=-15\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-5\text{A}$	---	8	15	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	3.3	6	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	2.3	5	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=-15\text{V}$, $V_{GS}=-10\text{V}$, $R_G=6\Omega$ $I_D=-1\text{A}$	---	4.6	9	ns
T_r	Rise Time ^{3, 4}		---	14	26	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	34	58	
T_f	Fall Time ^{3, 4}		---	18	35	
C_{iss}	Input Capacitance	$V_{DS}=-15\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	757	1280	pF
C_{oss}	Output Capacitance		---	122	210	
C_{rss}	Reverse Transfer Capacitance		---	88	175	

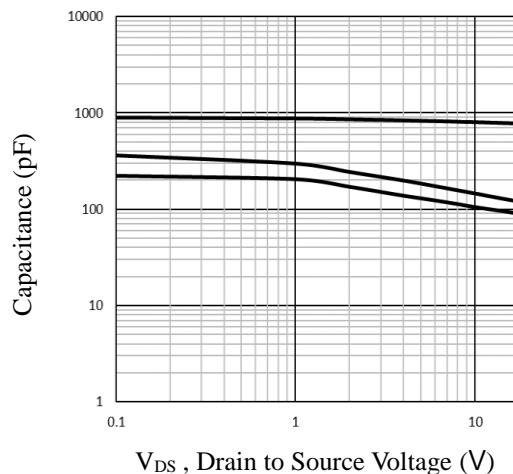
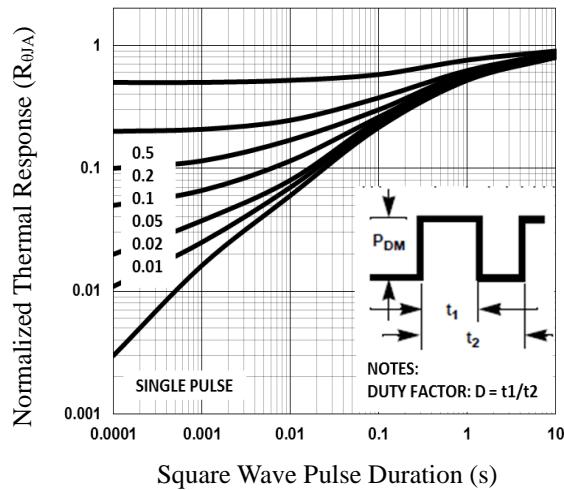
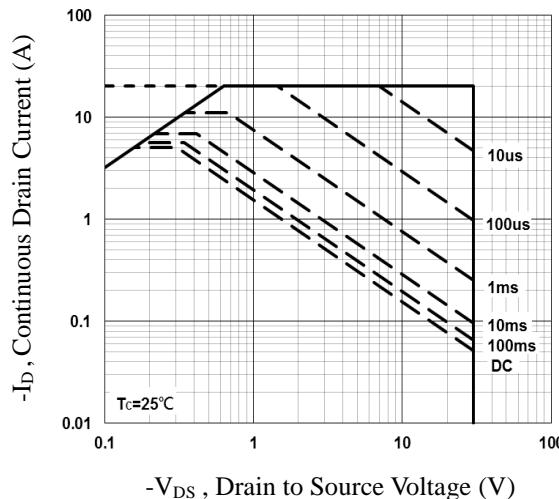
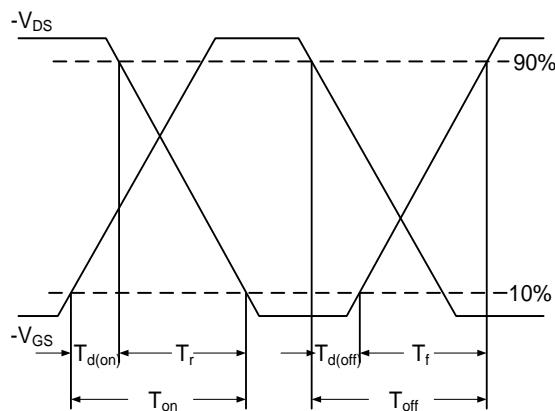
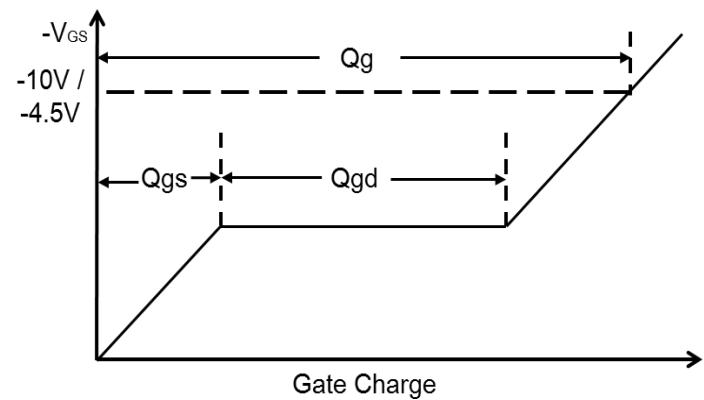
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-5.1	A
I_{SM}	Pulsed Source Current		---	---	-10.2	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	V

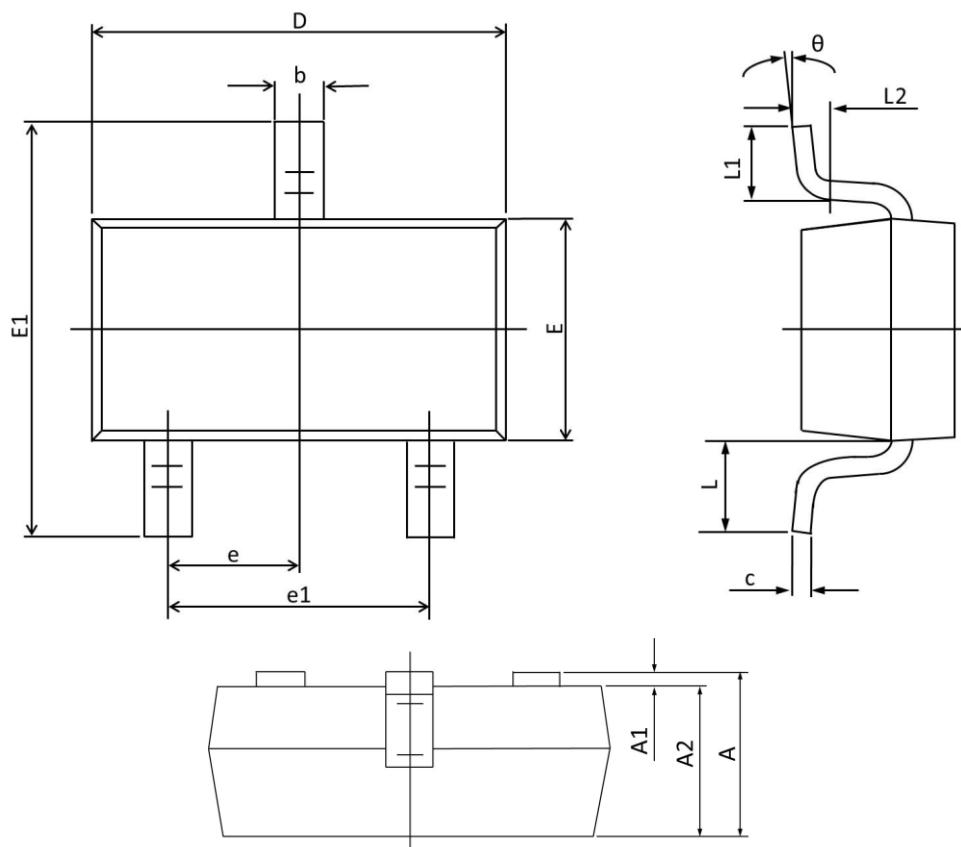
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=28\text{A}$, $R_G=25\Omega$, Starting $T_J=25\text{ }^{\circ}\text{C}$.
3. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. TA

Fig.2 Normalized RDSON vs. TJ

Fig.3 Normalized V_{th} vs. TJ

Fig.4 Gate Charge Waveform

Fig.5 Typical Output Characteristics

Fig.6 Turn-On Resistance vs. ID


Fig.7 Capacitance Characteristics

Fig.8 Normalized Transient Impedance

Fig.9 Maximum Safe Operation Area

Fig.10 Switching Time Waveform

Fig.11 Gate Charge Waveform

SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.150	0.900	0.045	0.035
A1	0.100	0.000	0.004	0.000
A2	1.050	0.900	0.041	0.035
b	0.500	0.300	0.020	0.012
c	0.150	0.080	0.006	0.003
D	3.000	2.800	0.118	0.110
E	1.400	1.200	0.055	0.047
E1	2.550	2.250	0.100	0.089
e	0.95 TYP.		0.037 TYP.	
e1	2.000	1.800	0.079	0.071
L	0.55 REF.		0.022 REF.	
L1	0.500	0.300	0.020	0.012
L2	0.25 TYP.		0.01 TYP.	
θ	8°	0°	8°	0°