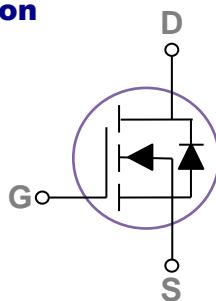


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
30V	36mΩ	5.3A

Features

- 30V, 5.3 A, RDS(ON) = 36mΩ@VGS = 4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for 2.5V Gate Drive Applications

Applications

- Notebook
- Load Switch
- LED applications

Absolute Maximum Ratings T_c=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±12	V
I _D	Drain Current – Continuous (T _A =25°C)	5.3	A
	Drain Current – Continuous (T _A =70°C)	4.2	A
I _{DM}	Drain Current – Pulsed ¹	21.2	A
P _D	Power Dissipation (T _A =25°C)	1.56	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	80	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$, $I_D=1\text{mA}$	---	0.06	---	V/C
I_{DS}	Drain-Source Leakage Current	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	μA
I_{GS}	Gate-Source Leakage Current	$V_{GS}=\pm 12\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=4.5\text{V}$, $I_D=4\text{A}$	---	31	36	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$, $I_D=3\text{A}$	---	36	45	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D = 250\mu\text{A}$	0.4	0.6	0.9	V
			---	-3	---	
g_{fs}	Forward Transconductance	$V_{DS}=10\text{V}$, $I_S=3\text{A}$	---	7	---	S

Dynamic and switching Characteristics

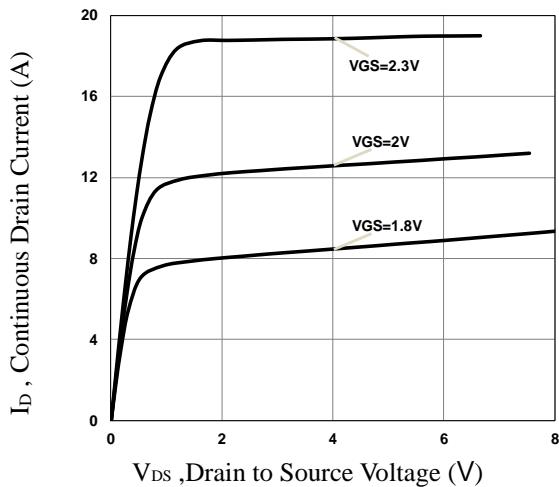
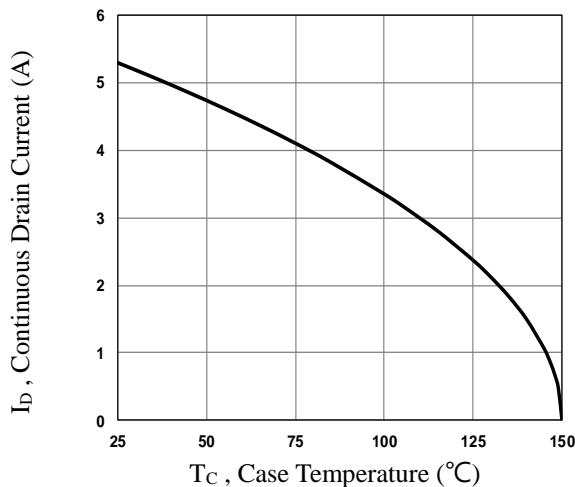
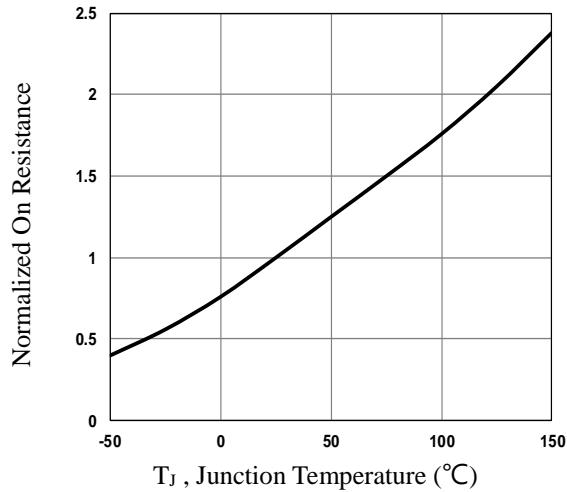
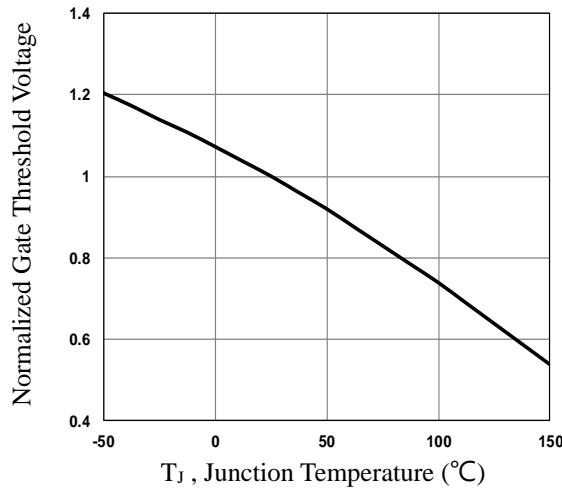
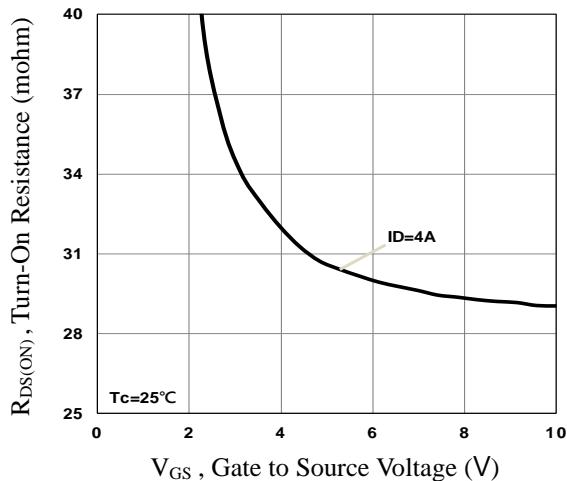
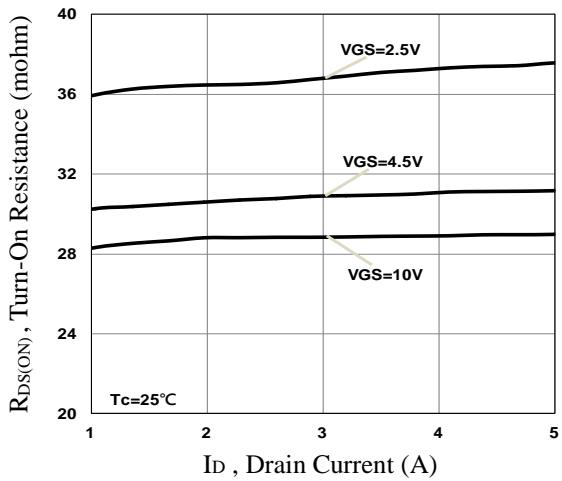
Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=10\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=4\text{A}$	---	8.4	12	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	1	2	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	2.2	4	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=10\text{V}$, $V_{GS}=4.5\text{V}$, $R_G=25\Omega$	---	4.5	9	nS
T_r	Rise Time ^{2, 3}		---	13	25	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	27	51	
T_f	Fall Time ^{2, 3}		---	8.3	16	
C_{iss}	Input Capacitance	$V_{DS}=10\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	695	1000	pF
C_{oss}	Output Capacitance		---	45	65	
C_{rss}	Reverse Transfer Capacitance		---	36	50	
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $F=1\text{MHz}$	---	1.5	3	Ω

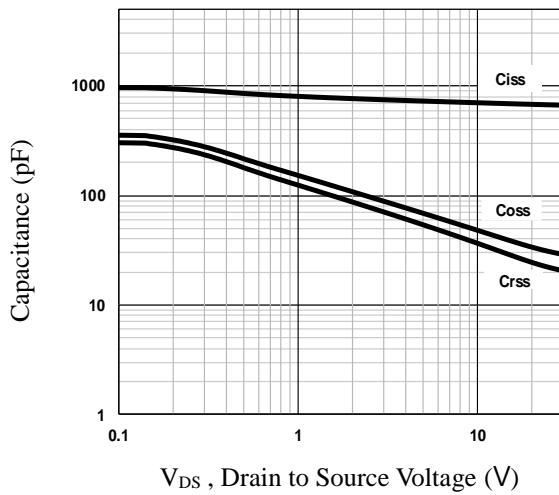
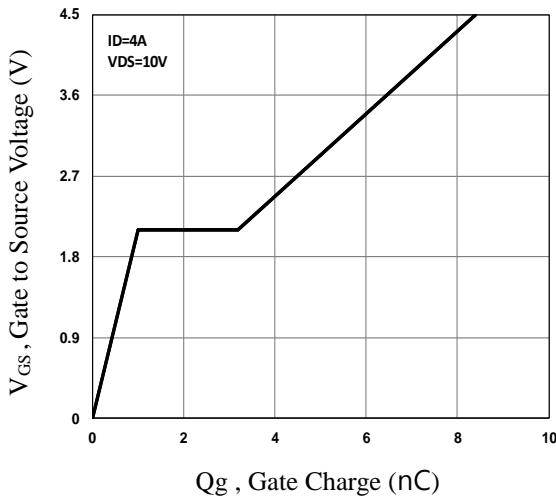
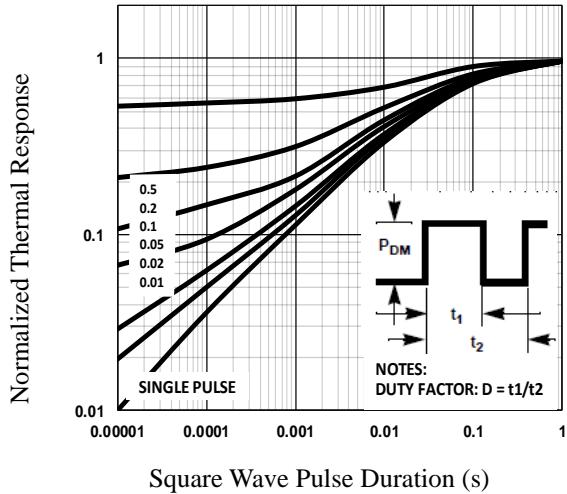
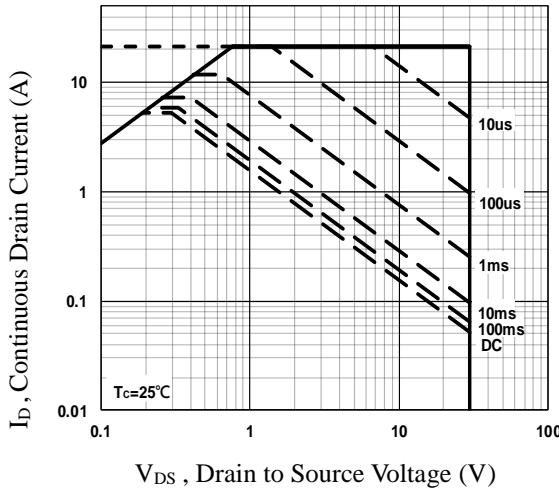
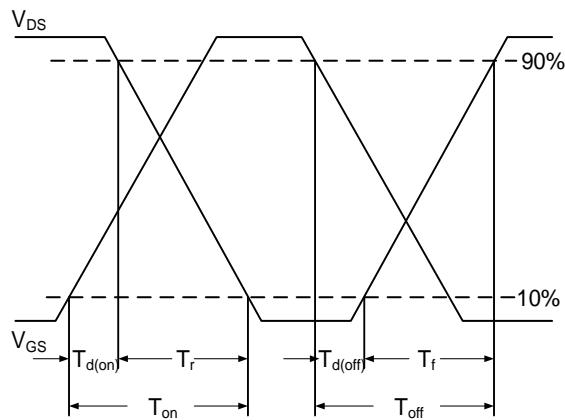
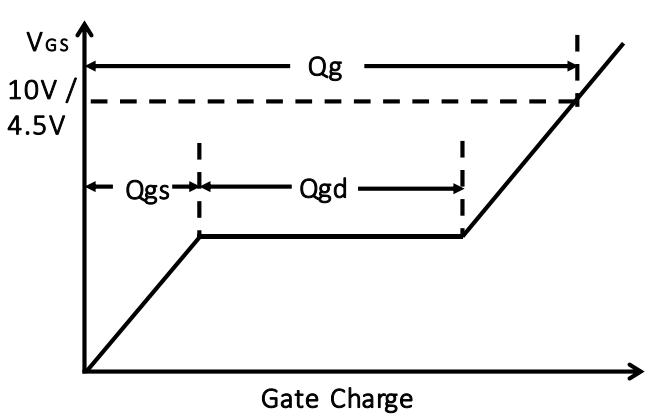
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	5.3	A
			---	---	21.2	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

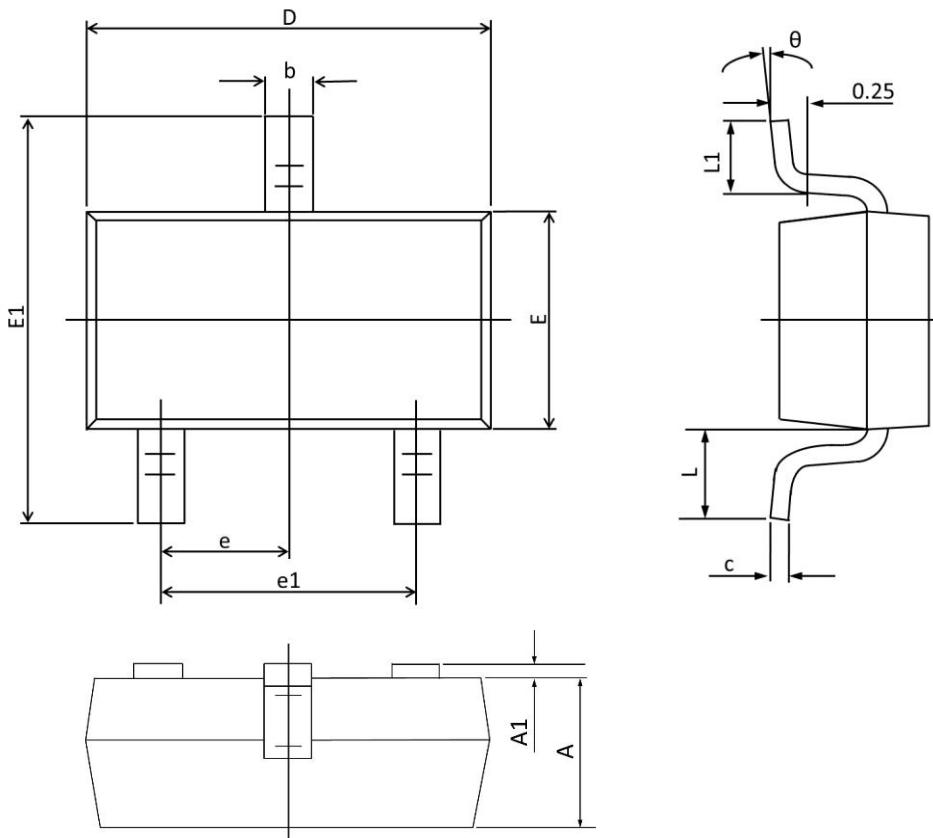
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Typical Output Characteristics

Fig.2 Continuous Drain Current vs. T_c

Fig.3 Normalized RDSON vs. T_j

Fig.4 Normalized V_{th} vs. T_j

Fig.5 Turn-On Resistance vs. V_{GS}

Fig.6 Turn-On Resistance vs. I_D


Fig.7 Capacitance Characteristics

Fig.8 Gate Charge Characteristics

Fig.9 Normalized Transient Impedance

Fig.10 Maximum Safe Operation Area

Fig.11 Switching Time Waveform

Fig.12 Gate Charge Waveform

SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.700	3.100	0.106	0.122
E	1.100	1.500	0.043	0.059
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
θ	1°	10°	1°	10°