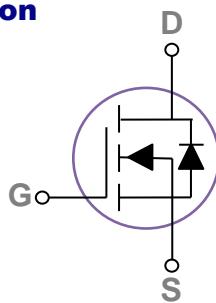


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
20V	25mΩ	5.8A

### Features

- 20V, 5.8A,  $RDS(ON) = 25m\Omega @ VGS = 4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for 1.8V Gate Drive Applications

### Applications

- Notebook
- Load Switch
- Hand-Held Instruments

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 10$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ C$ )	5.8	A
	Drain Current – Continuous ( $T_A=70^\circ C$ )	4.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	23.2	A
$P_D$	Power Dissipation ( $T_A=25^\circ C$ )	1.56	W
	Power Dissipation – Derate above $25^\circ C$	0.012	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	$^\circ C/W$

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.02	---	$\text{V}/\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=16\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=16\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=85\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 10\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=4.5\text{V}$ , $I_D=4\text{A}$	---	20	25	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$ , $I_D=3\text{A}$	---	27	35	
		$V_{GS}=1.8\text{V}$ , $I_D=2\text{A}$	---	39	55	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	0.4	0.6	0.8	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	-2	---	$\text{mV}/\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_S=3\text{A}$	---	6.5	---	S

**Dynamic and switching Characteristics**

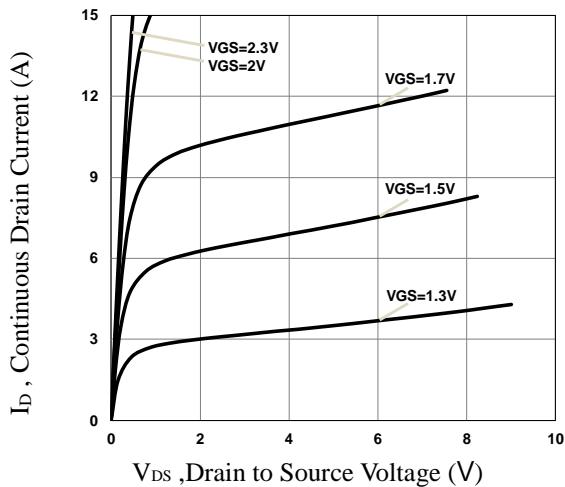
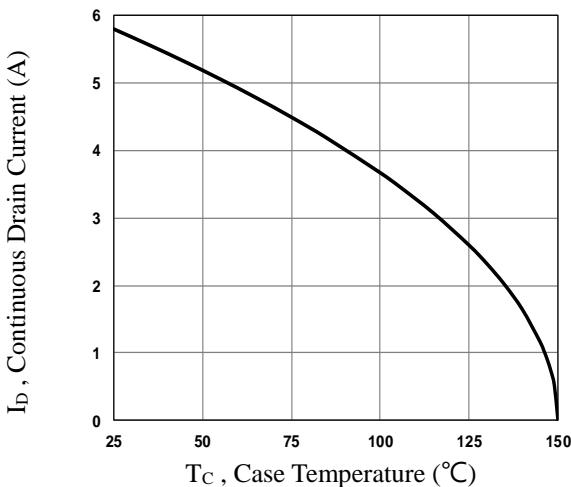
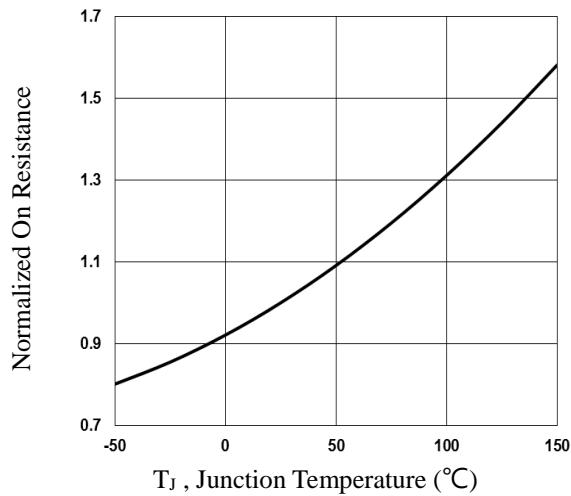
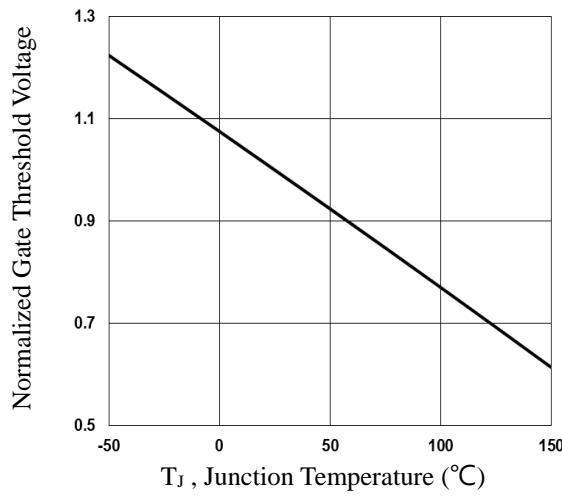
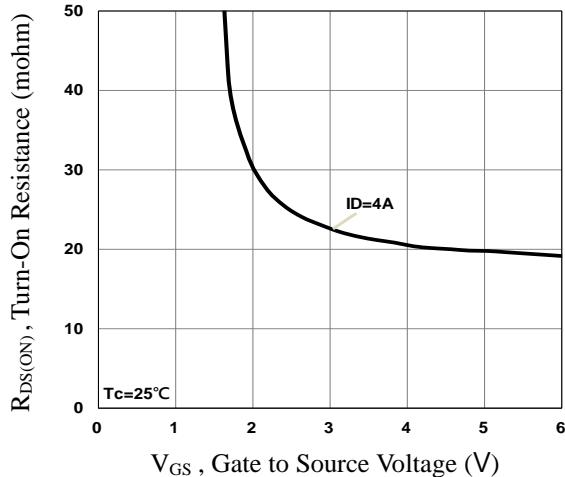
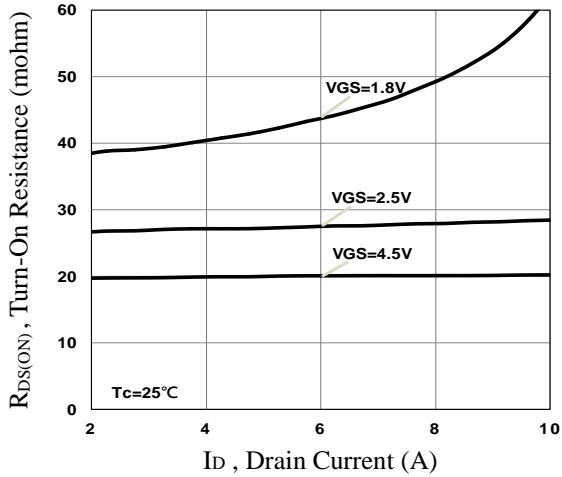
$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{DS}=10\text{V}$ , $V_{GS}=4.5\text{V}$ , $I_D=4\text{A}$	---	7.7	11	$\text{nC}$
$Q_{gs}$	Gate-Source Charge <sup>2, 3</sup>		---	0.9	1	
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>		---	2.4	5	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{DD}=10\text{V}$ , $V_{GS}=4.5\text{V}$ , $R_G=25\Omega$ $I_D=1\text{A}$	---	4.1	8	$\text{ns}$
$T_r$	Rise Time <sup>2, 3</sup>		---	11.6	22	
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>		---	23.9	45	
$T_f$	Fall Time <sup>2, 3</sup>		---	7.6	14	
$C_{iss}$	Input Capacitance	$V_{DS}=10\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	535	775	$\text{pF}$
$C_{oss}$	Output Capacitance		---	60	85	
$C_{rss}$	Reverse Transfer Capacitance		---	34	50	

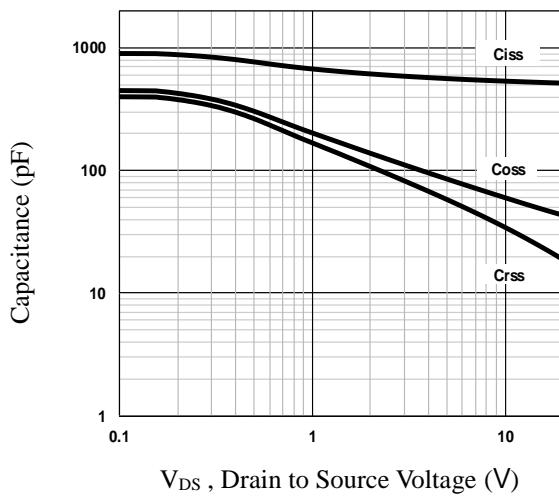
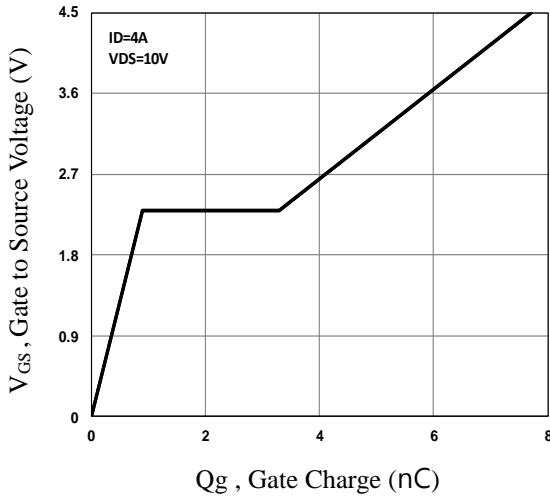
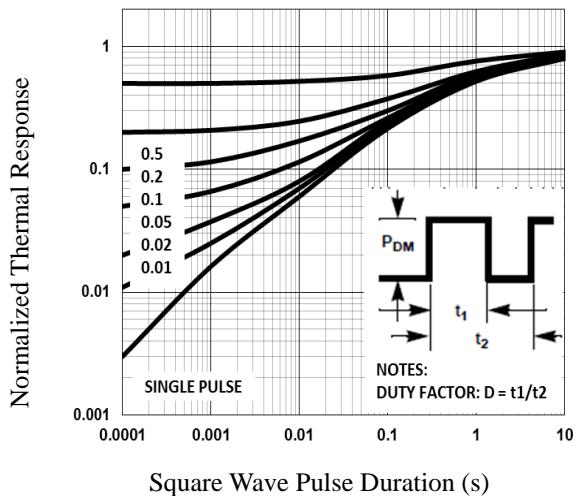
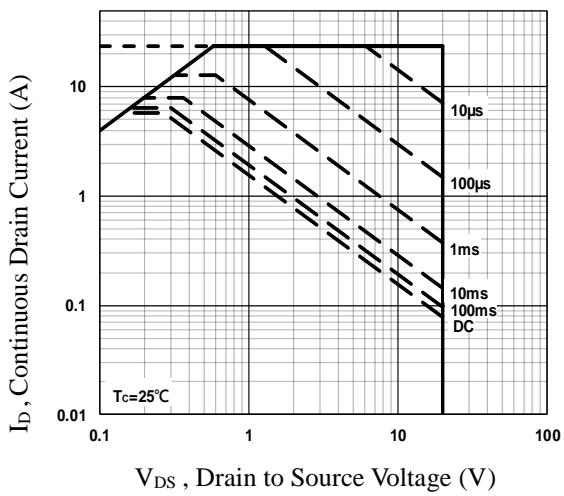
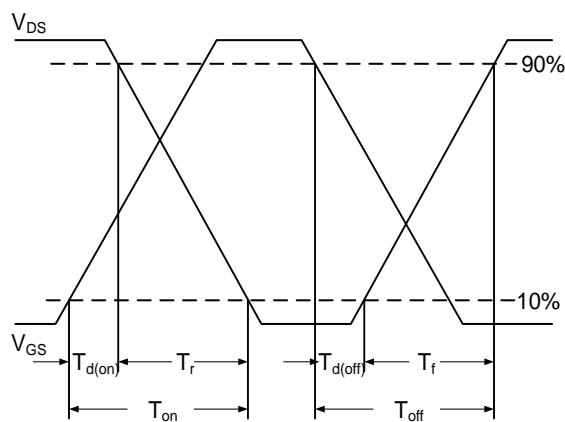
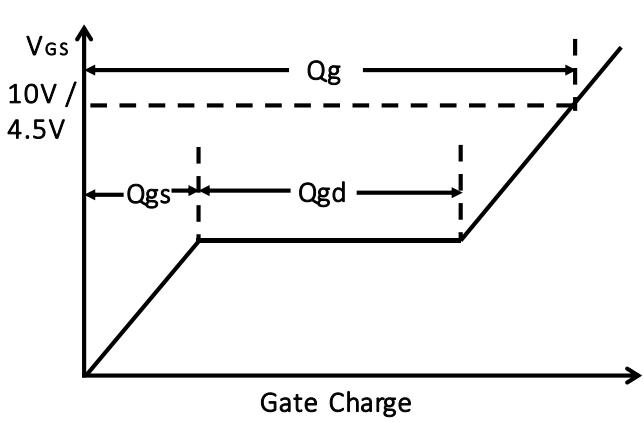
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	5.8	A
$I_{SM}$	Pulsed Source Current		---	---	11.6	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

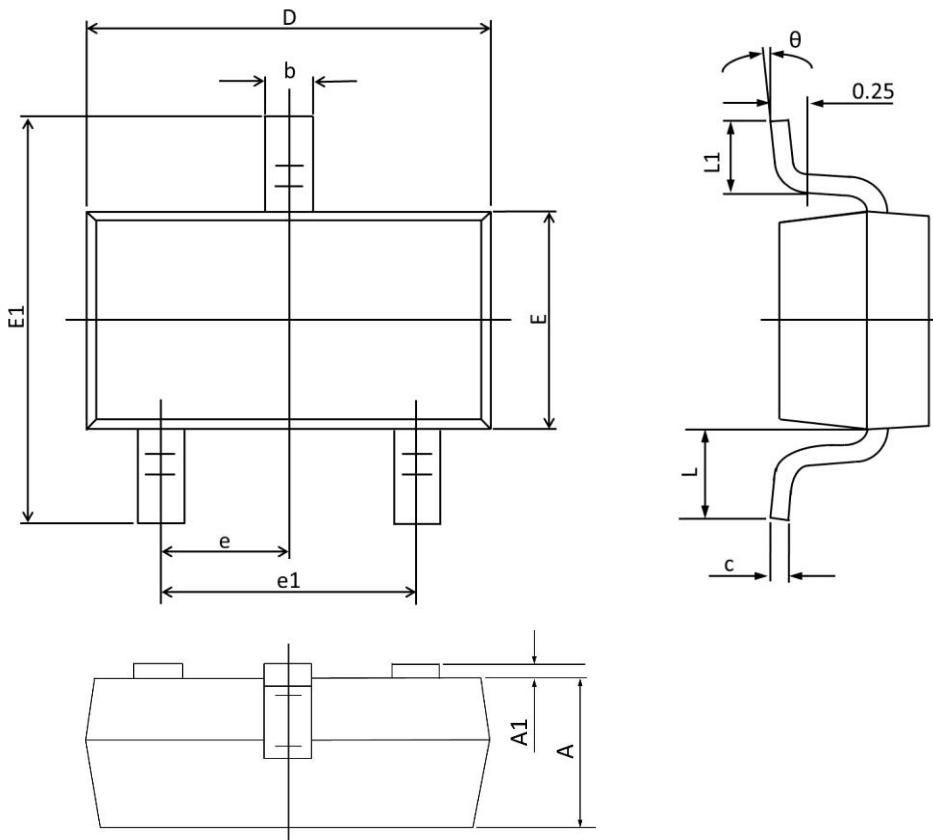
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Typical Output Characteristics**

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized  $R_{DSON}$  vs.  $T_j$** 

**Fig.4 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.5 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.6 Turn-On Resistance vs.  $I_D$**


**Fig.7 Capacitance Characteristics**

**Fig.8 Gate Charge Characteristics**

**Fig.9 Normalized Transient Impedance**

**Fig.10 Maximum Safe Operation Area**

**Fig.11 Switching Time Waveform**

**Fig.12 Gate Charge Waveform**

## SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.700	3.100	0.106	0.122
E	1.100	1.500	0.043	0.059
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
theta	1°	10°	1°	10°