

### General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
-20V	65mΩ	-4.1A

### Features

- -20V, -4.1A, RDS(ON) = 65mΩ@VGS = -4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for -1.8V Gate Drive Applications

### Applications

- Notebook
- Load Switch
- Hand-Held Instruments

### Absolute Maximum Ratings T<sub>c</sub>=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-20	V
V <sub>GS</sub>	Gate-Source Voltage	±10	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>A</sub> =25°C)	-4.1	A
	Drain Current – Continuous (T <sub>A</sub> =70°C)	-3.3	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	-16.4	A
P <sub>D</sub>	Power Dissipation (T <sub>A</sub> =25°C)	1.56	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	80	°C/W

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^\circ\text{C}$ , $I_D=-1\text{mA}$	---	-0.02	---	$\text{V}/\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-20\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^\circ\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{DS}=-16\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^\circ\text{C}$	---	---	-10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 10\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-4.5\text{V}$ , $I_D=-3\text{A}$	---	52	65	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}$ , $I_D=-2\text{A}$	---	73	85	
		$V_{GS}=-1.8\text{V}$ , $I_D=-1.5\text{A}$	---	105	130	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250\mu\text{A}$	-0.4	-0.6	-0.8	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	2	---	$\text{mV}/\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=-10\text{V}$ , $I_S=-3\text{A}$	---	5.5	---	S

**Dynamic and switching Characteristics**

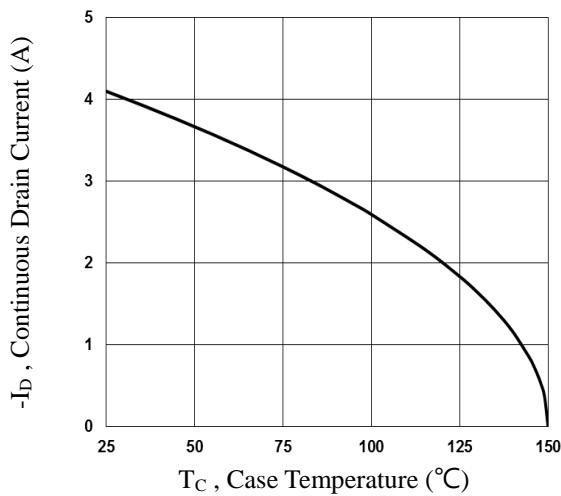
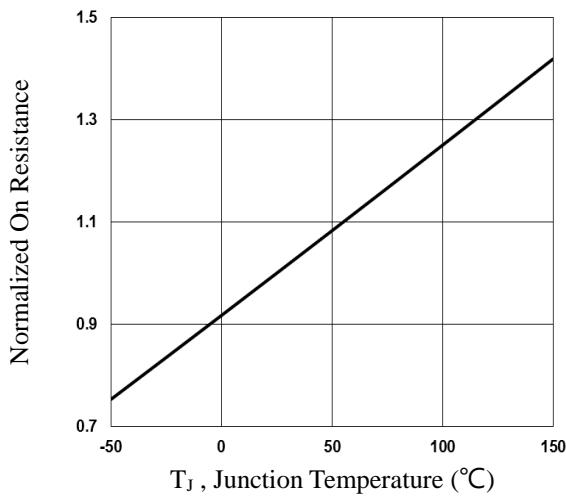
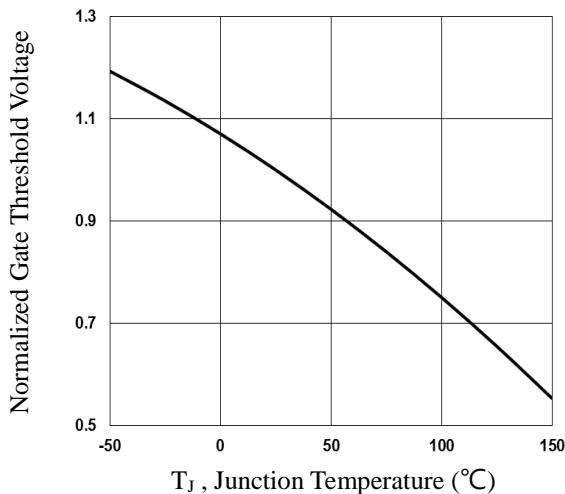
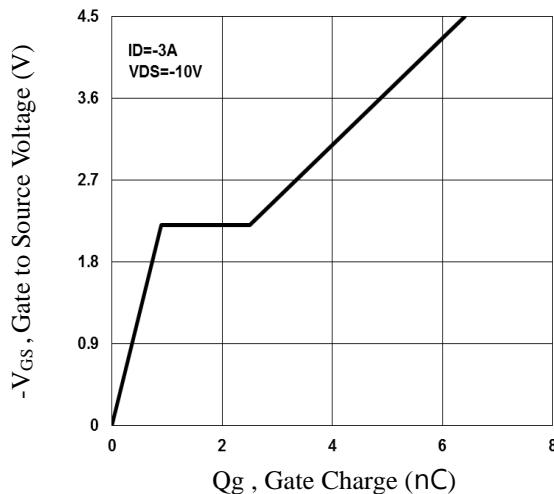
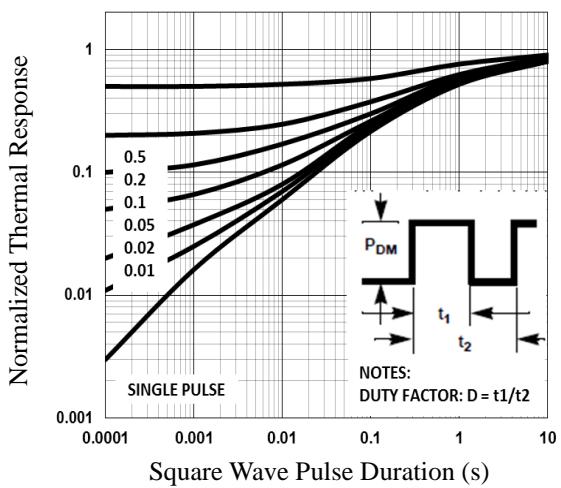
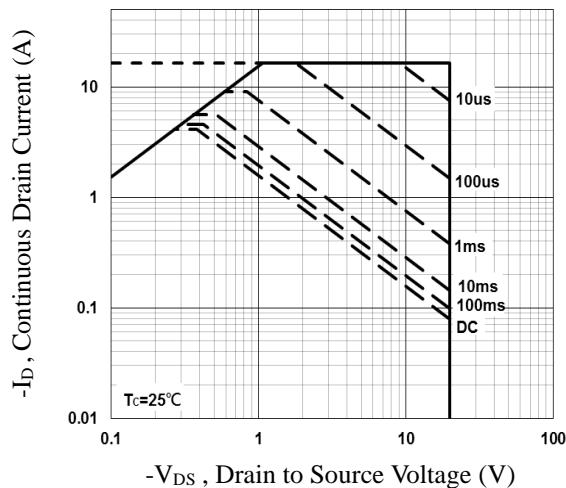
$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{DS}=-10\text{V}$ , $V_{GS}=-4.5\text{V}$ , $I_D=-3\text{A}$	---	6.4	9	nC
$Q_{gs}$	Gate-Source Charge <sup>2,3</sup>		---	0.9	1	
$Q_{gd}$	Gate-Drain Charge <sup>2,3</sup>		---	1.6	3	
$T_{d(on)}$	Turn-On Delay Time <sup>2,3</sup>	$V_{DD}=-10\text{V}$ , $V_{GS}=-4.5\text{V}$ , $R_G=25\Omega$	---	5	9	nS
$T_r$	Rise Time <sup>2,3</sup>		---	17.4	33	
$T_{d(off)}$	Turn-Off Delay Time <sup>2,3</sup>		---	40.7	80	
$T_f$	Fall Time <sup>2,3</sup>		---	11.4	23	
$C_{iss}$	Input Capacitance	$V_{DS}=-10\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	515	745	pF
$C_{oss}$	Output Capacitance		---	55	80	
$C_{rss}$	Reverse Transfer Capacitance		---	20	30	

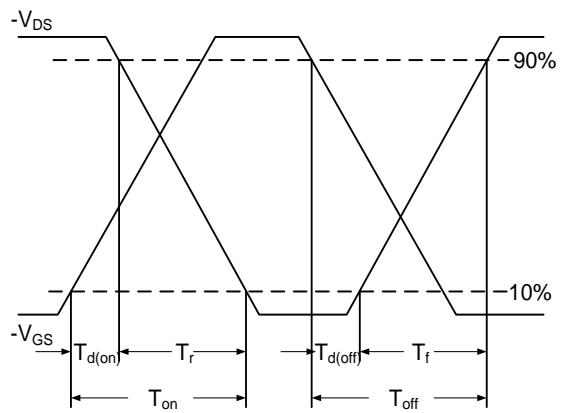
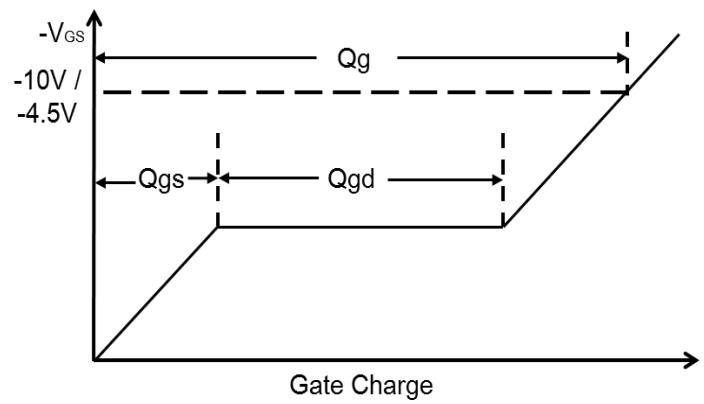
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-4.1	A
$I_{SM}$	Pulsed Source Current		---	---	-8.2	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S=-1\text{A}$ , $T_J=25\text{ }^\circ\text{C}$	---	---	-1	V

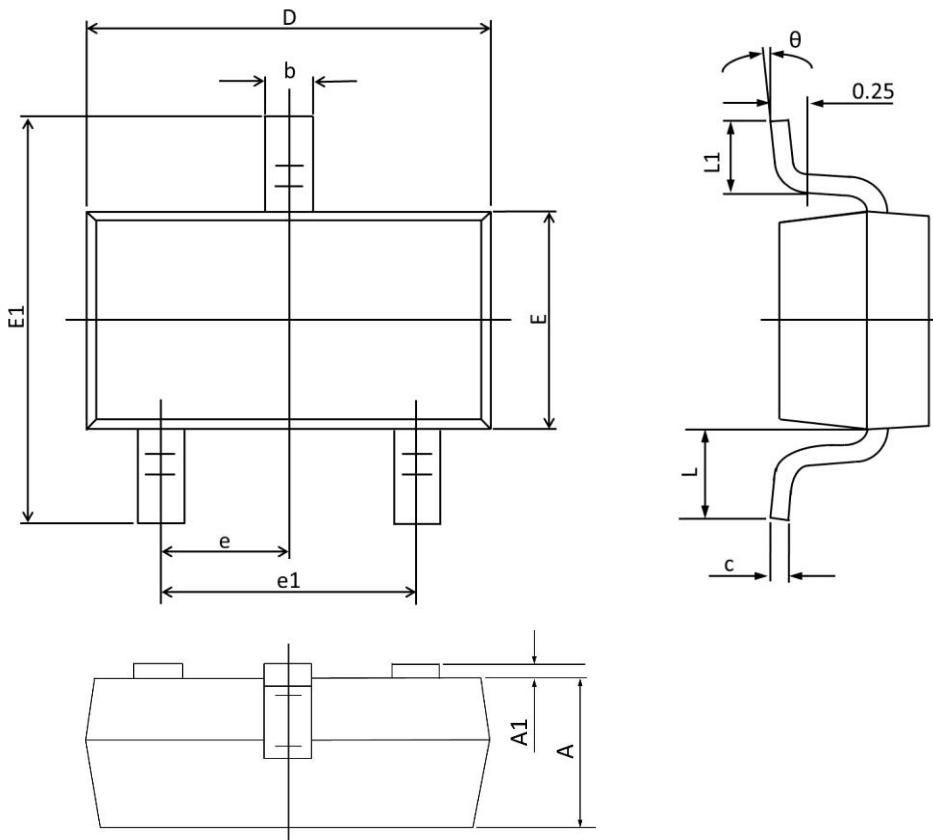
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. TC**

**Fig.2 Normalized RDSON vs. TJ**

**Fig.3 Normalized V<sub>th</sub> vs. TJ**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

## SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.110	0.035	0.044
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.800	3.040	0.110	0.120
E	1.200	1.400	0.047	0.055
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
theta	1°	10°	1°	10°