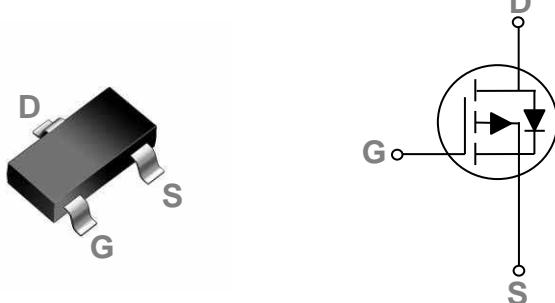


General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-3 Pin Configuration



BVDSS	RDS(ON)	ID
-20V	33mΩ	-5.8A

Features

- -20V, -5.8A, RDS(ON) = 33mΩ@VGS = -4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for -1.8V Gate Drive Applications

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 10	V
I_D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	-5.8	A
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	-4.6	A
I_{DM}	Drain Current – Pulsed ¹	-23.2	A
P_D	Power Dissipation ($T_A=25^\circ\text{C}$)	1.56	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	-20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$, $I_D=-1\text{mA}$	---	-0.02	---	V/C
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-20\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	μA
		$V_{DS}=-16\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 10\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-4.5\text{V}$, $I_D=-4\text{A}$	---	28	33	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}$, $I_D=-3\text{A}$	---	37	45	
		$V_{GS}=-1.8\text{V}$, $I_D=-2\text{A}$	---	49	65	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu\text{A}$	-0.3	-0.6	-1	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	2	---	mV/C
g_{fs}	Forward Transconductance	$V_{DS}=-10\text{V}$, $I_S=-3\text{A}$	---	8.4	---	S

Dynamic and switching Characteristics

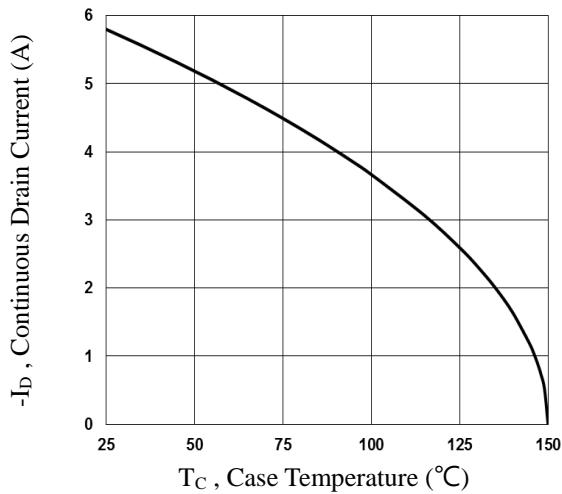
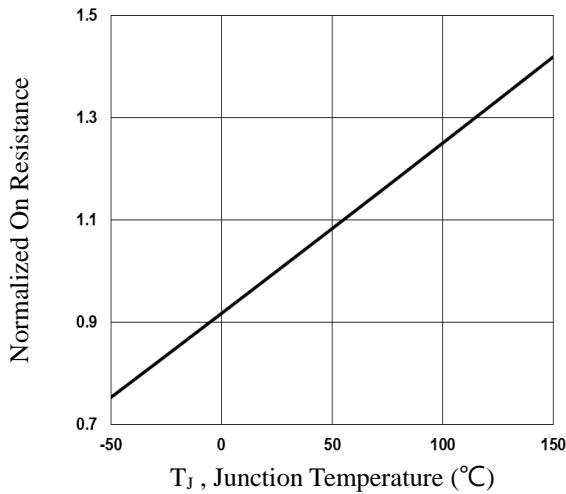
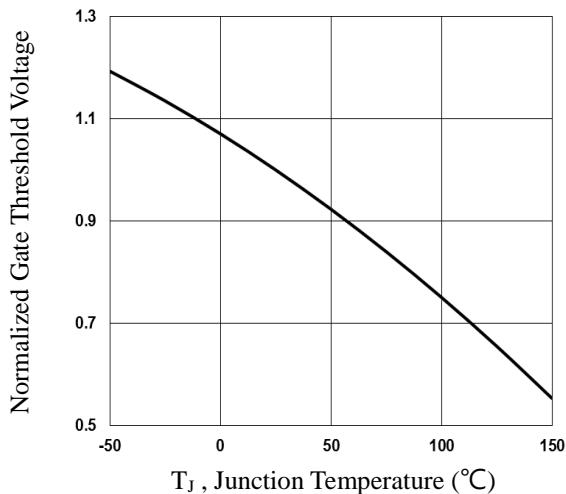
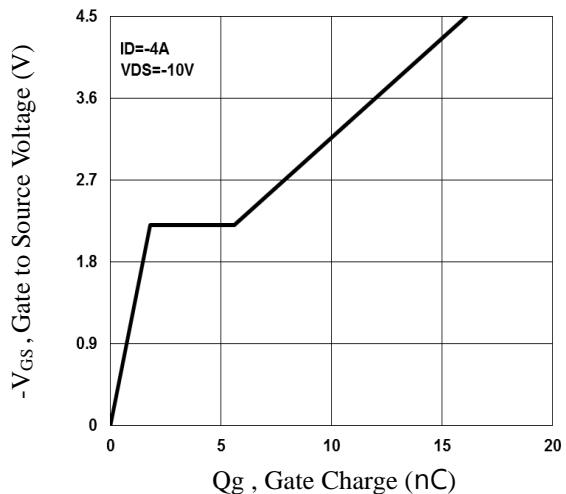
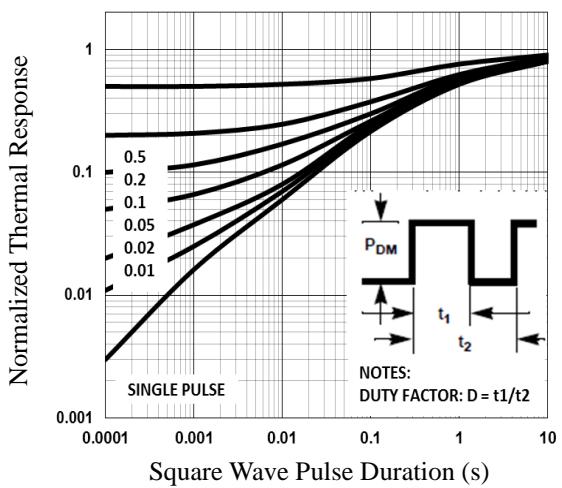
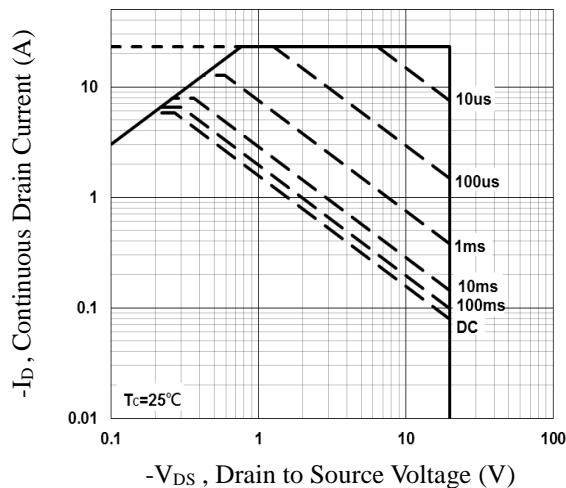
Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-4\text{A}$	---	16.1	25	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	1.8	3	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	3.8	7	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $R_G=25\Omega$	---	8.2	16	nS
T_r	Rise Time ^{2, 3}		---	30	57	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	71.1	135	
T_f	Fall Time ^{2, 3}		---	19.8	38	
C_{iss}	Input Capacitance	$V_{DS}=-15\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	1440	2100	pF
C_{oss}	Output Capacitance		---	155	230	
C_{rss}	Reverse Transfer Capacitance		---	115	170	

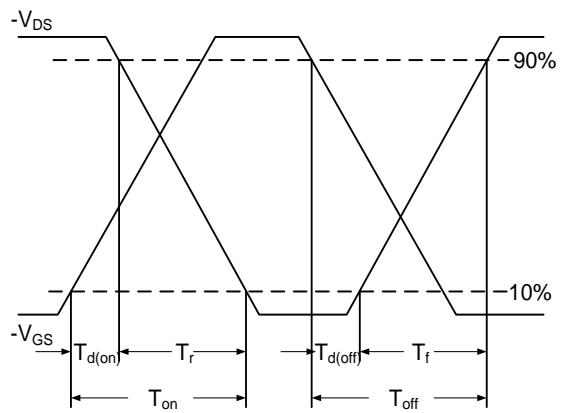
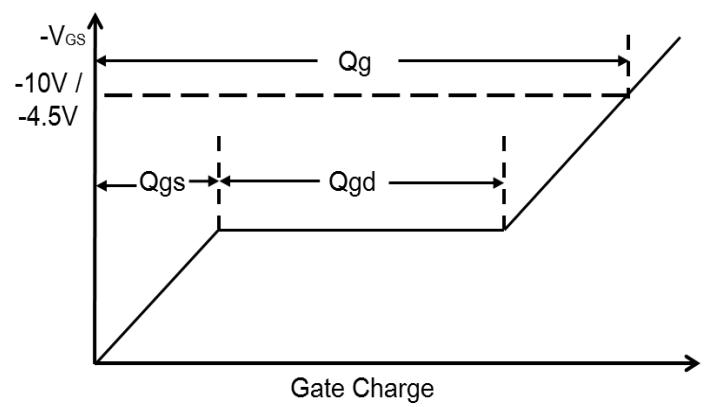
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-5.8	A
I_{SM}	Pulsed Source Current		---	---	-23.2	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_S=-1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	V

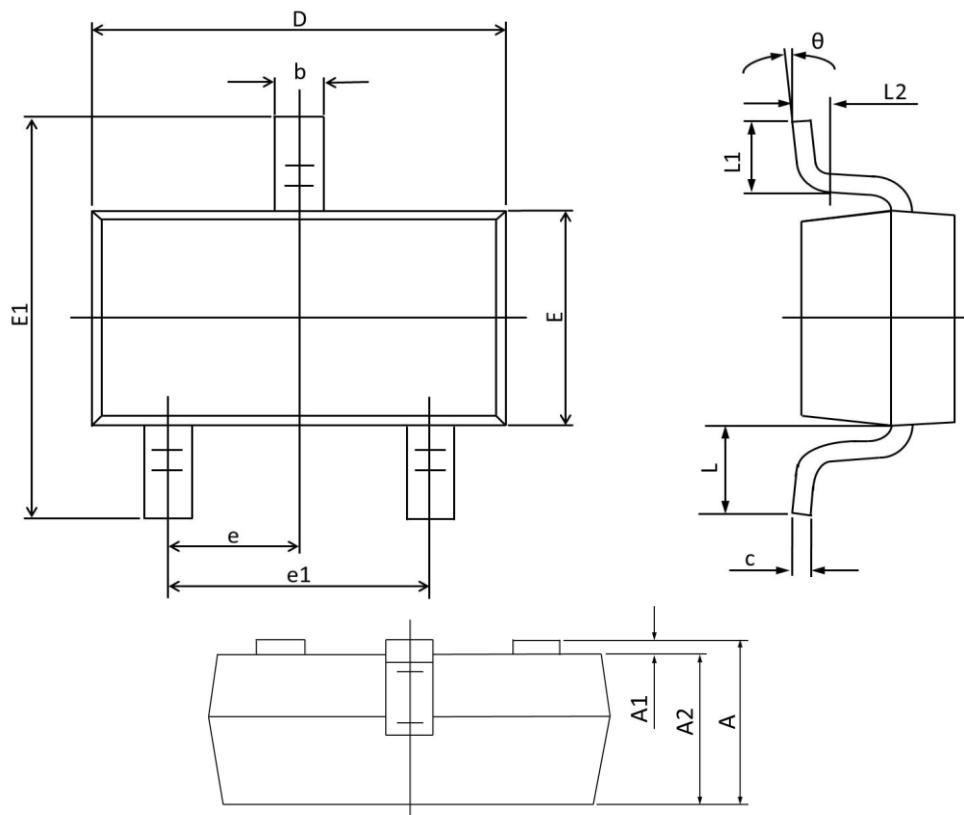
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. TC

Fig.2 Normalized RDSON vs. TJ

Fig.3 Normalized V_{th} vs. TJ

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

SOT23-3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.450	---	0.057	---
A1	0.100	0.000	0.004	0.000
A2	1.3	0.900	0.051	0.035
b	0.500	0.300	0.020	0.012
c	0.150	0.080	0.006	0.003
D	3.050	2.850	0.120	0.112
E	1.750	1.550	0.069	0.061
E1	3.000	2.600	0.118	0.102
e	0.95 TYP.		0.037 TYP.	
e1	2.000	1.800	0.079	0.071
L	0.59 REF.		0.022 REF.	
L1	0.600	0.350	0.024	0.014
L2	0.25 TYP.		0.01 TYP.	
θ	12°	0°	12°	0°