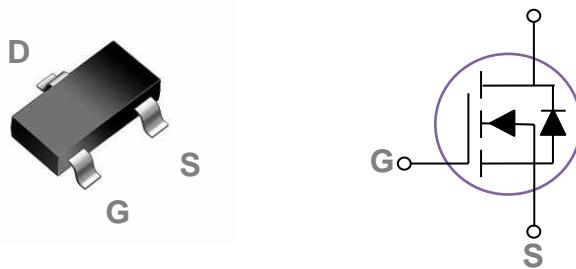


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
100V	200mΩ	2A

### Features

- 100V,2A,  $RDS(ON) = 200m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

### Applications

- Networking
- Load Switch
- LED applications

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ C$ )	2	A
	Drain Current – Continuous ( $T_A=70^\circ C$ )	1.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	8	A
$P_D$	Power Dissipation ( $T_A=25^\circ C$ )	1.56	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	°C/W

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	100	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.10	---	$\text{V}/\text{ }^{\circ}\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=100\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=80\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$ , $I_D=2\text{A}$	---	161	200	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=1\text{A}$	---	169	210	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D = 250\mu\text{A}$	1.2	1.8	2.5	V
			---	-4	---	$\text{mV}/\text{ }^{\circ}\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=1\text{A}$	---	5	---	S

**Dynamic and switching Characteristics**

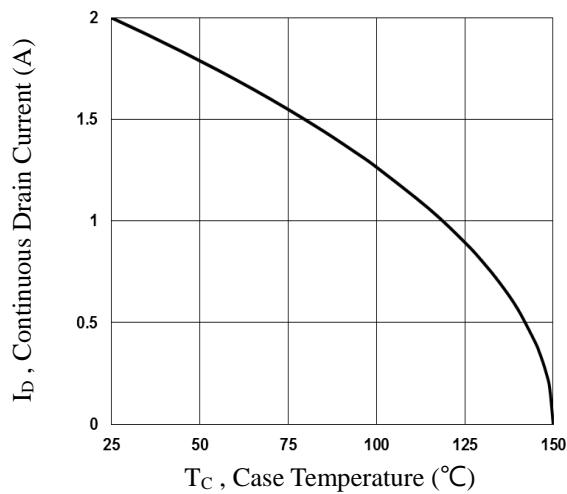
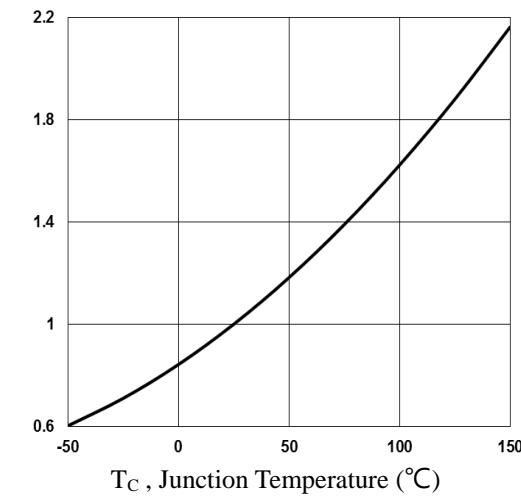
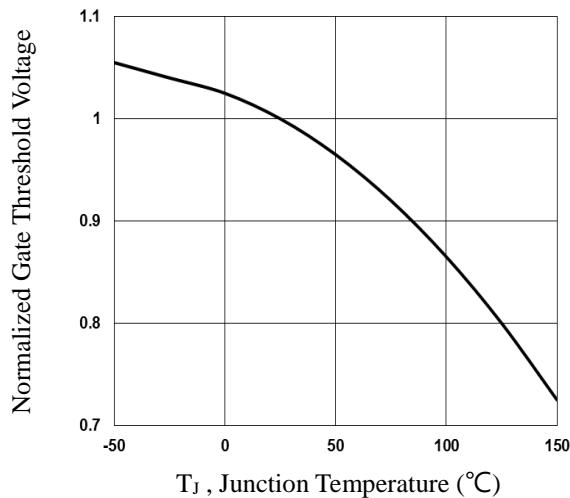
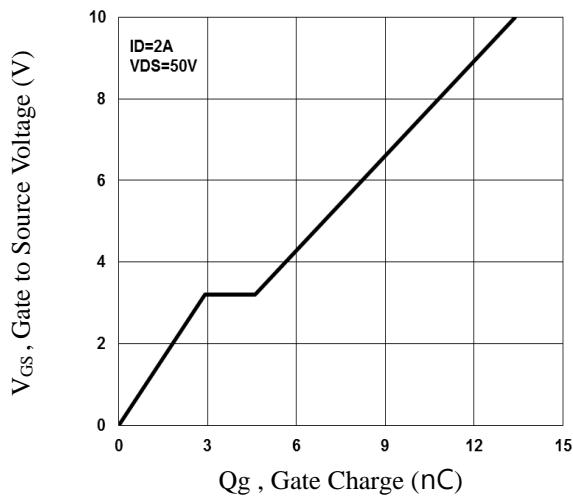
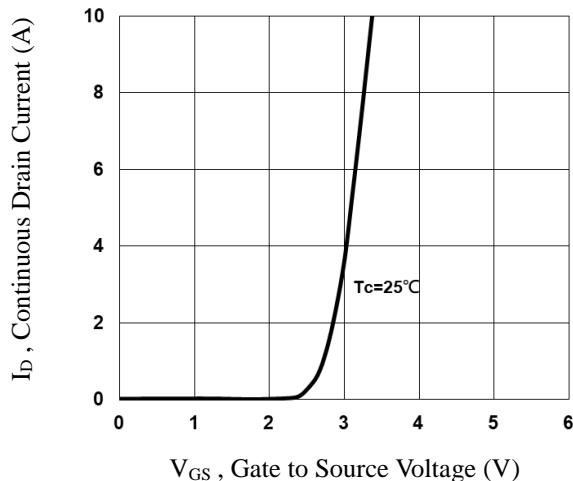
$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{DS}=50\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=2\text{A}$	---	13.4	21	nC
$Q_{gs}$	Gate-Source Charge <sup>2, 3</sup>		---	2.9	6	
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>		---	1.7	4	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{DD}=30\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=3.3\Omega$	---	1.6	3	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	6.6	13	
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>		---	11.5	22	
$T_f$	Fall Time <sup>2, 3</sup>		---	3.6	7	
$C_{iss}$	Input Capacitance	$V_{DS}=50\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	820	1190	pF
$C_{oss}$	Output Capacitance		---	35	55	
$C_{rss}$	Reverse Transfer Capacitance		---	20	30	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	1.3	2.6	$\Omega$

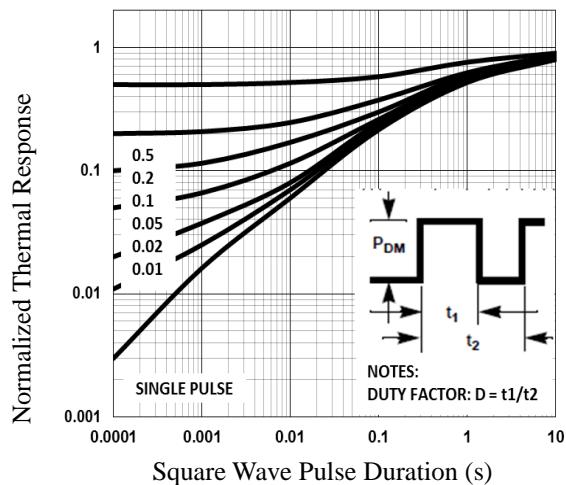
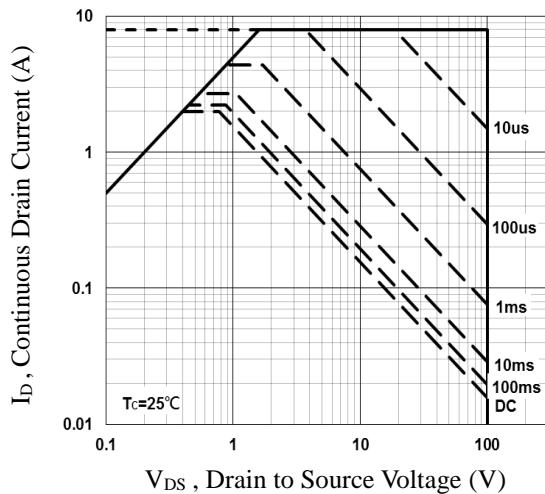
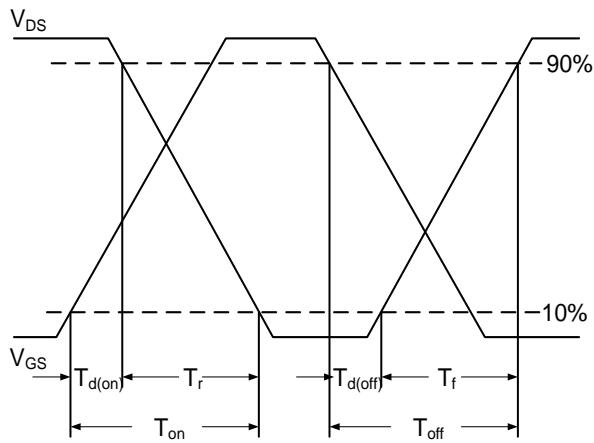
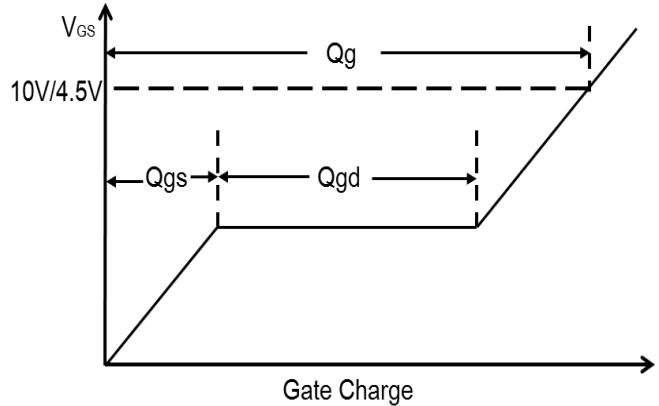
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	2	A
			---	---	8	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

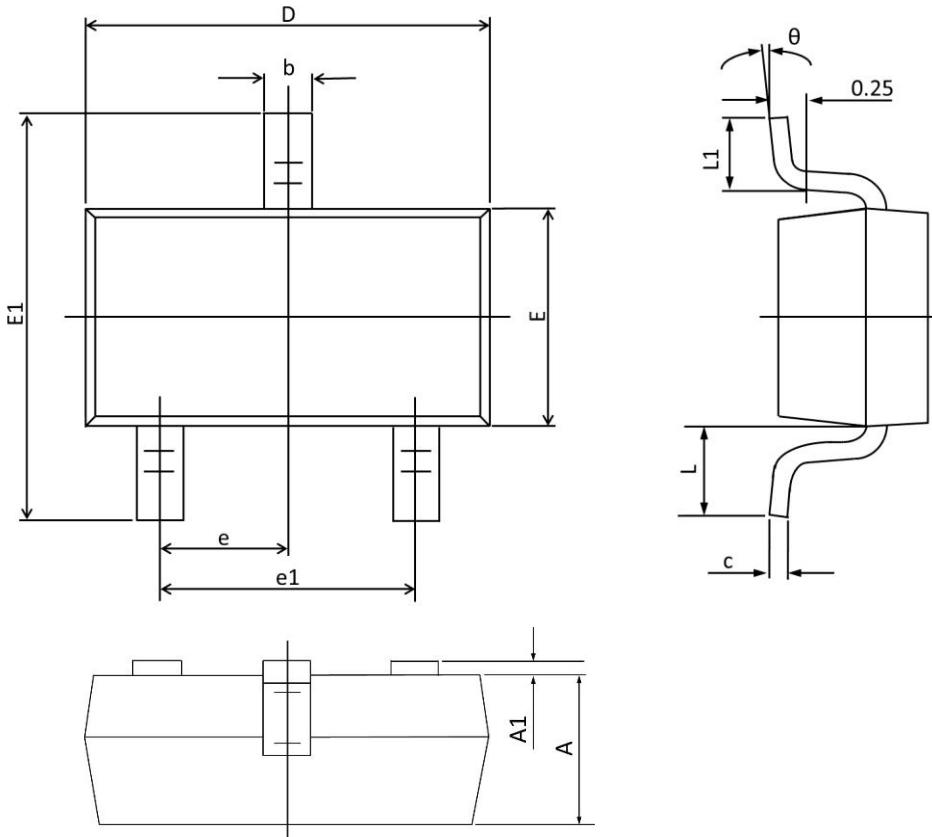
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_c$** 

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Transfer Characteristics**


**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**

**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

## SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.700	3.100	0.106	0.122
E	1.100	1.500	0.043	0.059
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
θ	1°	10°	1°	10°