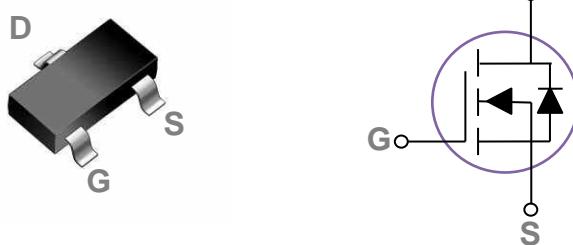


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT23-3 Pin Configuration



BVDSS	RDS(ON)	ID
150V	480mΩ	1.2A

### Features

- 150V, 1.2A, RDS(ON) = 480mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed

### Applications

- Networking
- Load Switch
- LED applications

### Absolute Maximum Ratings T<sub>c</sub>=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	150	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>A</sub> =25°C)	1.2	A
	Drain Current – Continuous (T <sub>A</sub> =70°C)	0.96	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	4.8	A
P <sub>D</sub>	Power Dissipation (T <sub>A</sub> =25°C)	1.56	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	80	°C/W



150V N-Channel MOSFETs

**PDN02N15**

### Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

#### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	150	---	---	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=150\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=120\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

#### On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=1\text{A}$	---	380	480	$\text{m}\Omega$
		$V_{\text{GS}}=6\text{V}$ , $I_{\text{D}}=0.5\text{A}$	---	410	520	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_{\text{D}}=250\mu\text{A}$	2	3	4	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_{\text{D}}=1\text{A}$	---	1.7	---	S

#### Dynamic and switching Characteristics

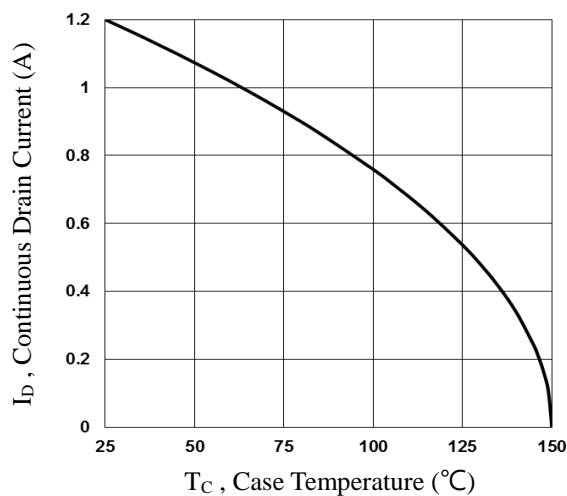
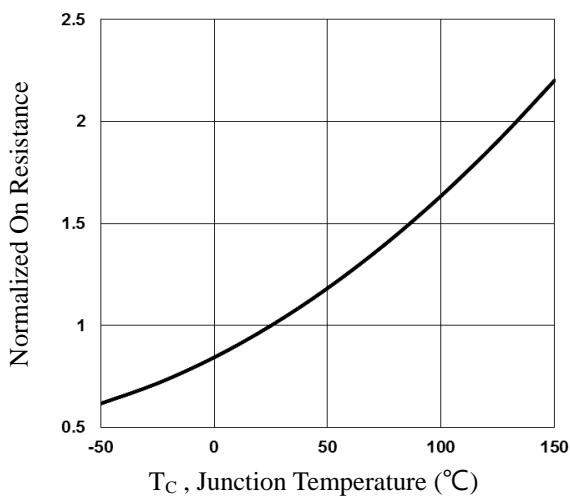
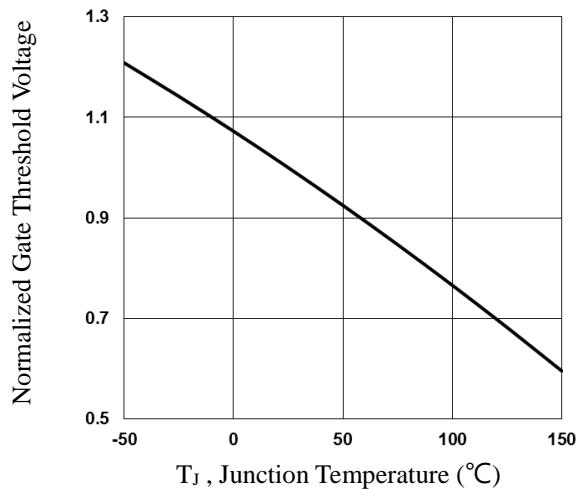
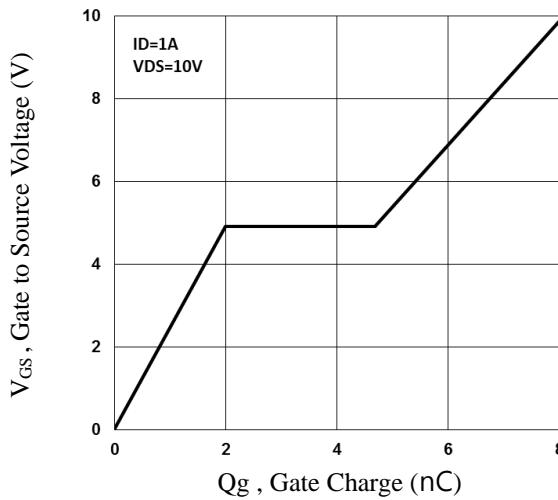
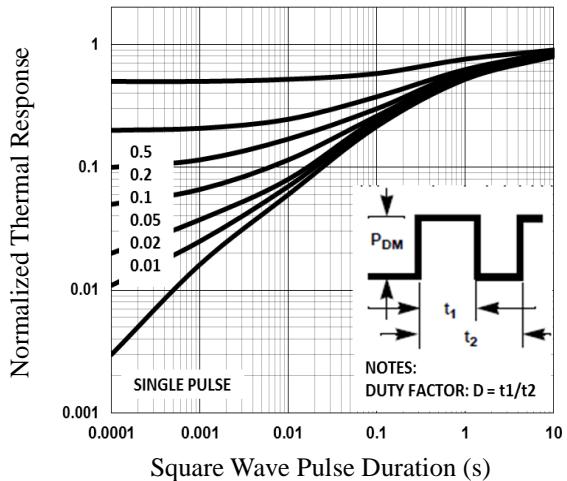
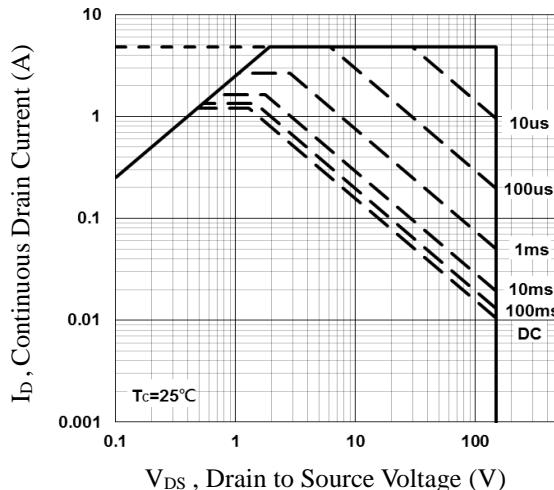
$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=1\text{A}$	---	8.1	16	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2, 3</sup>		---	2	4	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2, 3</sup>		---	2.7	5.4	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{\text{DD}}=75\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_{\text{G}}=10\Omega$ $I_{\text{D}}=1\text{A}$	---	8.2	16	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	5.8	12	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>2, 3</sup>		---	14.8	28	
$T_f$	Fall Time <sup>2, 3</sup>		---	8	16	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	350	700	pF
$C_{\text{oss}}$	Output Capacitance		---	34	68	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	26	52	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	2	---	$\Omega$

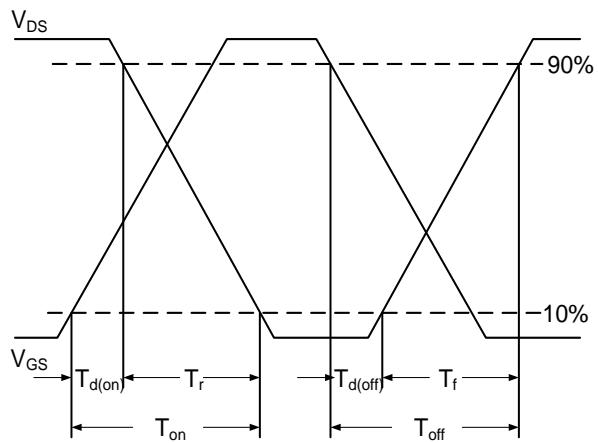
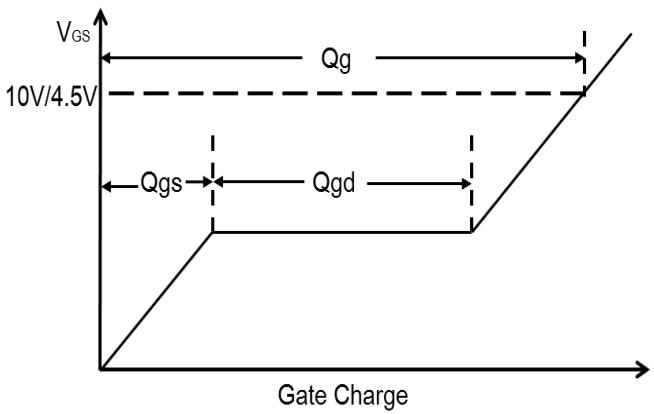
#### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	1.2	A
			---	---	2.4	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $\text{di}/\text{dt}=100\text{A}/\mu\text{s}$	---	43	---	ns
$Q_{\text{rr}}$	Reverse Recovery Charge	$T_J=25^\circ\text{C}$	---	37	---	nC

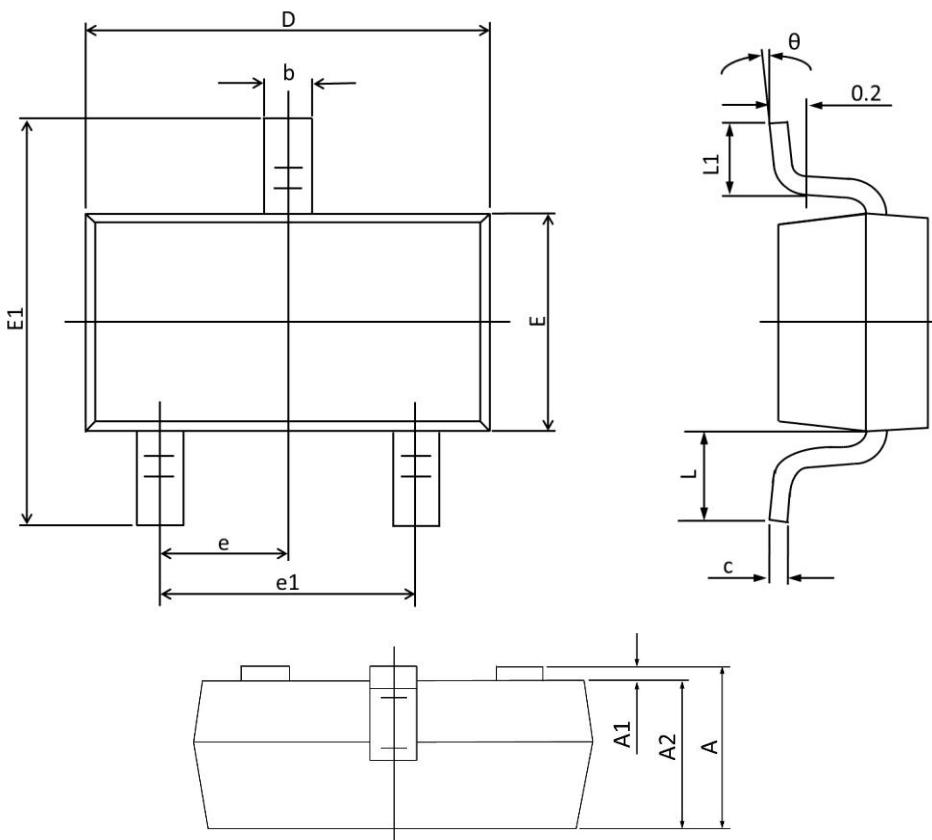
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**

**Fig.2 Continuous Drain Current vs. T<sub>c</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

## SOT23-3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.450	0.041	0.057
A1	---	0.150	---	0.006
A2	0.900	1.300	0.035	0.051
b	0.300	0.490	0.012	0.019
c	0.100	0.200	0.004	0.008
D	2.820	3.050	0.111	0.120
E	1.500	1.750	0.059	0.069
E1	2.600	3.000	0.102	0.118
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.700 REF.		0.028 REF.	
L1	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°