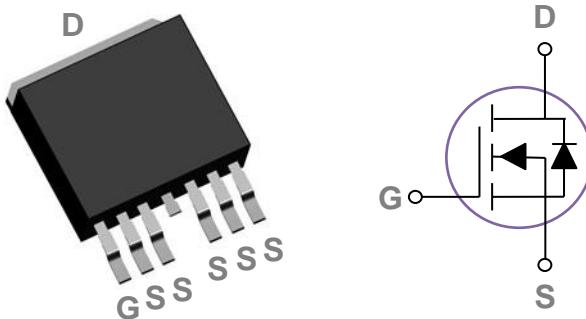


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO263-6L Pin Configuration



BVDSS	RDS(ON)	ID
150V	4.6mΩ	160A

### Features

- 150V, 160A, RDS(ON) = 4.6mΩ@VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	150	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	160	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	102	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	640	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	1800	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	60	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	305	W
	Power Dissipation – Derate above 25°C	2.44	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	55	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.41	°C/W



150V N-Channel MOSFETs

PDH150N15BH-S

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	150	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=120\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=120\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=85\text{ }^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$ , $I_D=30\text{A}$	---	3.8	4.6	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D = 250\mu\text{A}$	2.5	3.5	4.5	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=3\text{A}$	---	15	---	S

**Dynamic and switching Characteristics<sup>3</sup>**

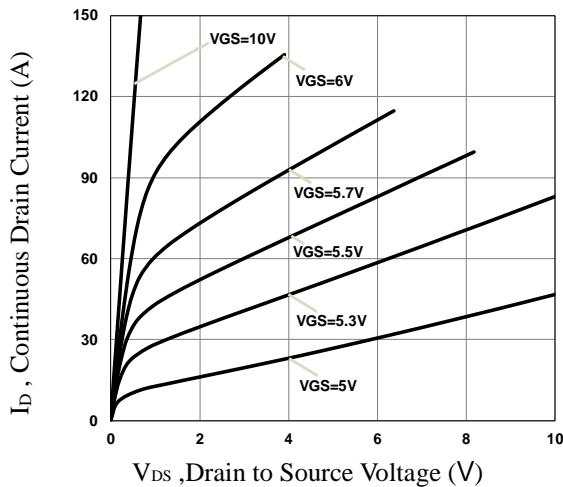
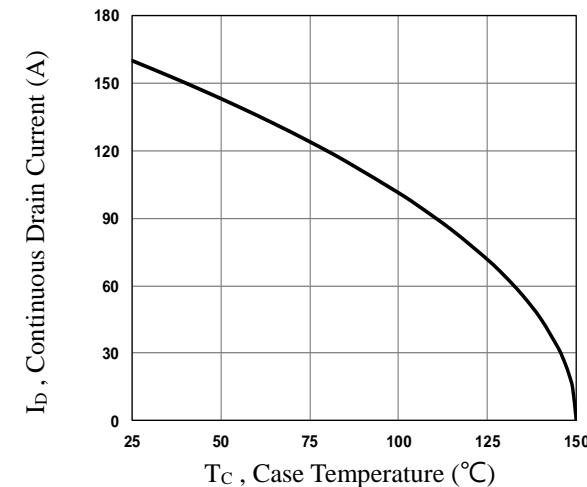
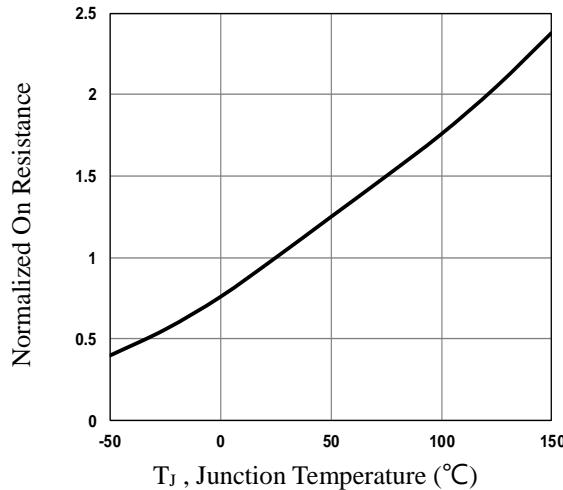
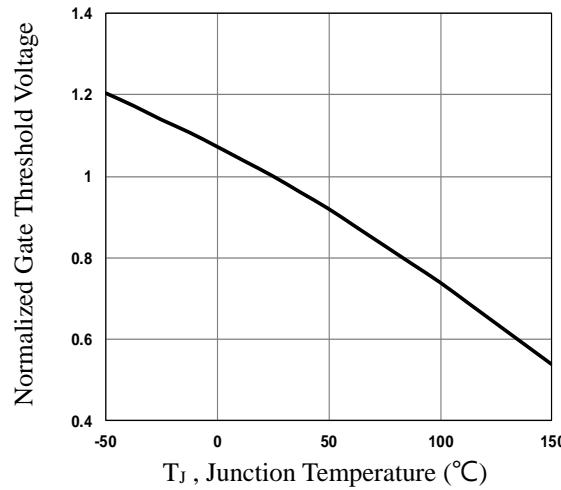
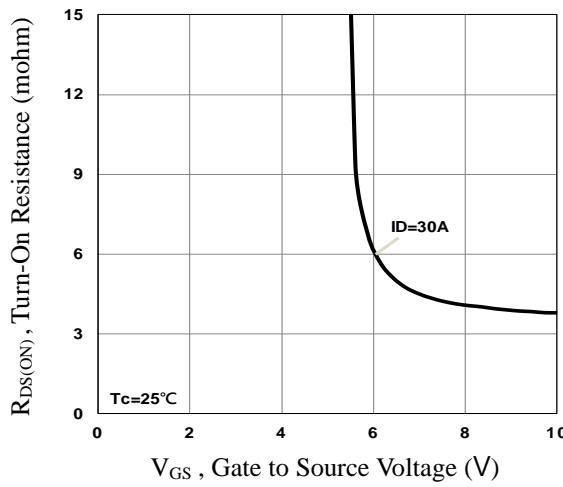
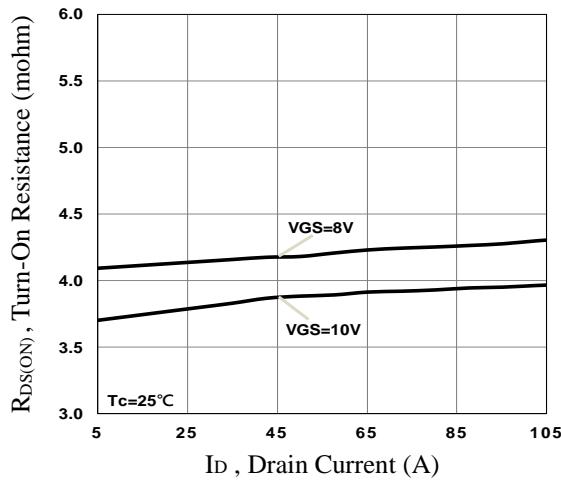
$Q_g$	Total Gate Charge	$V_{DS}=75\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=80\text{A}$	---	120	180	nC
$Q_{gs}$	Gate-Source Charge		---	31	45	
$Q_{gd}$	Gate-Drain Charge		---	34	50	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=75\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$ $I_D=80\text{A}$	---	30	45	ns
$T_r$	Rise Time		---	35	55	
$T_{d(off)}$	Turn-Off Delay Time		---	35	55	
$T_f$	Fall Time		---	45	70	
$C_{iss}$	Input Capacitance	$V_{DS}=75\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	7330	11000	pF
$C_{oss}$	Output Capacitance		---	680	1050	
$C_{rss}$	Reverse Transfer Capacitance		---	18	30	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	2.7	---	$\Omega$

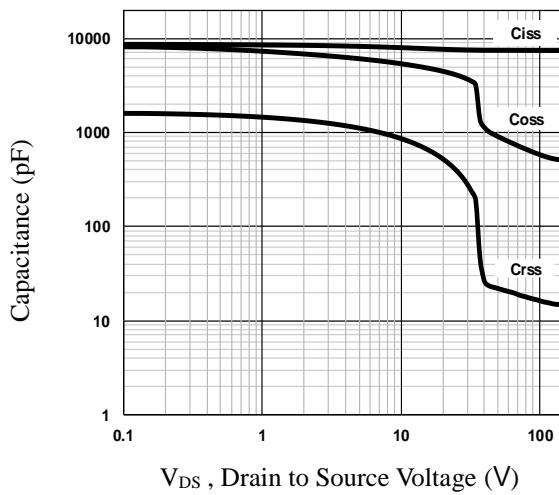
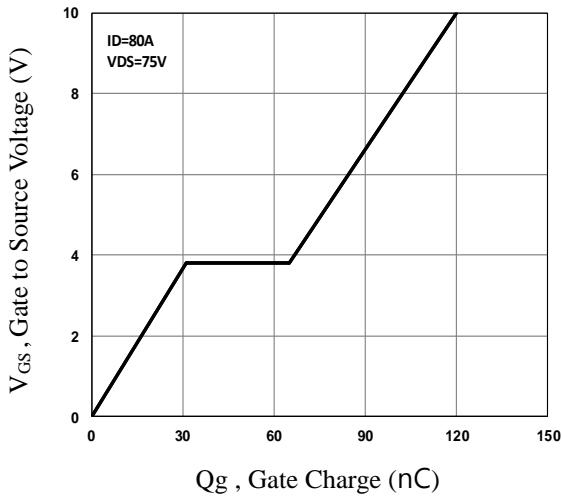
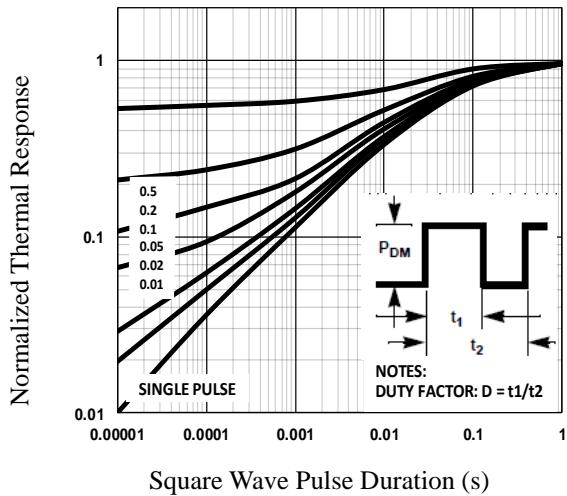
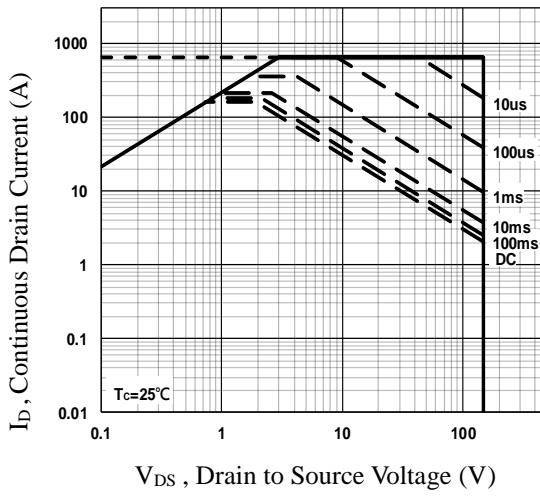
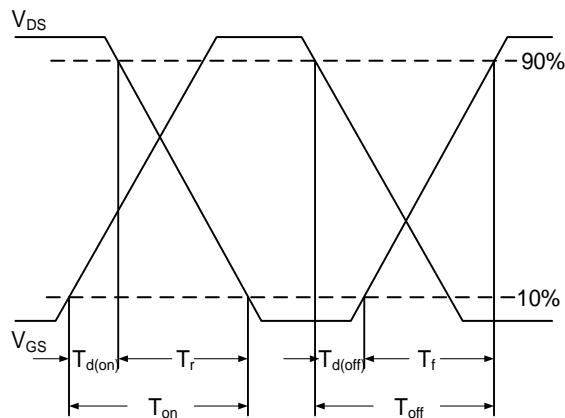
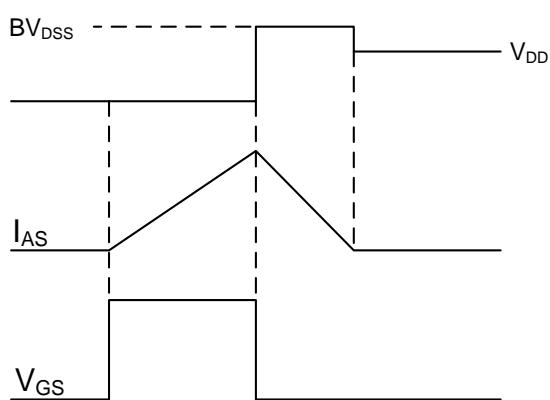
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	160	A
	Pulsed Source Current		---	---	320	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^\circ\text{C}$	---	---	1	V
$t_{rr}$	Reverse Recovery Time	$V_R=100\text{V}$ , $I_s=20\text{A}$ $di/dt=100\text{A}/\mu\text{s}$ , $T_J=25\text{ }^\circ\text{C}$	---	110	---	ns
	Reverse Recovery Charge		---	520	---	nC

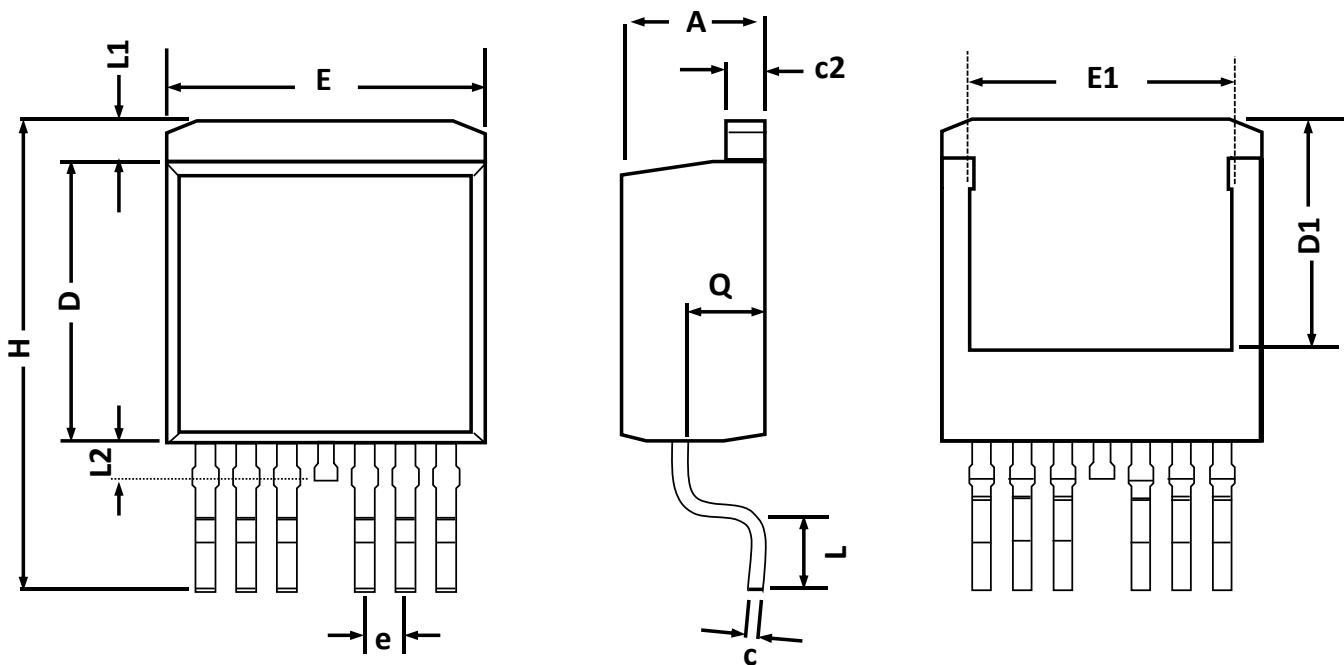
Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{DD}=50\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=1\text{mH}$ ,  $I_{AS}=60\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25\text{ }^\circ\text{C}$ .
- Essentially independent of operating temperature.


**Fig.1 Typical Output Characteristics**

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized  $R_{DSON}$  vs.  $T_j$** 

**Fig.4 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.5 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.6 Turn-On Resistance vs.  $I_D$**

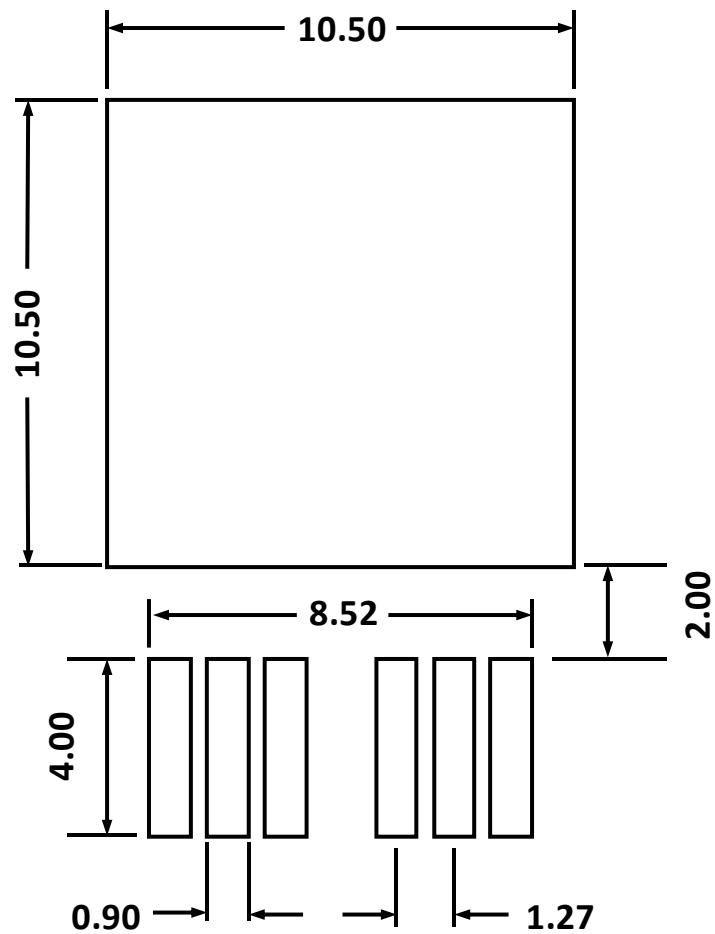

**Fig.7 Capacitance Characteristics**

**Fig.8 Gate Charge Characteristics**

**Fig.9 Normalized Transient Impedance**

**Fig.10 Maximum Safe Operation Area**

**Fig.11 Switching Time Waveform**

**Fig.12 EAS Waveform**

## TO263-6L PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Max	Min	Max	Min
A	4.650	4.200	0.183	0.165
b	0.700	0.500	0.028	0.020
c	0.600	0.400	0.024	0.016
c2	1.400	1.150	0.055	0.045
D	9.050	8.800	0.356	0.346
D1	---	6.850	---	0.270
E	10.400	9.950	0.409	0.392
E1	8.550	8.150	0.337	0.321
e	1.270 BSC		0.050 BSC	
H	15.900	14.600	0.626	0.575
L	2.800	1.750	0.110	0.069
L1	1.360 REF.		0.054 REF.	
L2	1.200 REF.		0.047 REF.	
Q	2.700	2.300	0.106	0.091

## TO263-6L RECOMMENDED LAND PATTERN



unit : mm