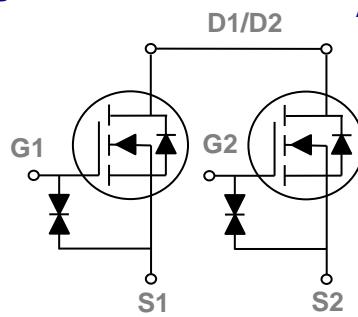
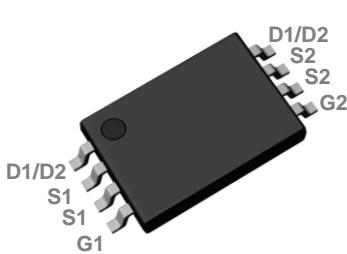


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TSSOP8 Dual Pin Configuration



BVDSS	RDS(ON)	ID
20V	26mΩ	7A

### Features

- 20V,7A, RDS(ON) 26mΩ @VGS = 4.5V
- G-S ESD Diode Embedded
- Improved dv/dt capability
- Fast switching
- Green Device Available

### Applications

- Notebook
- Load Switch
- LED applications

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ\text{C}$ )	7	A
	Drain Current – Continuous ( $T_A=70^\circ\text{C}$ )	5.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	28	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	2	W
	Power Dissipation – Derate above 25°C	16	mW/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62.5	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	20	---	---	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=20\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 12\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 10$	$\mu\text{A}$

**On Characteristics**

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=2.5\text{A}$	18	22	26	$\text{m}\Omega$
		$V_{\text{GS}}=4\text{V}$ , $I_{\text{D}}=2.5\text{A}$	19	23	28	$\text{m}\Omega$
		$V_{\text{GS}}=3.7\text{V}$ , $I_{\text{D}}=2.5\text{A}$	20	24	30	$\text{m}\Omega$
		$V_{\text{GS}}=3.1\text{V}$ , $I_{\text{D}}=2.5\text{A}$	21	25	32	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$ , $I_{\text{D}}=2.5\text{A}$	24	28	37	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_{\text{D}}=250\mu\text{A}$	0.5	0.7	1.5	V

**Dynamic and switching Characteristics**

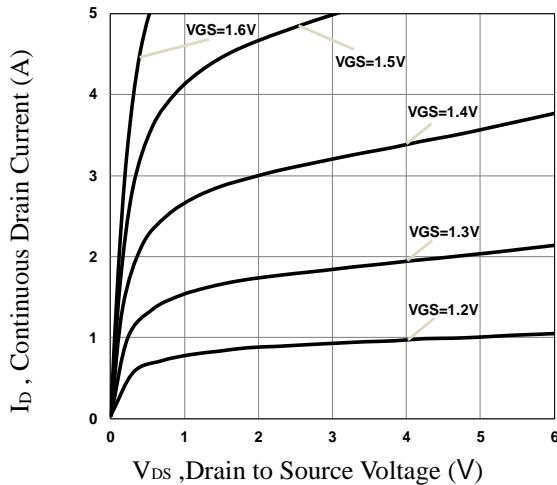
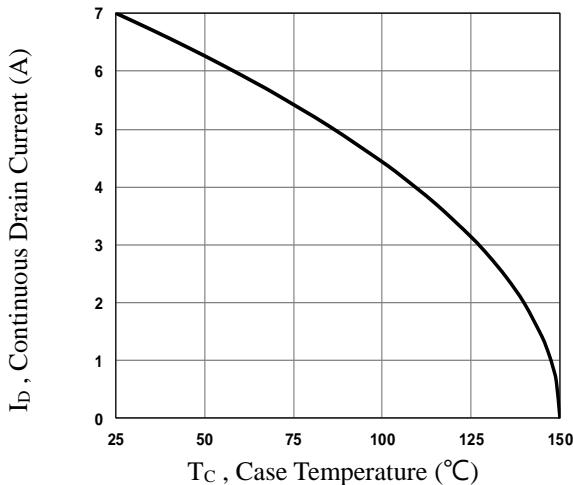
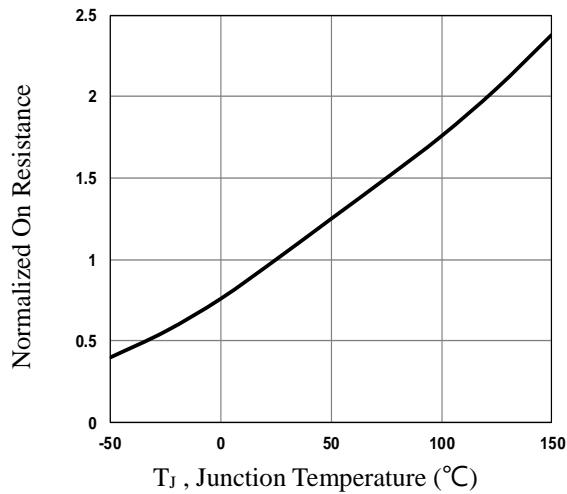
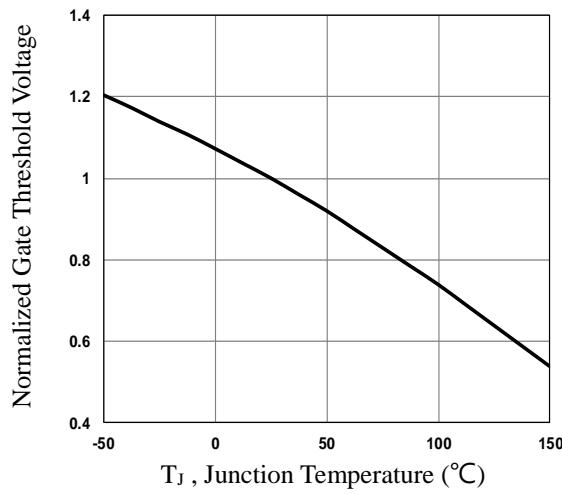
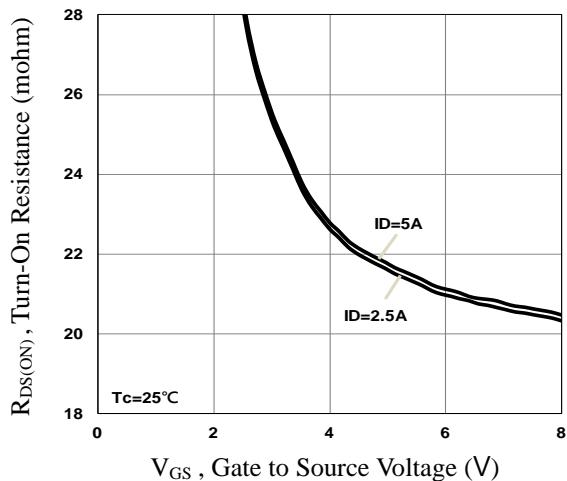
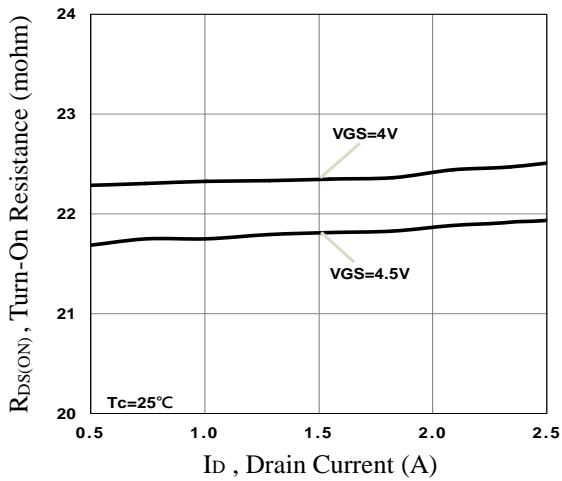
$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{\text{DS}}=10\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=3\text{A}$	---	9.2	15	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2, 3</sup>		---	0.4	2	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2, 3</sup>		---	1.9	3	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{\text{DD}}=10\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_{\text{G}}=6\Omega$ , $I_{\text{D}}=3\text{A}$	---	2.8	5	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	7.2	11	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>2, 3</sup>		---	15.8	25	
$T_f$	Fall Time <sup>2, 3</sup>		---	4.6	7	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=10\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	390	600	pF
$C_{\text{oss}}$	Output Capacitance		---	70	105	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	60	90	

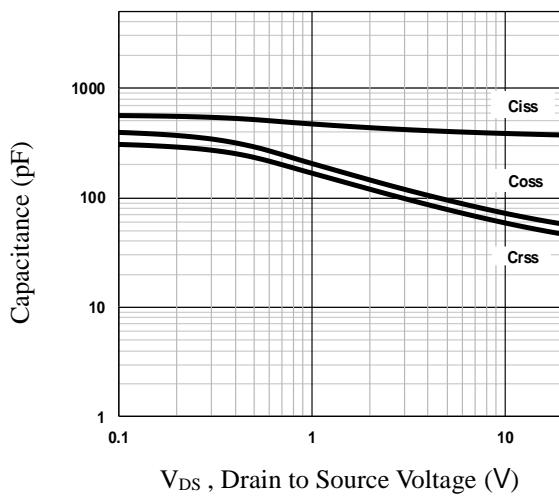
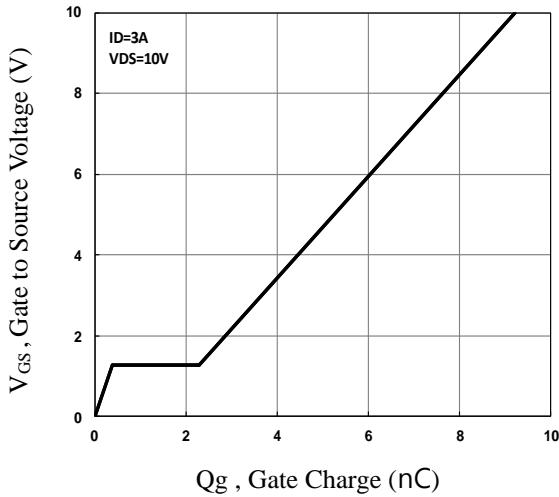
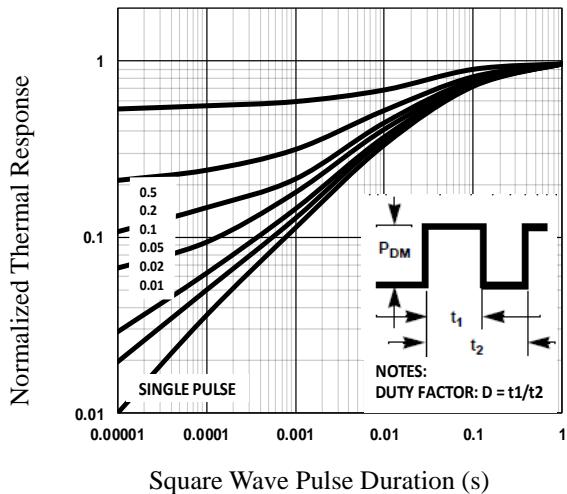
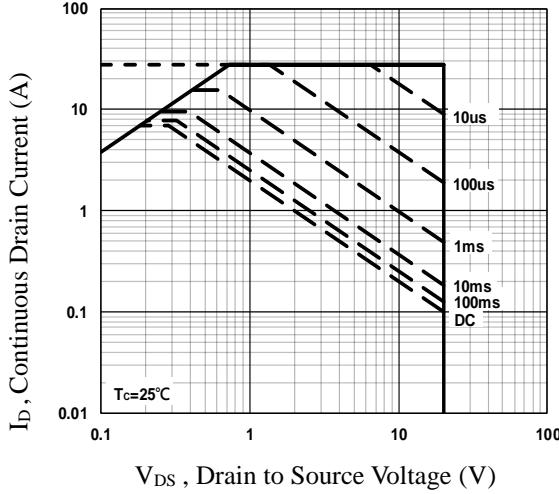
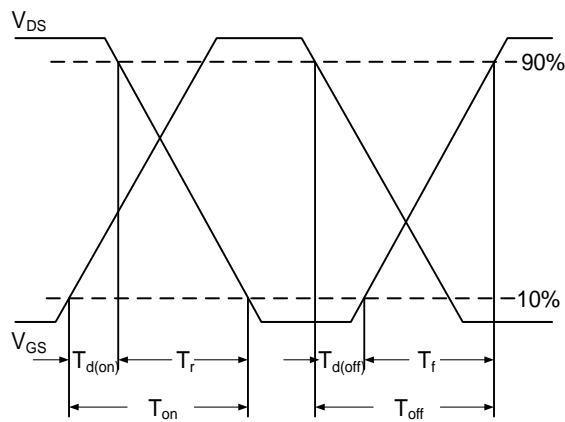
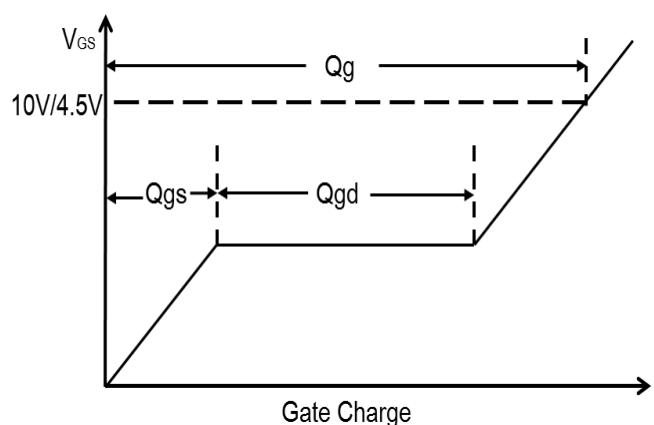
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	7	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	14	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V

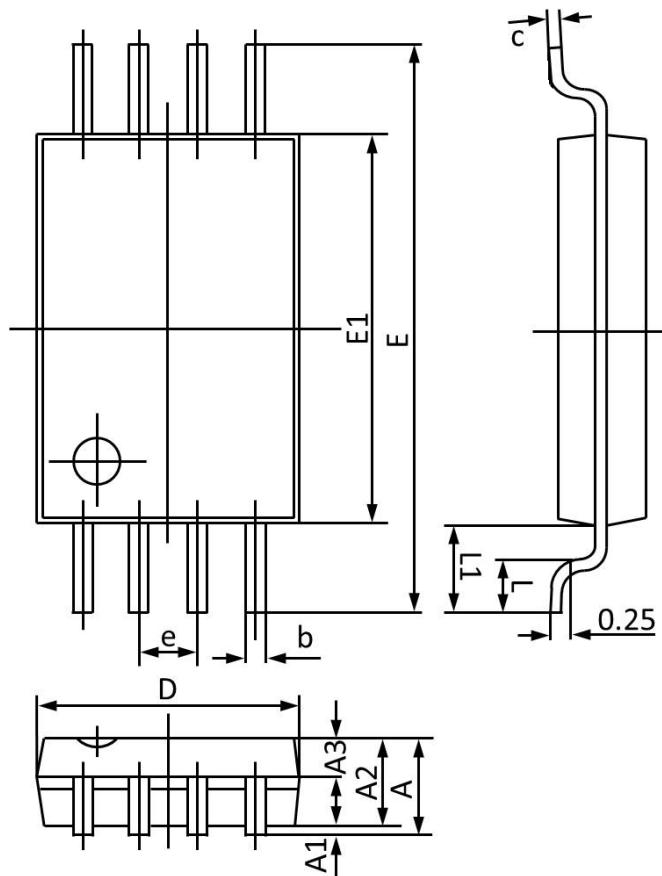
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Typical Output Characteristics**

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized RDSON vs.  $T_j$** 

**Fig.4 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.5 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.6 Turn-On Resistance vs.  $I_D$**


**Fig.7 Capacitance Characteristics**

**Fig.8 Gate Charge Characteristics**

**Fig.9 Normalized Transient Impedance**

**Fig.10 Maximum Safe Operation Area**

**Fig.11 Switching Time Waveform**

**Fig.12 Gate Charge Waveform**

## TSSOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.000	1.200	0.039	0.047
A1	0.020	0.180	0.001	0.007
A2	0.900	1.100	0.035	0.043
A3	0.390	0.490	0.015	0.019
b	0.170	0.300	0.007	0.012
c	0.120	0.190	0.005	0.008
D	2.850	3.100	0.112	0.122
E	6.200	6.600	0.244	0.260
E1	4.300	4.500	0.169	0.177
e	0.650(BSC)		0.026(BSC)	
L	0.400	0.800	0.016	0.031
L1	1.000(BSC)		0.039(BSC)	