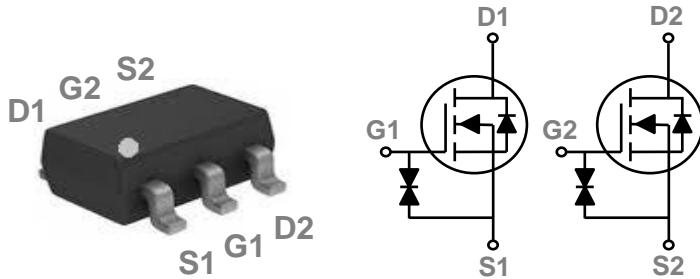


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT363 Dual Pin Configuration



BVDSS	RDS(ON)	ID
60V	3.5Ω	0.2A

Features

- 60V,0.2A, RDS(ON) =3.5Ω@VGS=10V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- G-S ESD Protection Diode Embedded

Applications

- Notebook
- Load Switch
- Networking
- Hand-held Instruments

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	0.2	A
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	0.16	A
I_{DM}	Drain Current – Pulsed ¹	0.8	A
P_D	Power Dissipation ($T_A=25^\circ\text{C}$)	278	mW
	Power Dissipation – Derate above 25°C	2.22	mW/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	450	°C/W



60V Dual N-Channel MOSFETs

PDEV68A8LZ

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=60\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=48\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	1	mA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 20	μA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$, $I_D=0.2\text{A}$	---	2.3	3.5	Ω
		$V_{GS}=4.5\text{V}$, $I_D=0.1\text{A}$	---	2.4	4	Ω
		$V_{GS}=2.5\text{V}$, $I_D=0.1\text{A}$	---	3.2	9	Ω
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	0.9	1.35	1.8	V

Dynamic and switching Characteristics

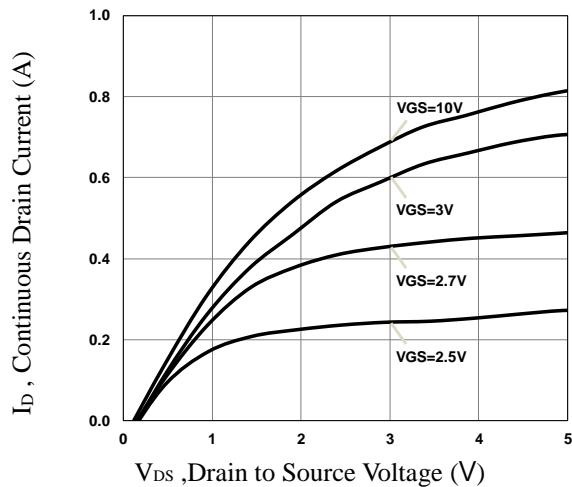
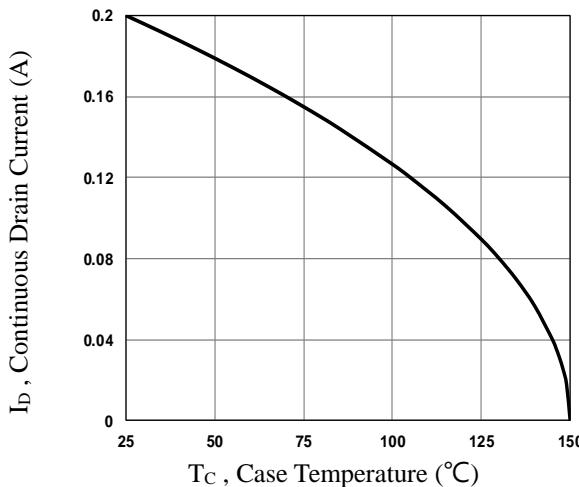
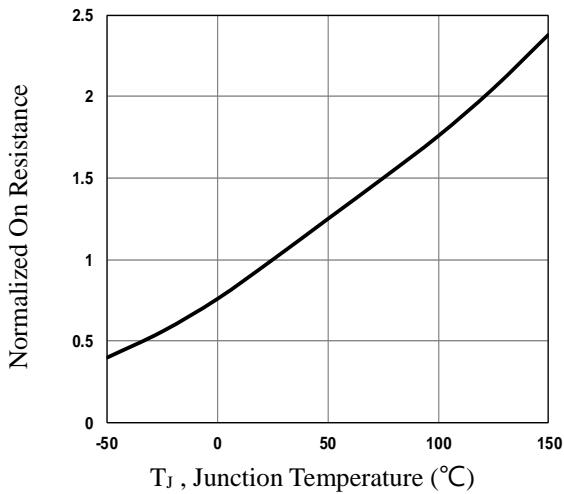
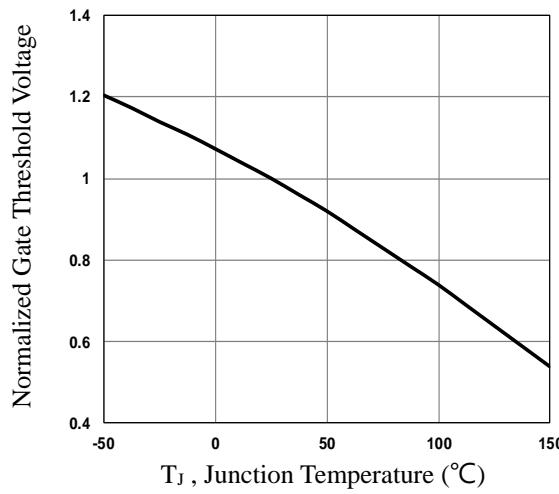
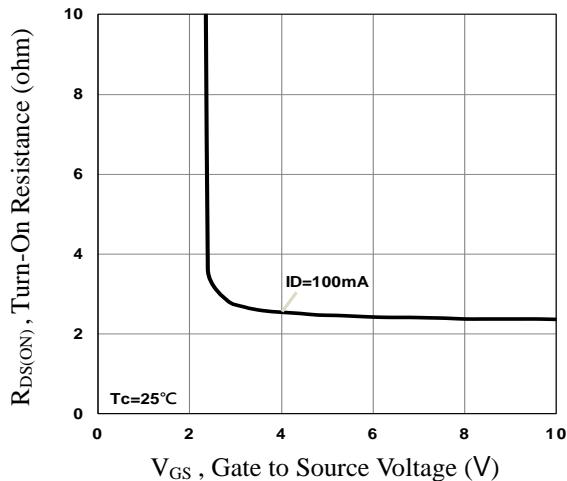
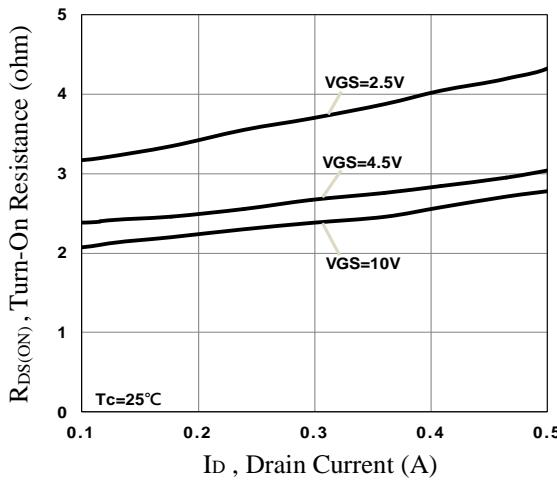
Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=30\text{V}$, $V_{GS}=10\text{V}$, $I_D=0.1\text{A}$	---	1	1.5	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	0.05	0.1	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	0.5	0.75	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=30\text{V}$, $V_{GS}=10\text{V}$, $R_G=6\Omega$ $I_D=0.1\text{A}$	---	5	10	ns
T_r	Rise Time ^{2, 3}		---	7	15	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	16	25	
T_f	Fall Time ^{2, 3}		---	12	20	
C_{iss}	Input Capacitance	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	32	50	pF
C_{oss}	Output Capacitance		---	15	30	
C_{rss}	Reverse Transfer Capacitance		---	7.5	15	

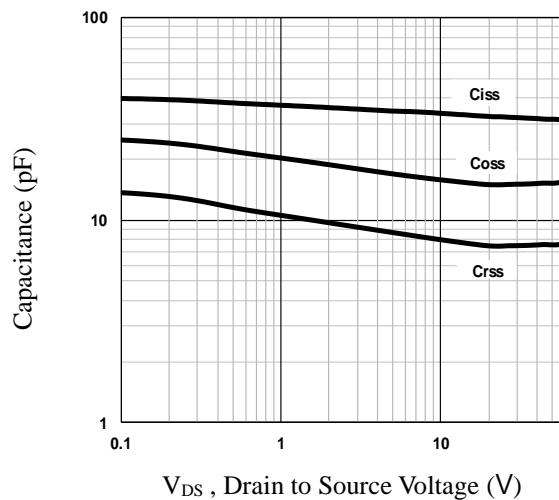
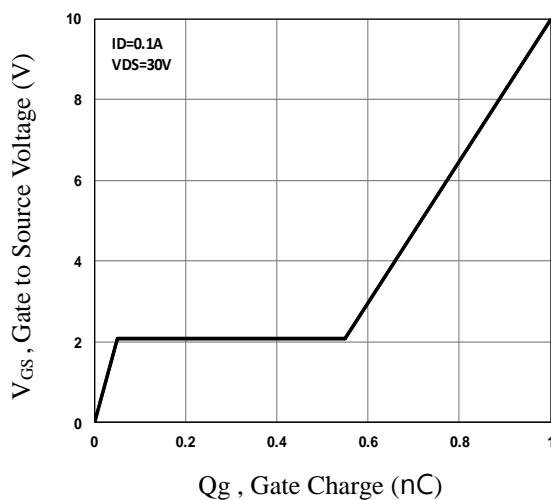
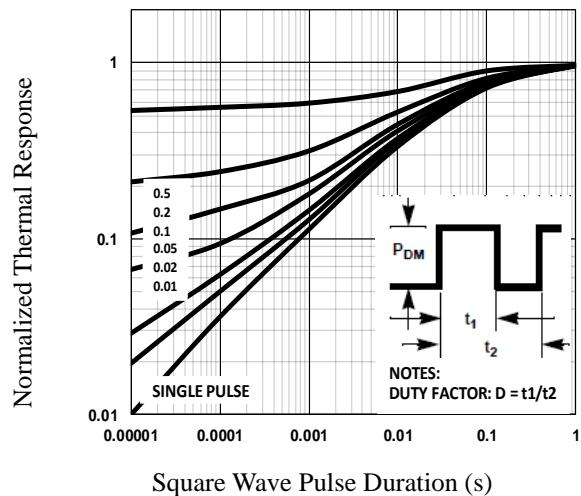
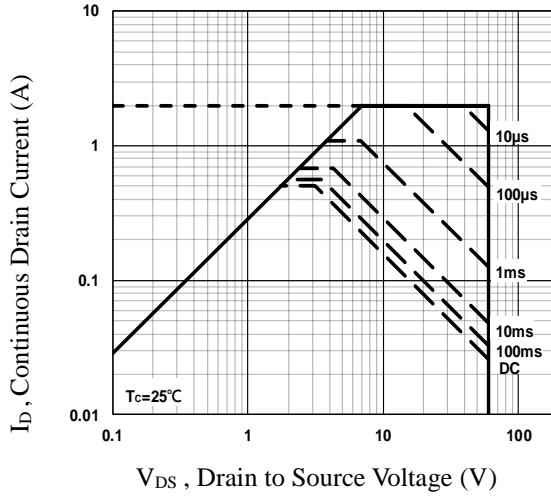
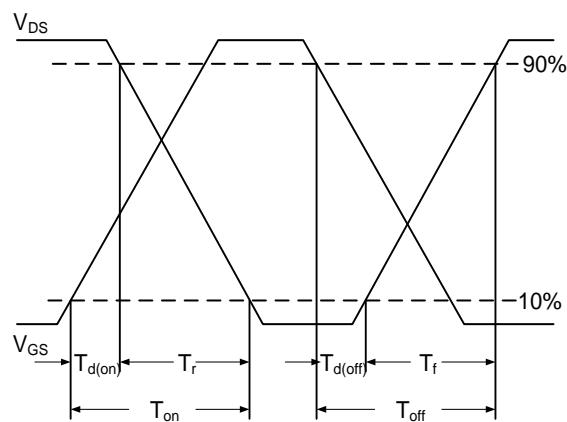
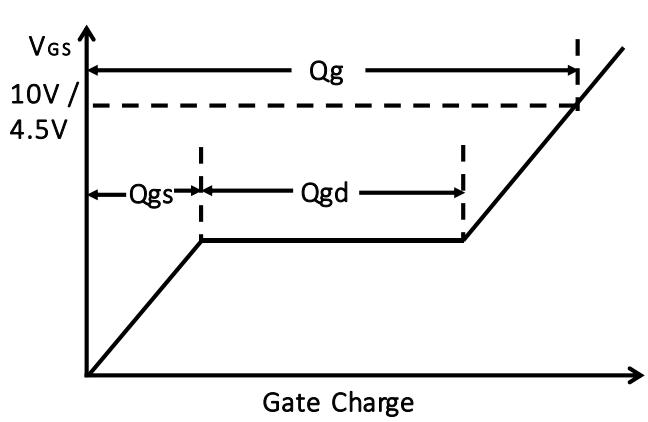
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	0.2	A
			---	---	0.4	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=0.2\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V
t_{rr}	Reverse Recovery Time	$V_R=50\text{V}$, $I_s=0.2\text{A}$ $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25\text{ }^{\circ}\text{C}$	---	20	---	ns
Q_{rr}	Reverse Recovery Charge		---	7.5	---	nC

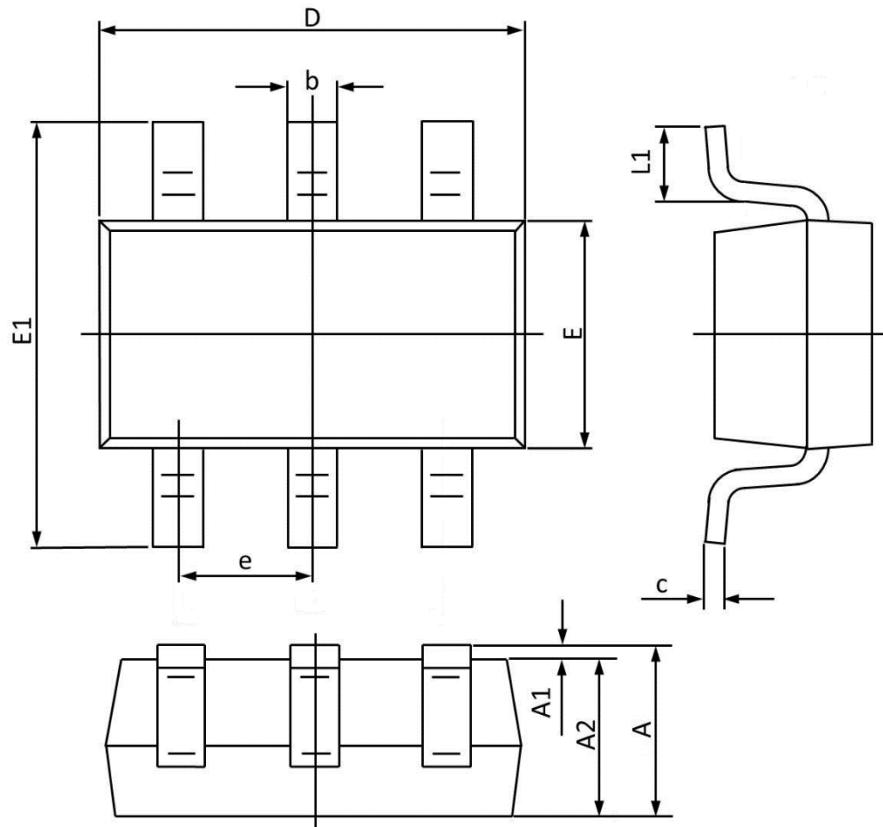
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Typical Output Characteristics

Fig.2 Continuous Drain Current vs. T_c

Fig.3 Normalized RDSON vs. T_j

Fig.4 Normalized V_{th} vs. T_j

Fig.5 Turn-On Resistance vs. V_{GS}

Fig.6 Turn-On Resistance vs. I_D

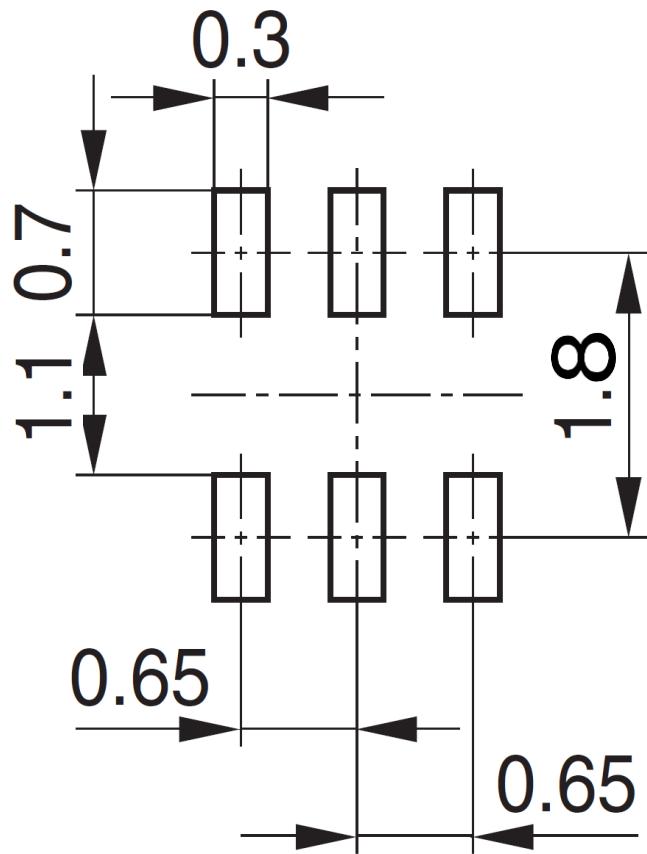

Fig.7 Capacitance Characteristics

Fig.8 Gate Charge Characteristics

Fig.9 Normalized Transient Impedance

Fig.10 Maximum Safe Operation Area

Fig.11 Switching Time Waveform

Fig.12 Gate Charge Waveform

SOT363 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.100	0.800	0.043	0.031
A1	0.100	0.000	0.004	0.000
A2	1.000	0.800	0.039	0.031
b	0.330	0.100	0.013	0.004
c	0.250	0.100	0.010	0.004
D	2.200	1.800	0.087	0.071
E	1.350	1.150	0.053	0.045
E1	2.400	1.800	0.094	0.071
e	0.65BSC		0.026BSC	
L1	0.350	0.100	0.014	0.004

SOT363 RECOMMENDED LAND PATTERN



unit : mm