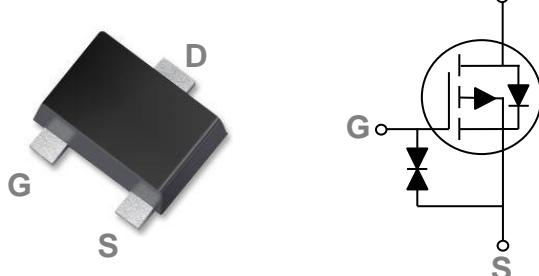


### General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT723 Pin Configuration



BVDSS	RDSON	ID
-20V	650mΩ	-400mA

### Features

- -20V, -400mA,  $RDS(ON) = 650m\Omega @ VGS = -4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for -1.5V Gate Drive Applications

### Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 8$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ C$ )	-400	mA
	Drain Current – Continuous ( $T_A=70^\circ C$ )	-320	mA
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	-1.6	A
$P_D$	Power Dissipation ( $T_A=25^\circ C$ )	450	mW
	Power Dissipation – Derate above 25°C	3.6	mW/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	280	°C/W

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$ , $I_D=-1\text{mA}$	---	-0.01	---	$\text{V}/\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-20\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{DS}=-16\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	-10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 8\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 20$	$\mu\text{A}$

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-4.5\text{V}$ , $I_D=-0.3\text{A}$	---	500	650	$\text{m}\Omega$	
		$V_{GS}=-2.5\text{V}$ , $I_D=-0.2\text{A}$	---	700	900		
		$V_{GS}=-1.8\text{V}$ , $I_D=-0.1\text{A}$	---	1100	1400		
		$V_{GS}=-1.5\text{V}$ , $I_D=-0.1\text{A}$	---	1700	2300		
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250\mu\text{A}$		-0.3	-0.7	-1.0	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient			---	3	---	$\text{mV}/\text{C}$

**Dynamic and switching Characteristics**

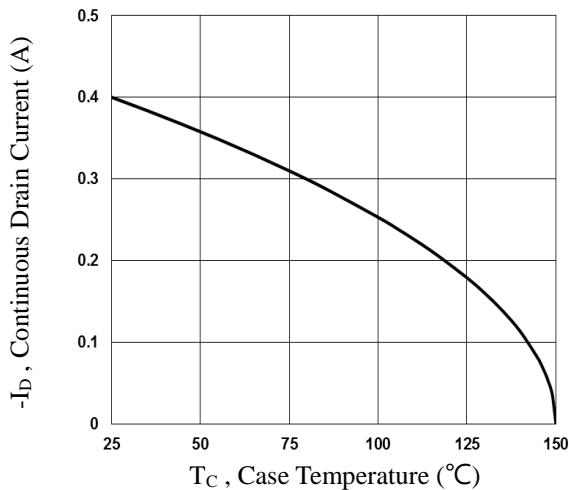
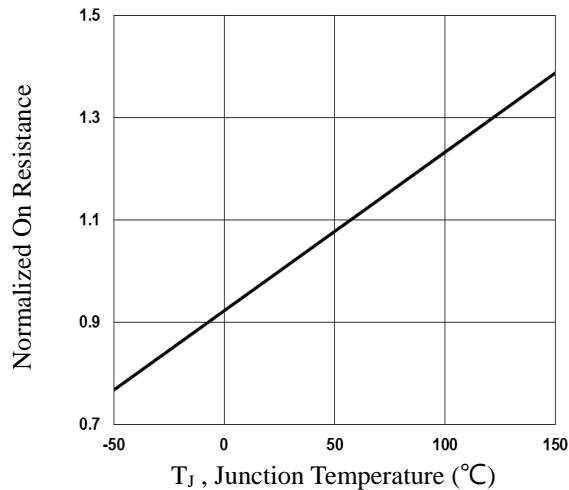
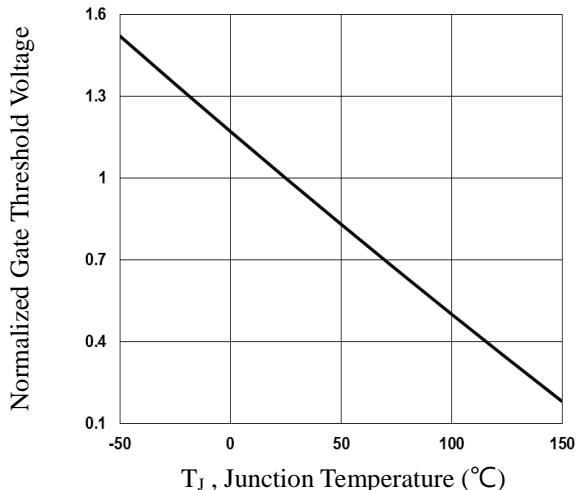
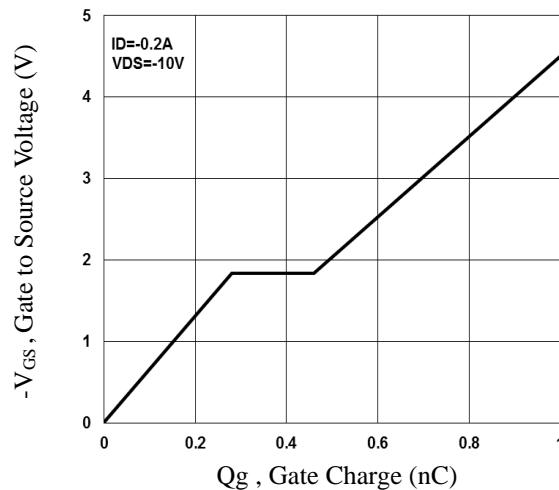
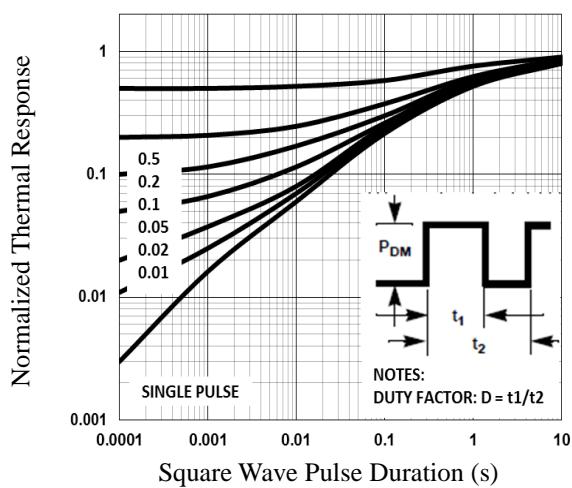
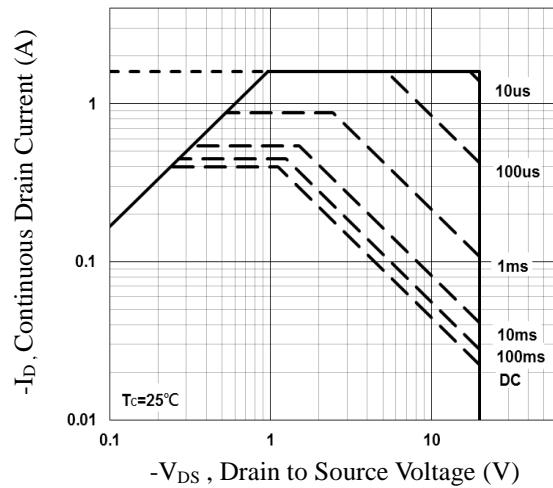
$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{DS}=-10\text{V}$ , $V_{GS}=-4.5\text{V}$ , $I_D=-0.2\text{A}$	---	1	2	$\text{nC}$
$Q_{gs}$	Gate-Source Charge <sup>2, 3</sup>		---	0.28	0.5	
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>		---	0.18	0.4	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{DD}=-10\text{V}$ , $V_{GS}=-4.5\text{V}$ , $R_G=10\Omega$ $I_D=-0.2\text{A}$	---	8	16	$\text{ns}$
$T_r$	Rise Time <sup>2, 3</sup>		---	5.2	10	
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>		---	30	60	
$T_f$	Fall Time <sup>2, 3</sup>		---	18	36	
$C_{iss}$	Input Capacitance	$V_{DS}=-10\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	40	78	$\text{pF}$
$C_{oss}$	Output Capacitance		---	15	30	
$C_{rss}$	Reverse Transfer Capacitance		---	6.5	13	

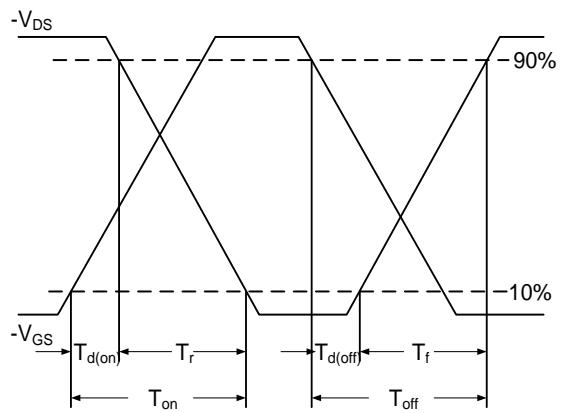
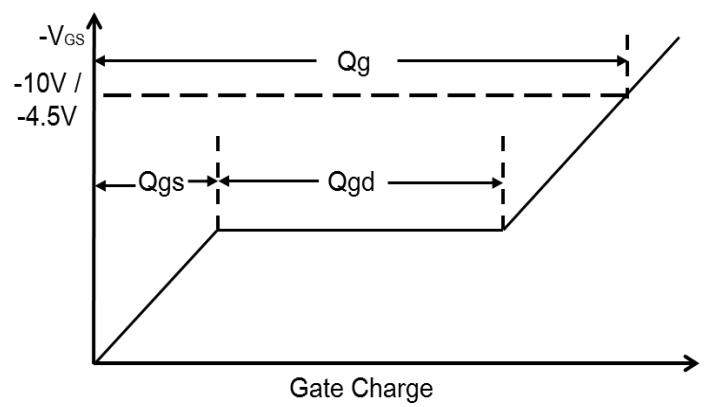
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-0.4	A
$I_{SM}$	Pulsed Source Current		---	---	-0.8	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=-0.2\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	V

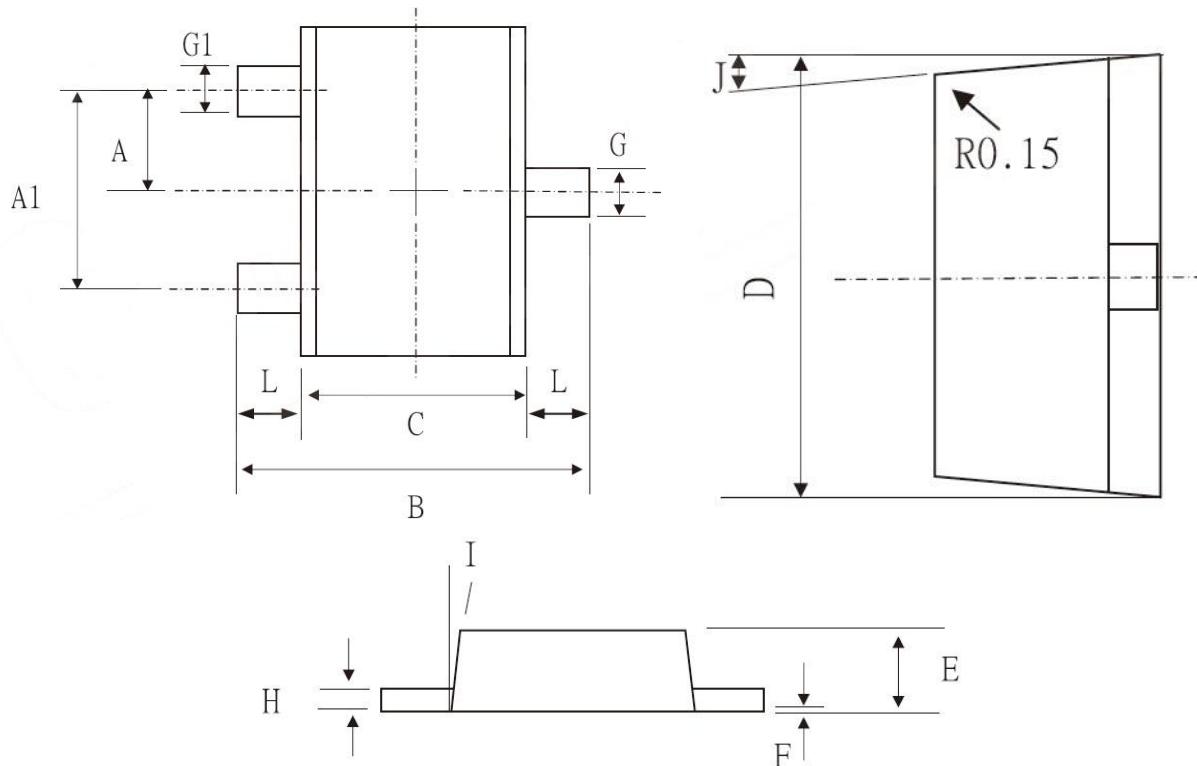
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**

**Fig.2 Normalized RDSON vs. T<sub>j</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Response**

**Fig.6 Maximum Safe Operation Area**


**Fig.7** Switching Time Waveform

**Fig.8** Gate Charge Waveform

## SOT723 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	<b>0.4 BSC</b>		<b>0.016 BSC</b>	
A1	<b>0.8 BSC</b>		<b>0.031 BSC</b>	
B	1.250	1.150	0.049	0.045
C	0.850	0.750	0.033	0.030
D	1.250	1.150	0.049	0.045
E	0.390	0.370	0.015	0.015
F	0.050	0.000	0.002	0.000
G	0.270	0.220	0.011	0.009
G1	0.250	0.170	0.010	0.007
H	0.150	0.080	0.006	0.003
I	13°	9°	13°	9°
L	0.250	0.150	0.010	0.006
J	11°	7°	11°	7°