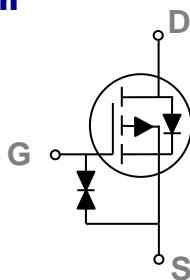
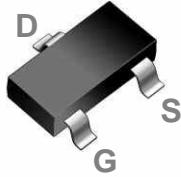


General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-3S Pin Configuration



BVDSS	RDSON	ID
-30V	45mΩ	-4.6A

Features

- -30V, -4.6A, $RDS(ON) = 45m\Omega @ VGS = -10V$
- Fast switching
- Green Device Available
- Suit for -2.5V Gate Drive Applications

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-Held Instruments

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Drain Current – Continuous ($T_A=25^\circ C$)	-4.6	A
	Drain Current – Continuous ($T_A=70^\circ C$)	-3.7	A
I_{DM}	Drain Current – Pulsed ¹	-18.4	A
P_D	Power Dissipation ($T_A=25^\circ C$)	1.56	W
	Power Dissipation – Derate above 25°C	0.0125	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	-30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$, $I_D=-1\text{mA}$	---	-0.03	---	$\text{V}/^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-30\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	μA
		$V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 12\text{V}$, $V_{DS}=0\text{V}$	---	---	± 20	μA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$, $I_D=-4\text{A}$	---	37	45	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-3\text{A}$	---	43	56	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}$, $I_D=-2\text{A}$	---	55	74	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu\text{A}$	-0.5	-0.7	-1.2	V
$\Delta V_{GS(\text{th})}$			---	3	---	$\text{mV}/^{\circ}\text{C}$

Dynamic and switching Characteristics

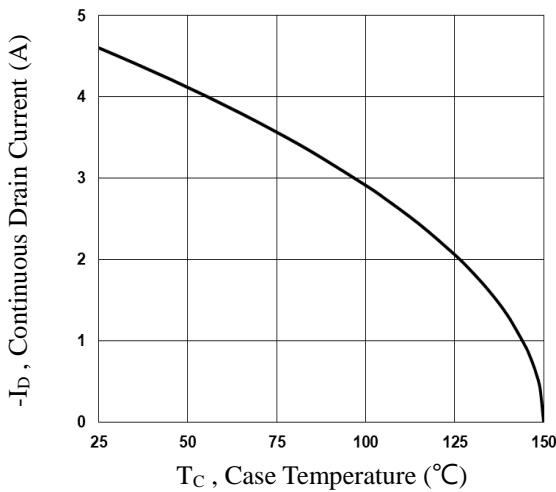
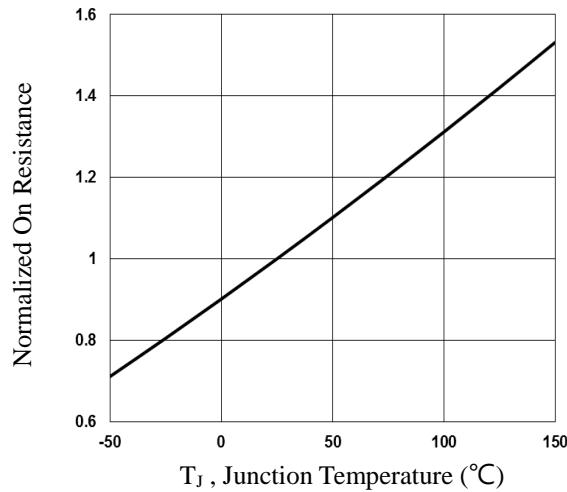
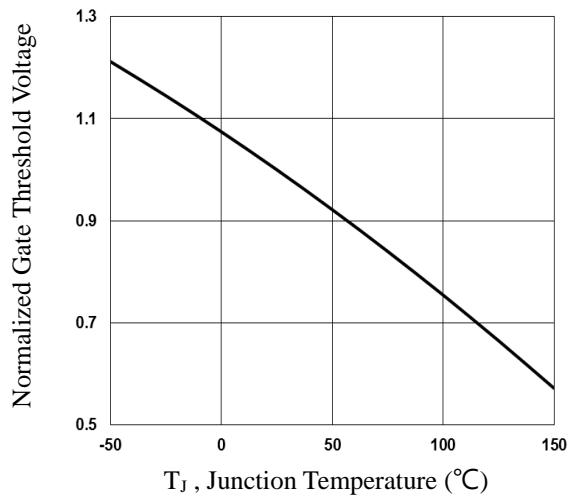
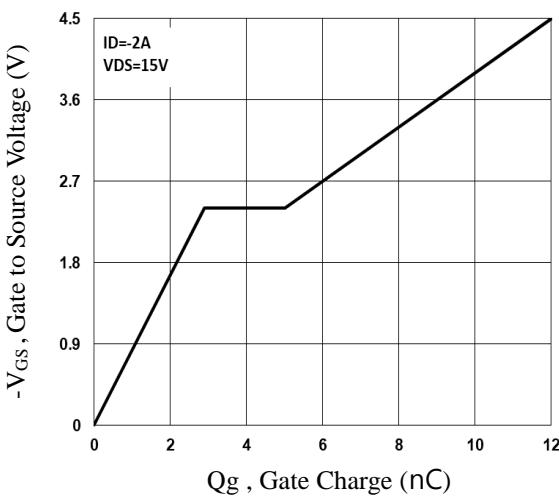
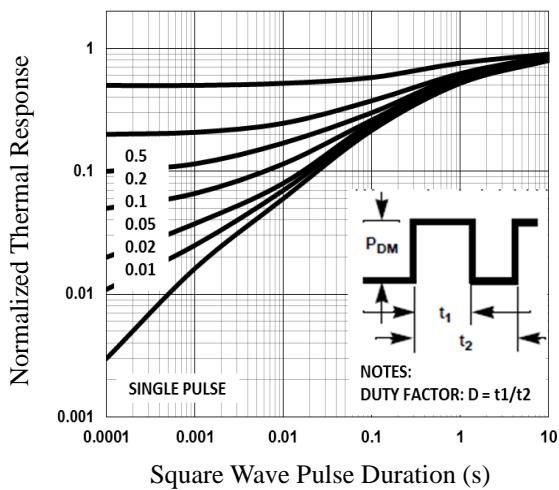
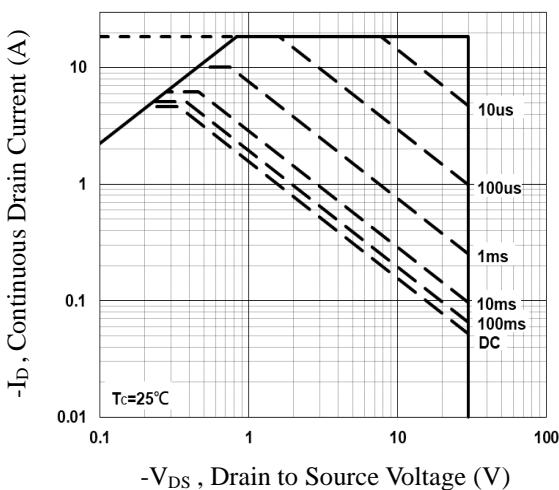
Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=-15\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-4\text{A}$	---	12	18	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	2.9	4.4	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	2.1	3.2	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=-15\text{V}$, $V_{GS}=-10\text{V}$, $R_G=6\Omega$	---	7.3	11	ns
T_r	Rise Time ^{2, 3}		---	26	39	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	60	90	
T_f	Fall Time ^{2, 3}		---	16	24	
C_{iss}	Input Capacitance	$V_{DS}=-15\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	1585	2378	pF
C_{oss}	Output Capacitance		---	97	146	
C_{rss}	Reverse Transfer Capacitance		---	68	102	

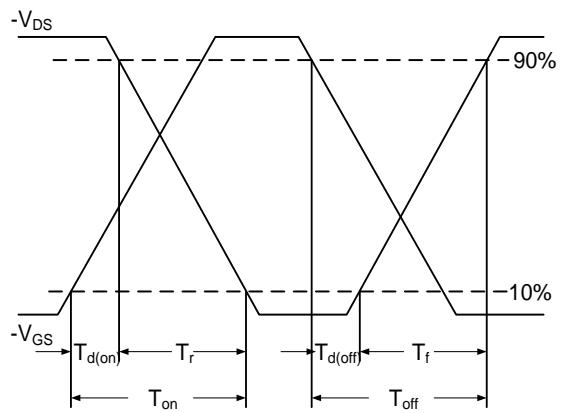
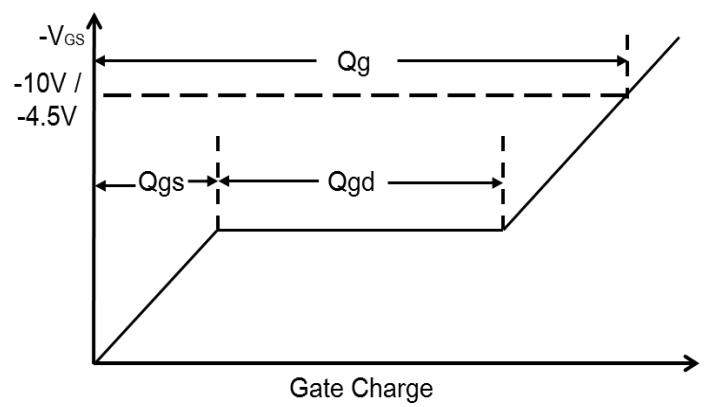
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-4.6	A
I_{SM}	Pulsed Source Current		---	---	-9.2	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	V
T_{rr}	Reverse Recovery Time	$VR=-30\text{V}$, $I_S=-2\text{A}$,	---	45	---	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100\text{A}/\mu\text{s}$, $T_J=25\text{ }^{\circ}\text{C}$	---	50	---	nC

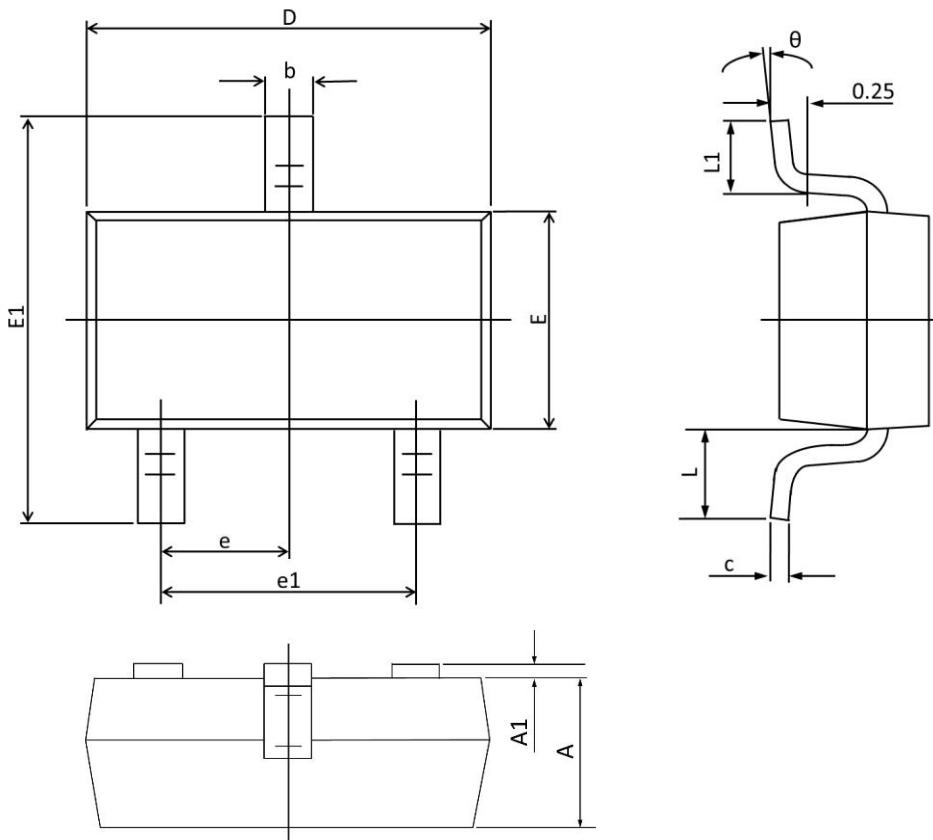
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_c

Fig.2 Normalized RDSON vs. T_j

Fig.3 Normalized V_{th} vs. T_j

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.700	3.100	0.106	0.122
E	1.100	1.500	0.043	0.059
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
theta	1°	10°	1°	10°