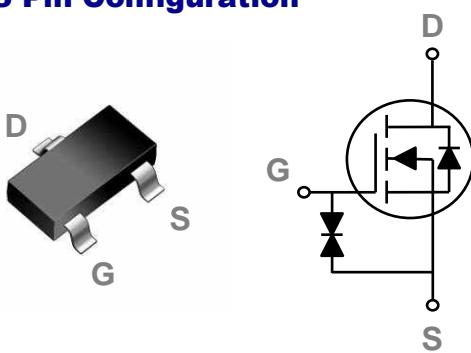


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
20V	300mΩ	1.45A

Features

- 20V, 1.45A, RDS(ON) = 300mΩ@VGS = 4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for 1.5V Gate Drive Applications

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{Gs}	Gate-Source Voltage	±8	V
I _D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	1.45	A
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	1.15	A
I _{DM}	Drain Current – Pulsed ¹	5.8	A
P _D	Power Dissipation ($T_A=25^\circ\text{C}$)	1	W
	Power Dissipation – Derate above 25°C	8	mW/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	125	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$, $I_D=1\text{mA}$	---	-0.01	---	V/C
I_{DS}	Drain-Source Leakage Current	$V_{DS}=20\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=16\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	μA
I_{GS}	Gate-Source Leakage Current	$V_{GS}=\pm 8\text{V}$, $V_{DS}=0\text{V}$	---	---	± 10	μA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=4.5\text{V}$, $I_D=0.5\text{A}$	---	215	300	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$, $I_D=0.4\text{A}$	---	255	400	
		$V_{GS}=1.8\text{V}$, $I_D=0.2\text{A}$	---	315	550	
		$V_{GS}=1.5\text{V}$, $I_D=0.1\text{A}$	---	390	800	
		$V_{GS}=1.2\text{V}$, $I_D=0.1\text{A}$	---	815	1500	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	0.3	0.6	1.0	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	3	---	mV/C

Dynamic and switching Characteristics

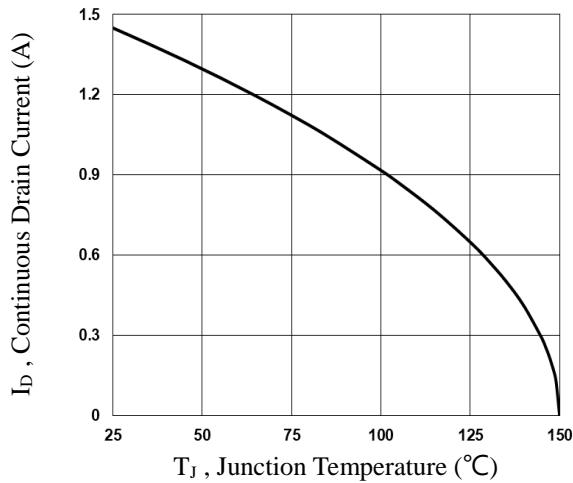
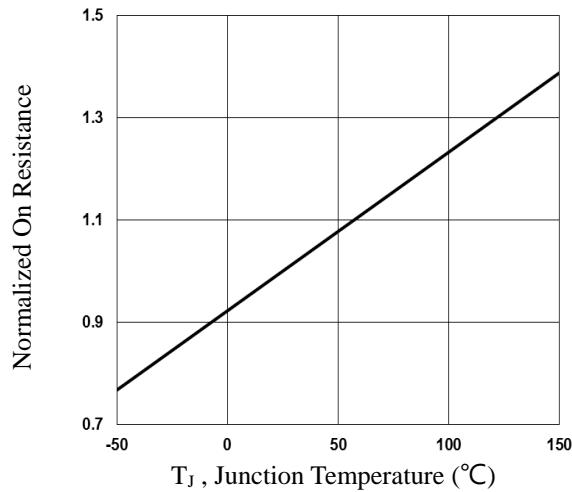
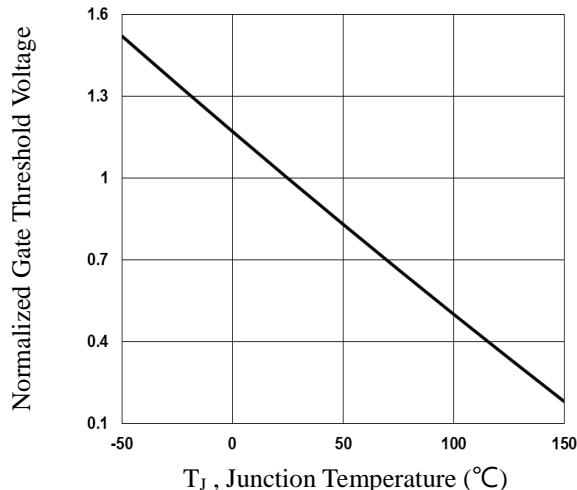
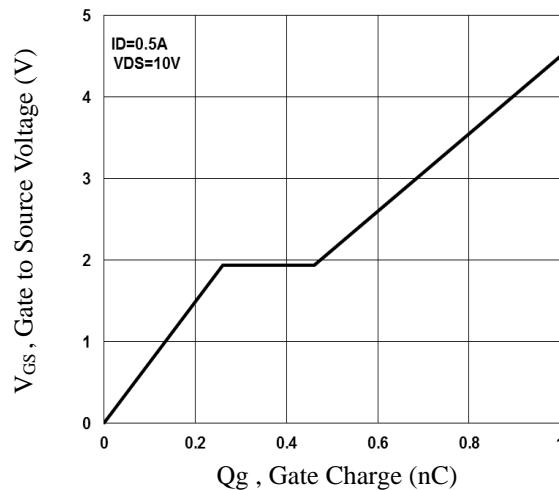
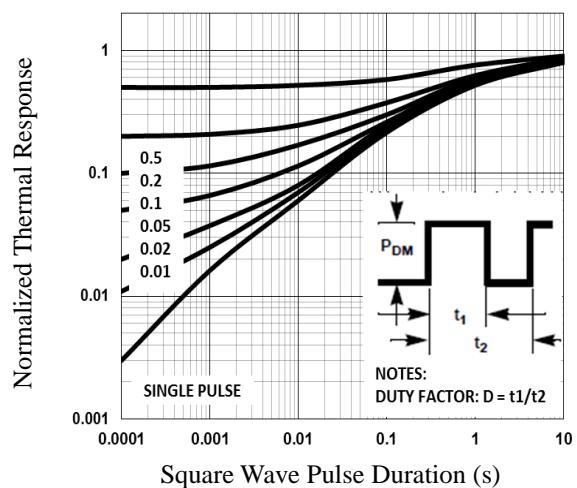
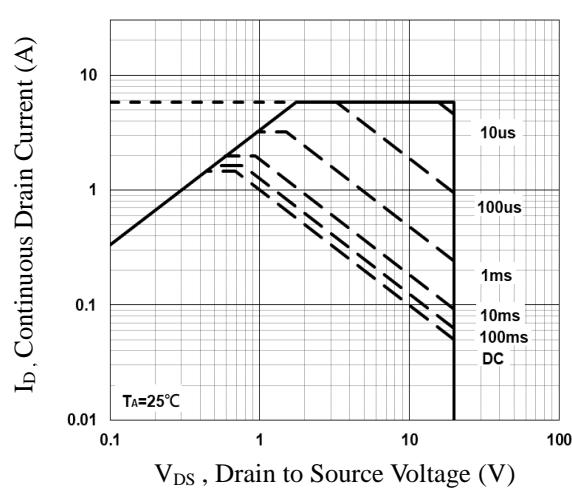
Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=10\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=0.5\text{A}$	---	1	2	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	0.26	0.5	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	0.2	0.4	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=10\text{V}$, $V_{GS}=4.5\text{V}$, $R_G=10\Omega$ $I_D=0.5\text{A}$	---	5	10	ns
T_r	Rise Time ^{2, 3}		---	3.5	7	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	14	28	
T_f	Fall Time ^{2, 3}		---	6	12	
C_{iss}	Input Capacitance	$V_{DS}=10\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	38.2	75	pF
C_{oss}	Output Capacitance		---	14.4	28	
C_{rss}	Reverse Transfer Capacitance		---	6	12	

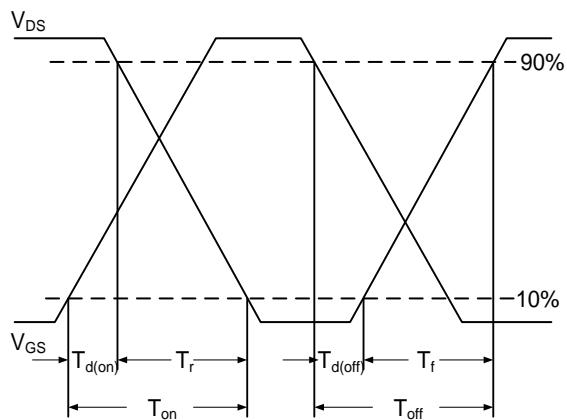
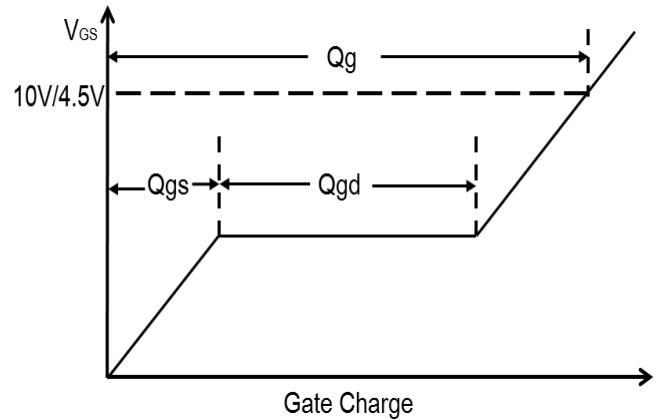
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	1.45	A
I_{SM}	Pulsed Source Current		---	---	2.9	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=0.2\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

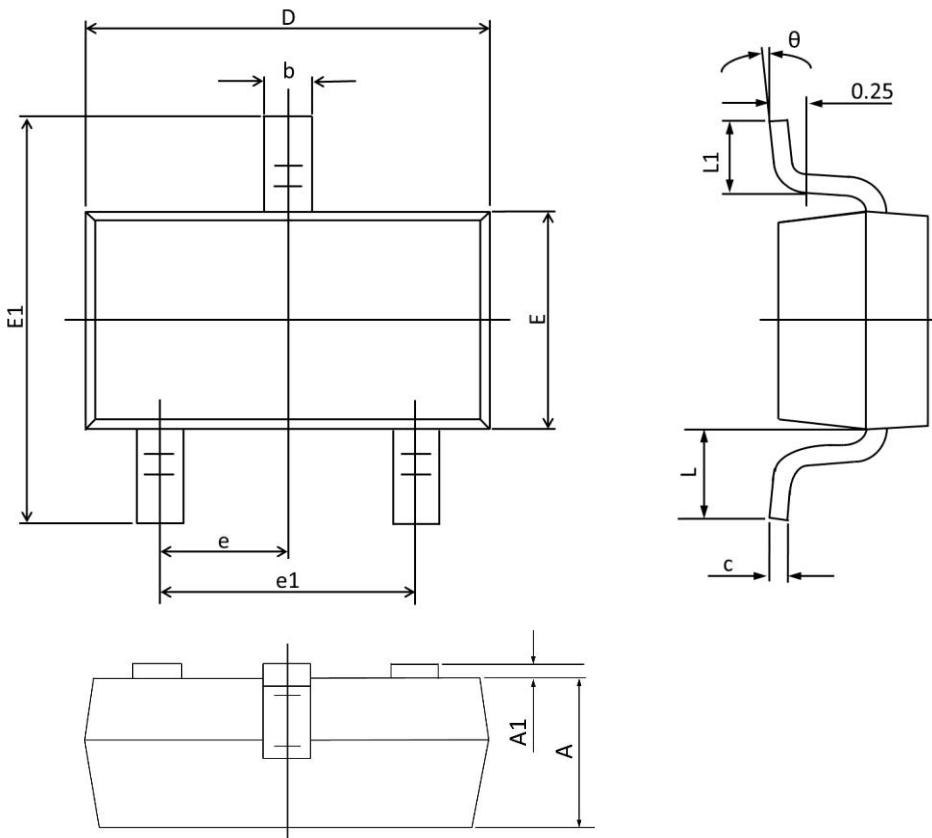
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_J

Fig.2 Normalized RDSON vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Response

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.110	0.035	0.044
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.800	3.040	0.110	0.120
E	1.200	1.400	0.047	0.055
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
θ	1°	10°	1°	10°