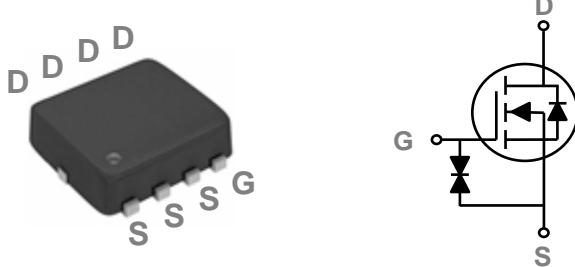


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### PPAK3X3 Pin Configuration



BVDSS	RDSON	ID
30V	8.5mΩ	48A

### Features

- 30V,48A,  $RDS(ON) = 8.5m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	48	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	30	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	192	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	45	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	30	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	35	W
	Power Dissipation – Derate above $25^\circ C$	0.28	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	3.6	$^\circ C/W$

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Static State Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.04	---	$\text{V}/\text{ }^{\circ}\text{C}$
$I_{DS}$	Drain-Source Leakage Current	$V_{DS}=30\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=24\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 10$	$\mu\text{A}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10\text{V}$ , $I_D=16\text{A}$	---	6.2	8.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=8\text{A}$	---	9	13	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	1.2	1.6	2.5	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	-4	---	$\text{mV}/\text{ }^{\circ}\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=8\text{A}$	---	9.5	---	S

**Dynamic Characteristics**

$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=15\text{V}$ , $V_{GS}=4.5\text{V}$ , $I_D=20\text{A}$	---	7.5	12	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	1.3	2.6	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	4.5	8	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=15\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=3.3\Omega$	---	4.8	9	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	12.5	24	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	27.6	52	
$T_f$	Fall Time <sup>3, 4</sup>		---	8.2	16	
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	680	1000	pF
$C_{oss}$	Output Capacitance		---	150	220	
$C_{rss}$	Reverse Transfer Capacitance		---	70	105	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	2.7	5.4	$\Omega$

**Guaranteed Avalanche Energy**

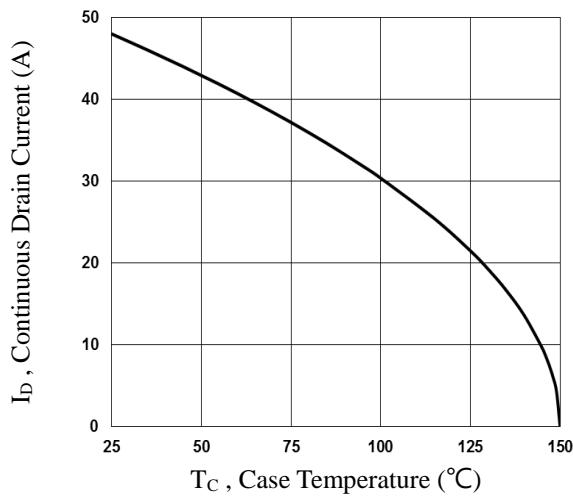
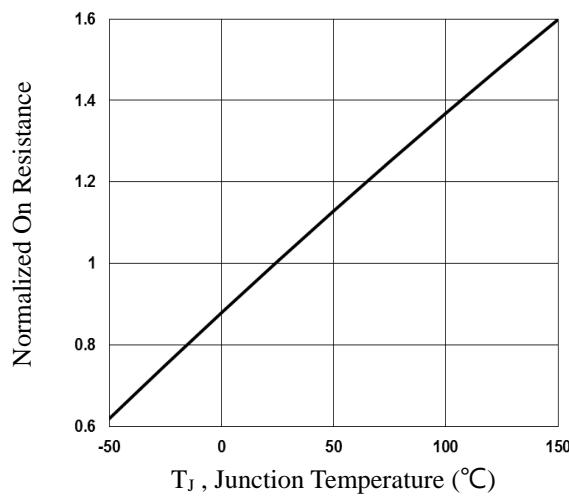
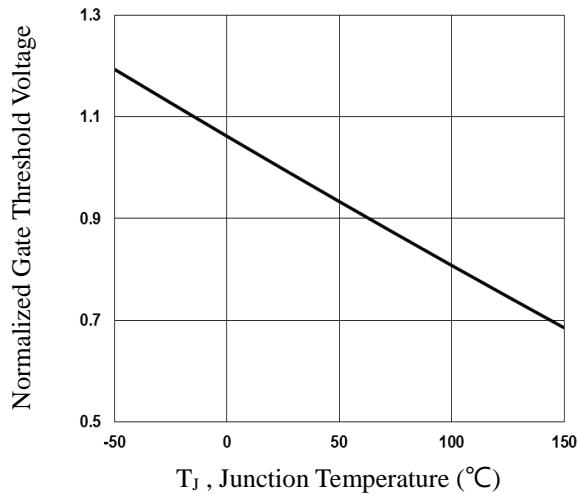
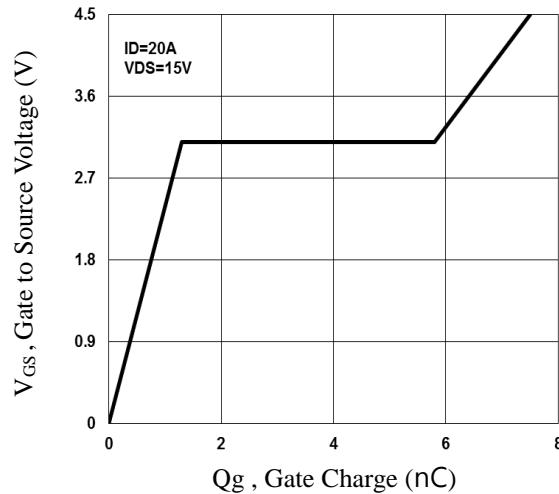
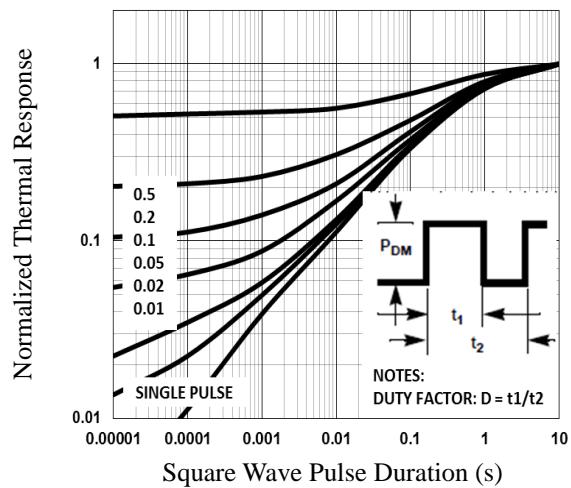
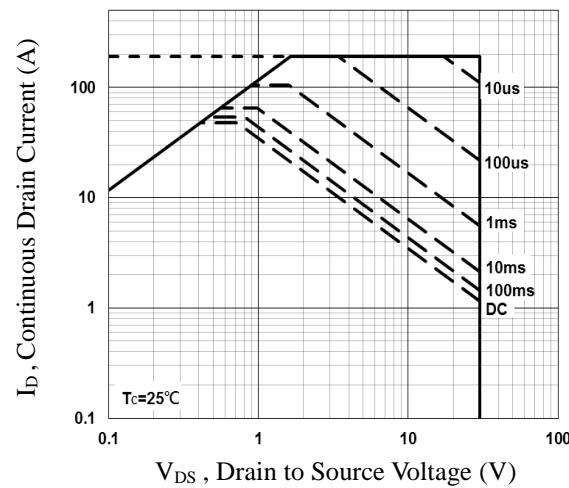
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy	$V_{DD}=25\text{V}$ , $L=0.1\text{mH}$ , $I_{AS}=15\text{A}$	12	---	---	$\text{mJ}$

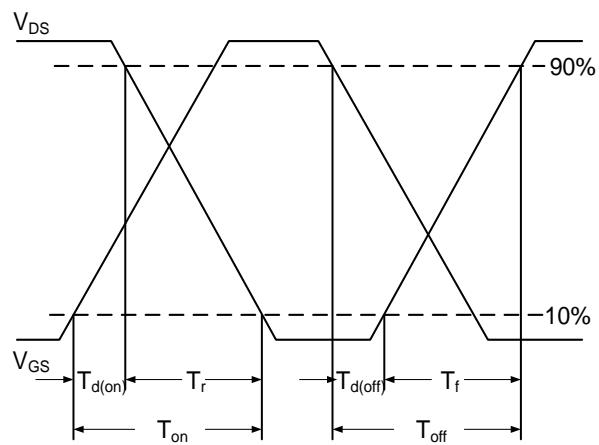
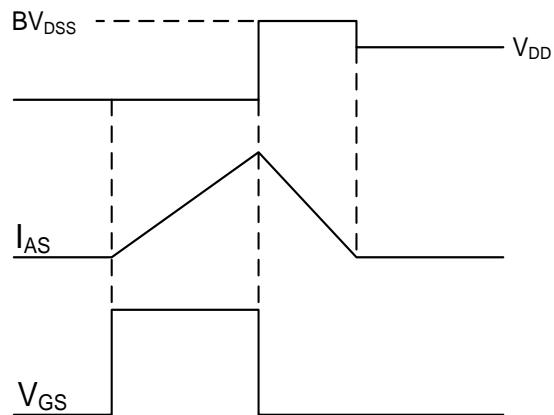
**Drain-Source Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	48	A
$I_{SM}$	Pulsed Source Current <sup>3</sup>		---	---	192	A
$V_{SD}$	Diode Forward Voltage <sup>3</sup>	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

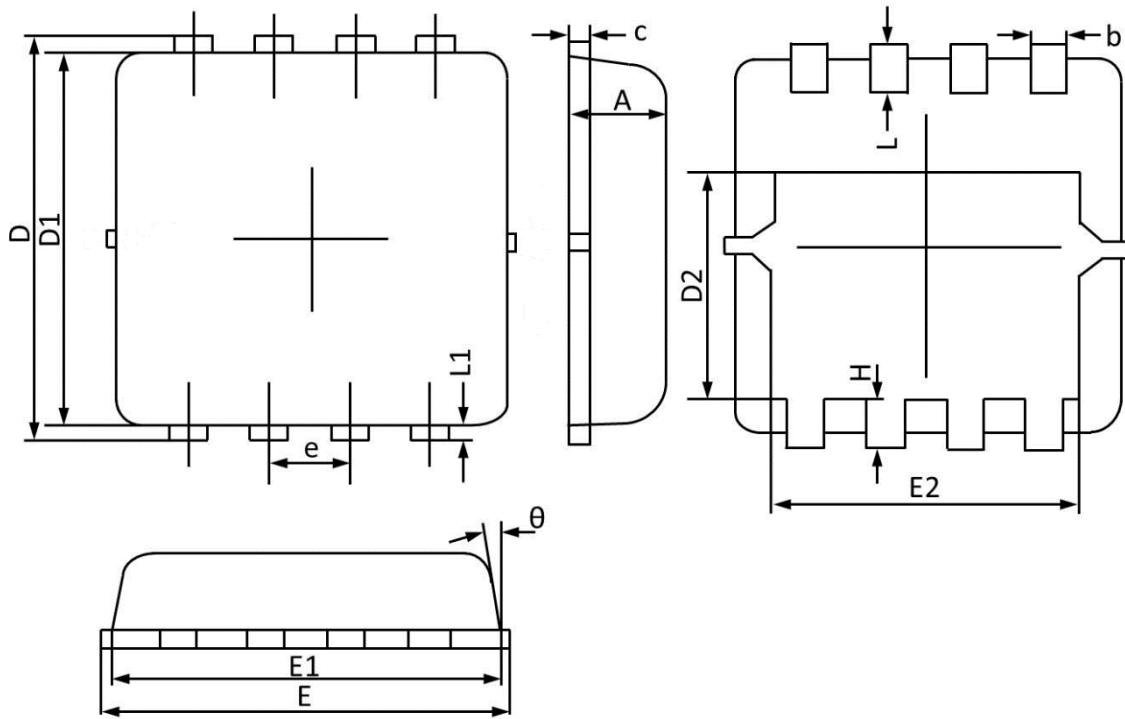
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=30\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25\text{ }^{\circ}\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_C$** 

**Fig.2 Normalized RD<sub>SON</sub> vs.  $T_J$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## PPAK3x3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
c	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
e	0.65BSC		0.026BSC	
H	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
θ	14°	6°	14°	6°

**PPAK3X3 RECOMMENDED LAND PATTERN**