

### General Description

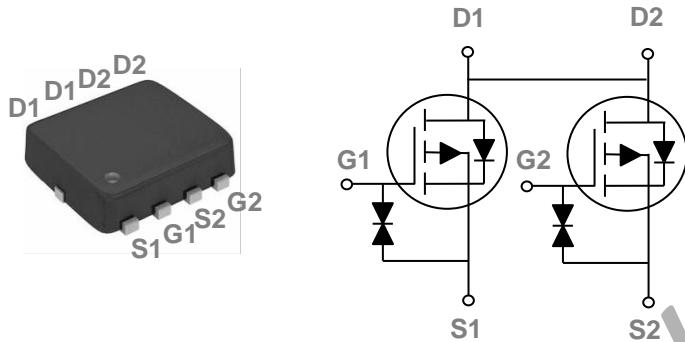
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
-20V	33mΩ	-5.6A

### Features

- -20V,-5.6A, RDS(ON) =33mΩ @VGS = -4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available

### PPAK3x3 Dual NEP Pin Configuration



### Applications

- MB / VGA / Vcore
- POL Applications
- Networking

### Absolute Maximum Ratings T<sub>c</sub>=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-20	V
V <sub>GS</sub>	Gate-Source Voltage	±10	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>A</sub> =25°C)	5.6	A
	Drain Current – Continuous (T <sub>A</sub> =70°C)	4.5	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	22.4	A
P <sub>D</sub>	Power Dissipation (T <sub>A</sub> =25°C)	1.67	W
	Power Dissipation – Derate above 25°C	0.01	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	75	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=-250\mu\text{A}$	-20	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_{\text{D}}=-1\text{mA}$	---	-0.02	---	$\text{V}/^\circ\text{C}$
$I_{\text{DS}}^{\text{SS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-20\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{\text{DS}}=-16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	-10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 10\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=-4.5\text{V}$ , $I_{\text{D}}=-4\text{A}$	---	28	33	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}$ , $I_{\text{D}}=-3\text{A}$	---	37	45	$\text{m}\Omega$
		$V_{\text{GS}}=-1.8\text{V}$ , $I_{\text{D}}=-2\text{A}$	---	49	65	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_{\text{D}}=-250\mu\text{A}$	-0.3	-0.6	-1	V
$\Delta V_{\text{GS}(\text{th})}$	$V_{\text{GS}(\text{th})}$ Temperature Coefficient		---	2	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$ , $I_{\text{D}}=-3\text{A}$	---	8.5	---	S

**Dynamic and switching Characteristics**

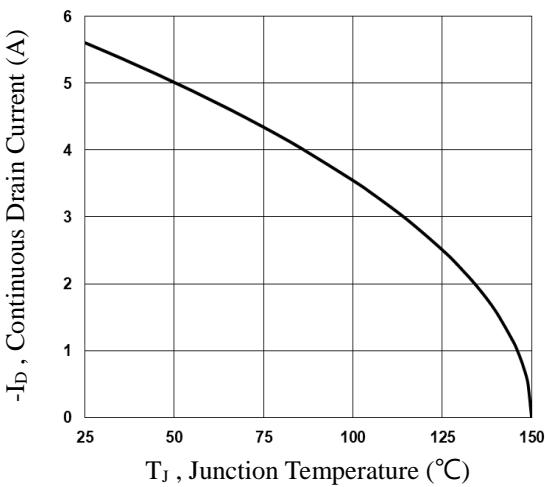
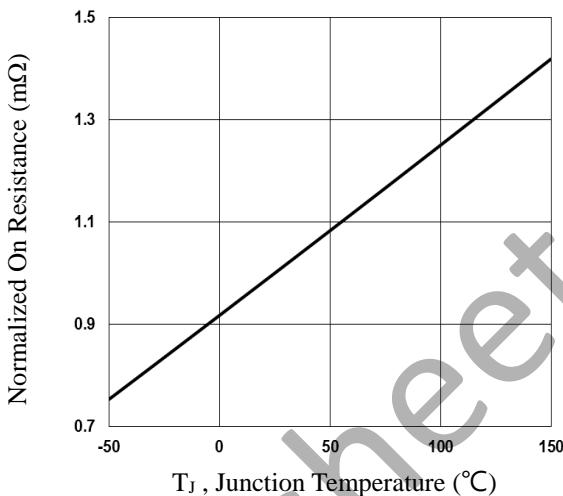
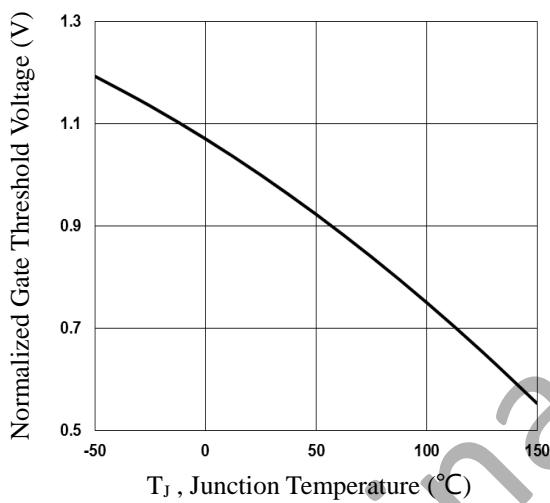
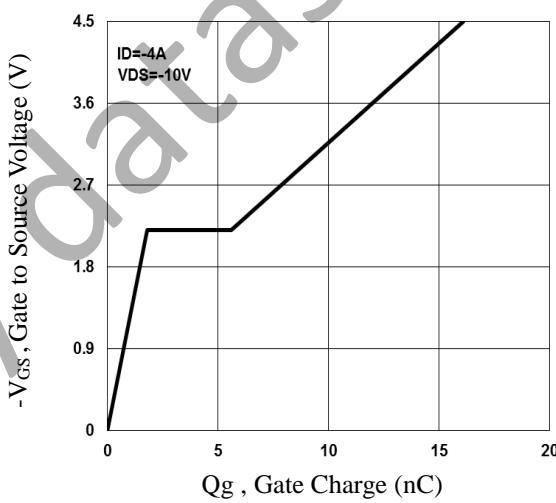
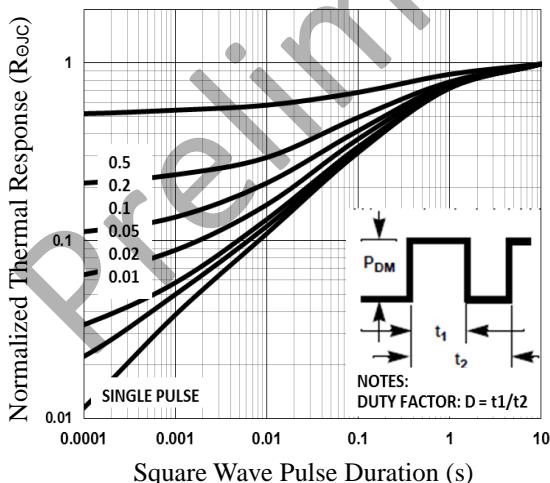
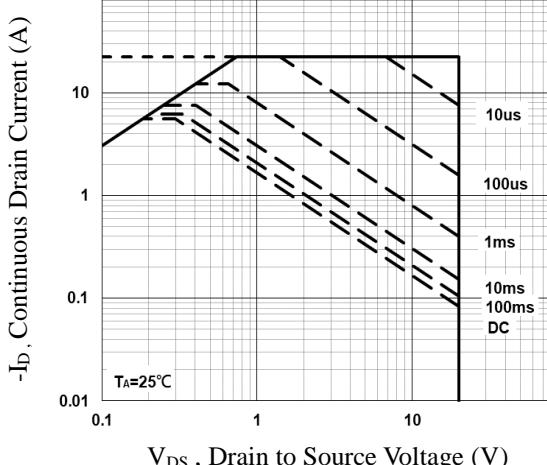
$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{\text{DS}}=-10\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $I_{\text{D}}=-4\text{A}$	---	16.1	25	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2,3</sup>		---	1.8	3.6	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2,3</sup>		---	3.8	7	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time <sup>2,3</sup>	$V_{\text{DD}}=-10\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $R_{\text{G}}=25\Omega$ $I_{\text{D}}=1\text{A}$	---	8.2	16	ns
$T_r$	Rise Time <sup>2,3</sup>		---	30	57	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time <sup>2,3</sup>		---	71	135	
$T_f$	Fall Time <sup>2,3</sup>		---	20	38	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	1440	2100	pF
$C_{\text{oss}}$	Output Capacitance		---	155	230	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	115	170	

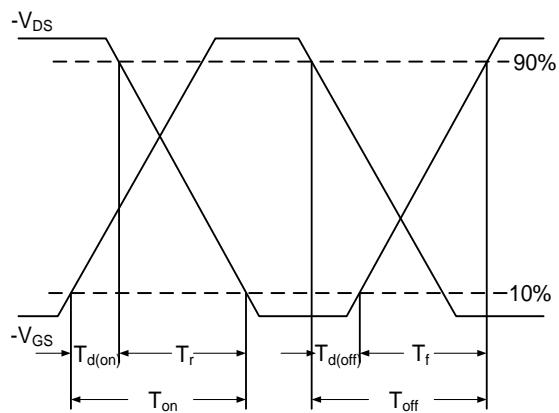
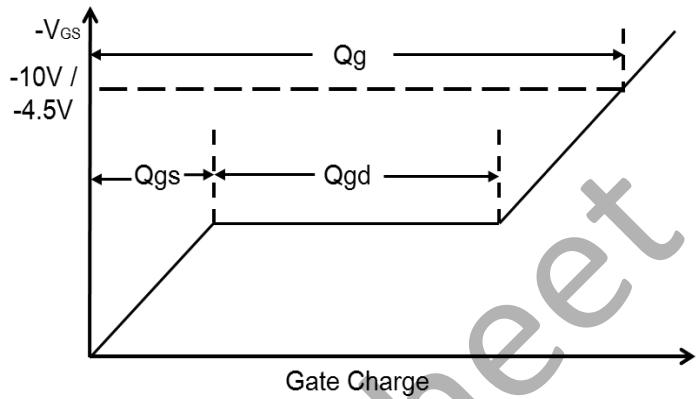
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-5.6	A
$I_{\text{SM}}$	Pulsed Source Current <sup>2</sup>		---	---	-11.2	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_{\text{s}}=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

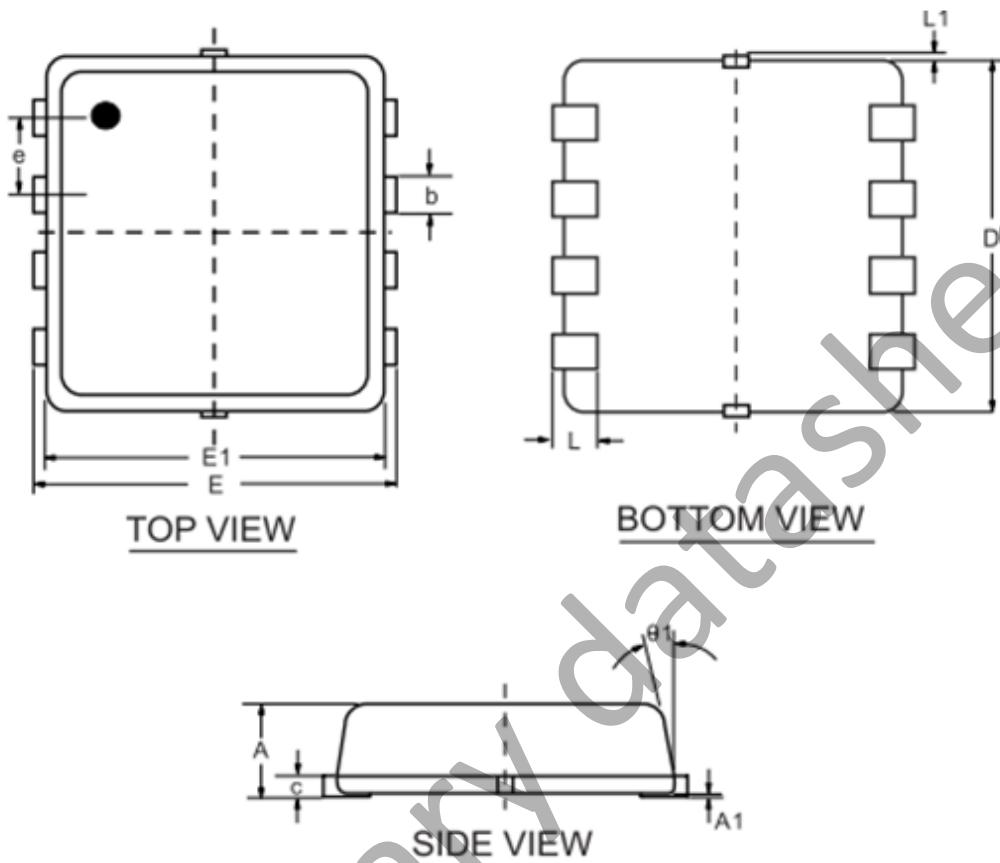
- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- The data tested by pulsed, pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
- Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**

**Fig.2 Normalized RDS(ON) vs. T<sub>J</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Response**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

Preliminary datasheet

## PPAK3x3 Dual NEP PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		
	Min	Typ	Max
A	0.700	0.800	0.900
A1	0.000	---	0.050
b	0.250	0.300	0.350
c	0.080	0.152	0.250
D	2.800	2.900	3.000
E	2.700	2.800	2.900
E1	2.200	2.300	2.400
e	0.65BSC		
L	0.200	0.375	0.450
L1	0.00	---	0.10
$\Theta_1$	0°	10°	12°