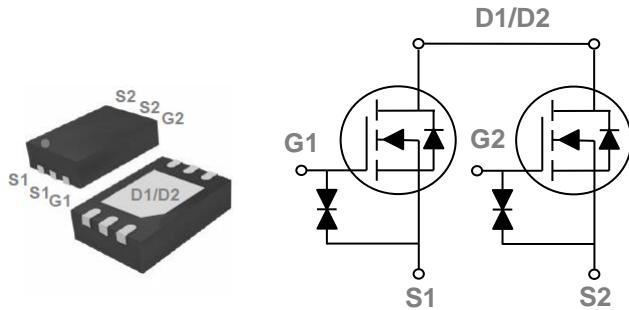


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### DFN2x3 Dual Pin Configuration



BVDSS	RDSON	ID
20V	6.7mΩ	32A

### Features

- 20V,32A,  $RDS(ON) = 6.7m\Omega$  @ $VGS = 4.5V$
- Improved dv/dt capability
- Fast switching
- G-S ESD Protection Diode Embedded
- Green Device Available

### Applications

- Handheld Instruments
- POL Applications
- Battery Protection Applications

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	32	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	20.2	A
	Drain Current – Continuous ( $T_A=25^\circ C$ )	13	A
	Drain Current – Continuous ( $T_A=70^\circ C$ )	10.4	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup> ( $T_c=25^\circ C$ )	128	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	20	W
	Power Dissipation – Derate above $T_c=25^\circ C$	0.16	W/ $^\circ C$
	Power Dissipation ( $T_A=25^\circ C$ )	2	W
	Power Dissipation – Derate above $T_A=25^\circ C$	0.016	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to case	---	6.1	$^\circ C/W$

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	20	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=20\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=16\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=85\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 12\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 20$	$\mu\text{A}$

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=4.5\text{V}$ , $I_D=5.5\text{A}$	4.5	5.6	6.7	$\text{m}\Omega$
		$V_{GS}=4.0\text{V}$ , $I_D=5.5\text{A}$	4.8	5.8	7.2	$\text{m}\Omega$
		$V_{GS}=3.7\text{V}$ , $I_D=5.5\text{A}$	5	6	7.6	$\text{m}\Omega$
		$V_{GS}=3.1\text{V}$ , $I_D=5.5\text{A}$	5.5	6.5	8.2	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$ , $I_D=5.5\text{A}$	6	7.4	9.6	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	0.5	0.75	1.5	V
$g_{fs}$	Forward Transconductance	$V_{DS}=5\text{V}$ , $I_D=5\text{A}$	---	15	---	S

**Dynamic and switching Characteristics**

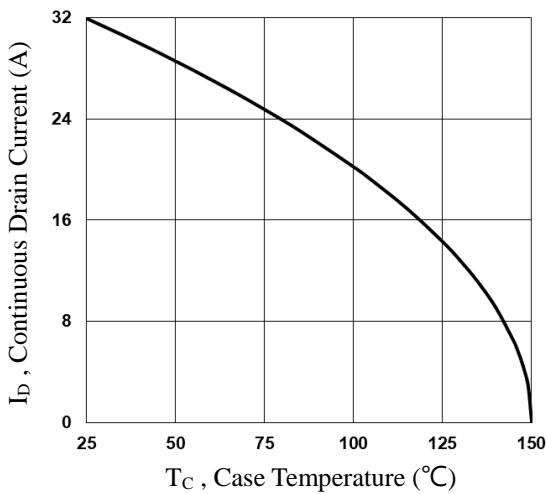
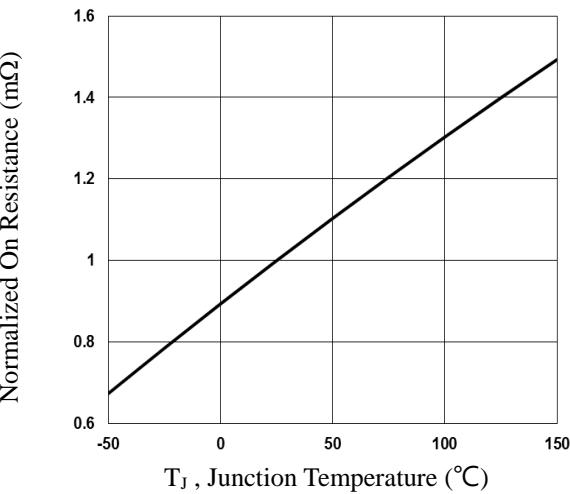
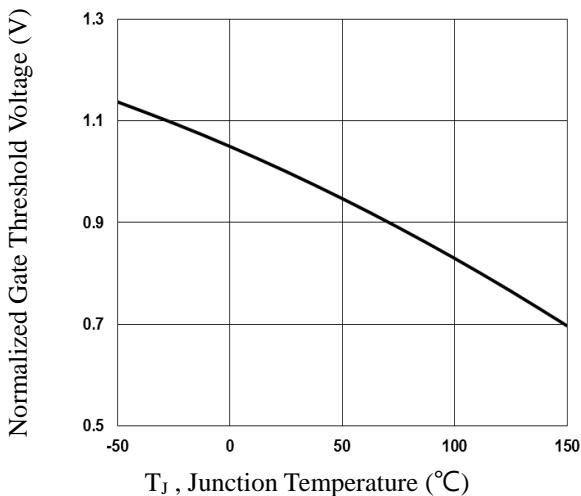
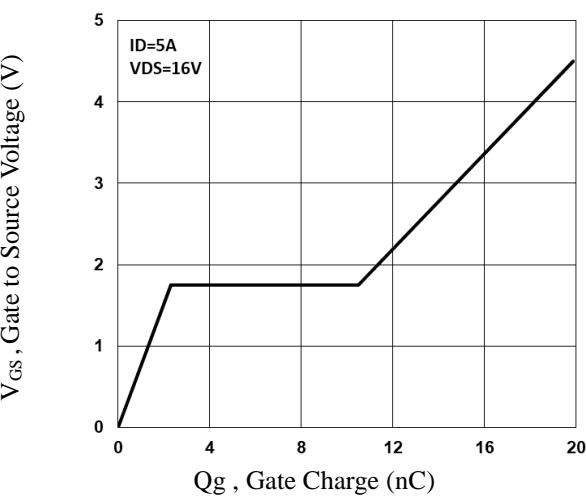
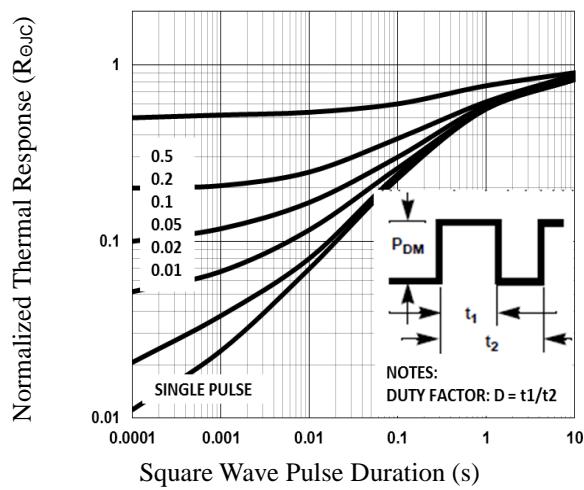
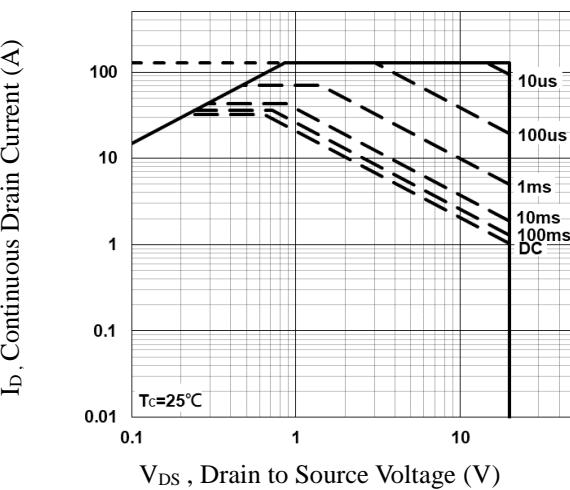
$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{DS}=16\text{V}$ , $V_{GS}=4.5\text{V}$ , $I_D=5\text{A}$	---	19.9	30	nC
$Q_{gs}$	Gate-Source Charge <sup>2,3</sup>		---	2.3	3.8	
$Q_{gd}$	Gate-Drain Charge <sup>2,3</sup>		---	8.2	12.3	
$T_{d(on)}$	Turn-On Delay Time <sup>2,3</sup>	$V_{DD}=15\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$ $I_D=5\text{A}$	---	31	60	ns
$T_r$	Rise Time <sup>2,3</sup>		---	69	140	
$T_{d(off)}$	Turn-Off Delay Time <sup>2,3</sup>		---	66	132	
$T_f$	Fall Time <sup>2,3</sup>		---	58	119	
$C_{iss}$	Input Capacitance	$V_{DS}=15\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	780	1180	pF
$C_{oss}$	Output Capacitance		---	237	356	
$C_{rss}$	Reverse Transfer Capacitance		---	90	136	

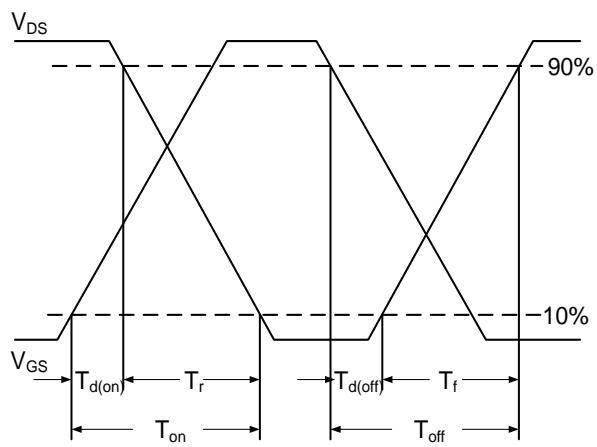
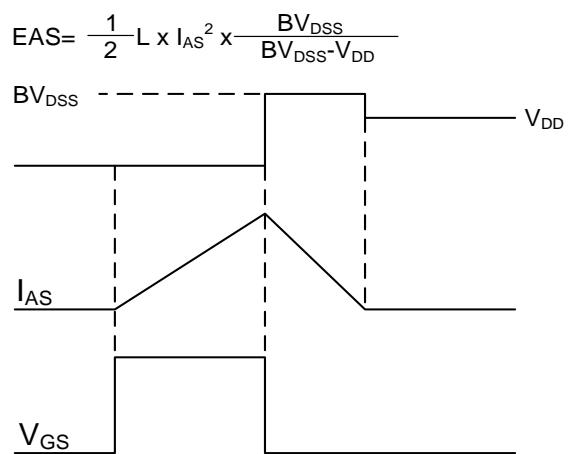
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	32	A
$I_{SM}$	Pulsed Source Current		---	---	64	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V
$T_{rr}$	Reverse Recovery Time	$V_R=20\text{V}$ , $I_S=5\text{A}$	---	670	---	ns
$Q_{rr}$	Reverse Recovery Charge	$dI/dt=100\text{A}/\mu\text{s}$ $T_J=25\text{ }^{\circ}\text{C}$	---	9.8	---	$\mu\text{C}$

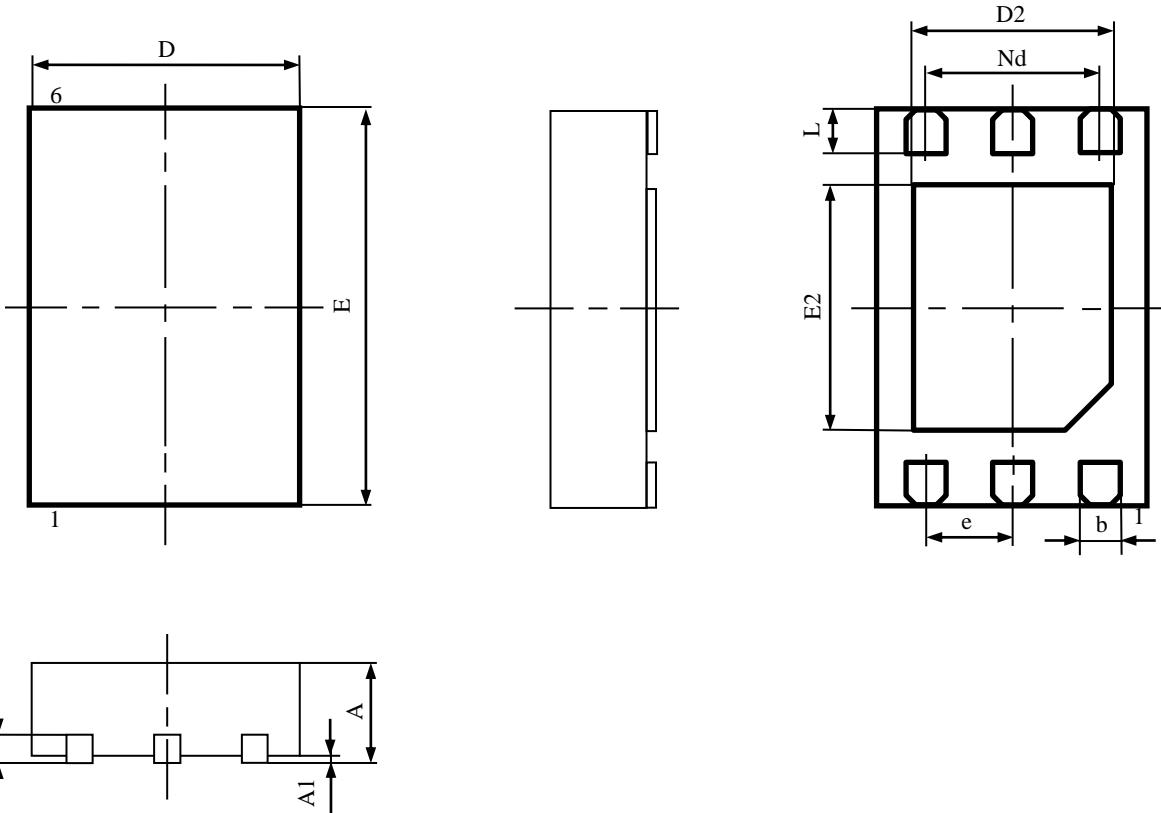
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. T<sub>C</sub>**

**Fig.2 Normalized RDSON vs. T<sub>J</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Response**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## DFN2X3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.800	0.700	0.031	0.028
A1	0.050	0.02typ.	0.002	0.001typ.
b	0.350	0.200	0.014	0.008
c	0.250	0.180	0.010	0.007
D	2.100	1.900	0.083	0.075
D2	1.600	1.400	0.063	0.055
e	0.5BSC		0.02BSC	
Nd	1.0BSC		0.04BSC	
E	3.100	2.900	0.122	0.114
E2	1.750	1.650	0.069	0.065
L	0.400	0.300	0.016	0.012

## DFN2X3 RECOMMENDED LAND PATTERN

