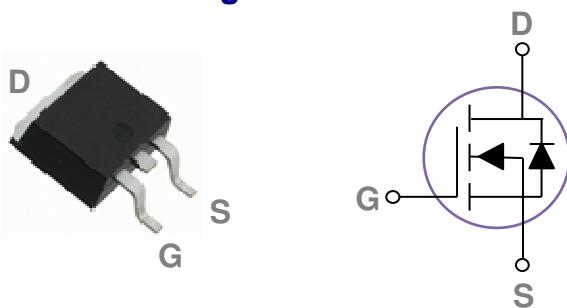


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO252 Pin Configuration



BVDSS	RDS(ON)	ID
80V	3.9mΩ	100A

### Features

- 80V, 100A,  $RDS(ON) = 3.9m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	80	V
$V_{GS}$	Gate-Source Voltage	+20/-12	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	100	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	63	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	400	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	245	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	70	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	130	W
	Power Dissipation – Derate above 25°C	1.04	W/°C
$T_{STG}$	Storage Temperature Range	-50 to 150	°C
$T_J$	Operating Junction Temperature Range	-50 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.96	°C/W

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	80	---	---	V
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	---	0.03	---	V/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =64V, V <sub>GS</sub> =0V, T <sub>J</sub> =85°C	---	---	10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V	---	---	100	nA

**On Characteristics**

R <sub>DSON</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	3.2	3.9	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	---	4.6	6.2	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	1	1.6	2.5	V
△V <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-5.8	---	mV/°C
g <sub>fS</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =5A	---	10	---	S

**Dynamic and switching Characteristics**

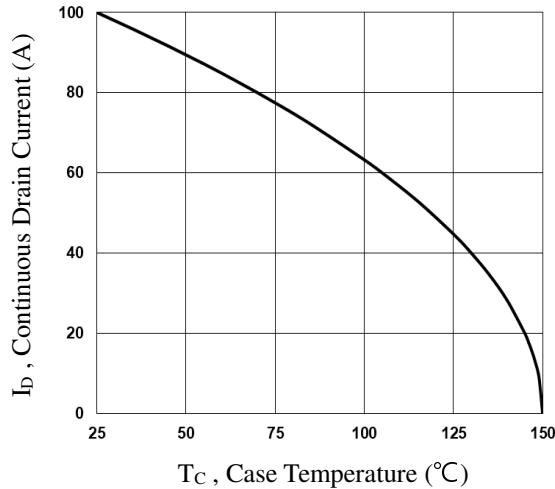
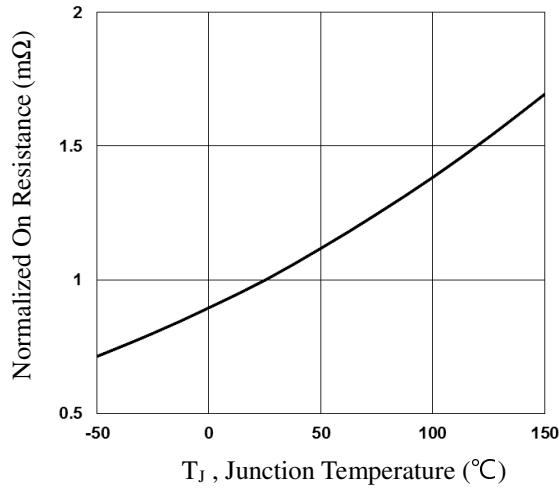
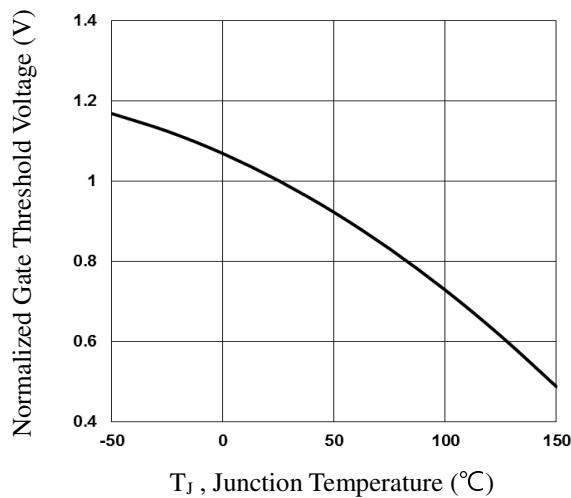
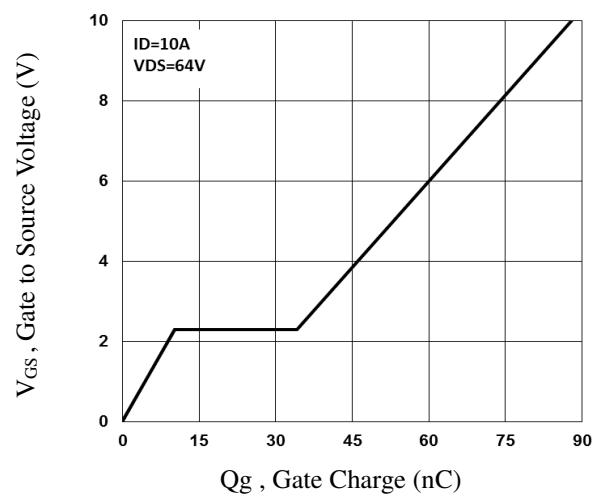
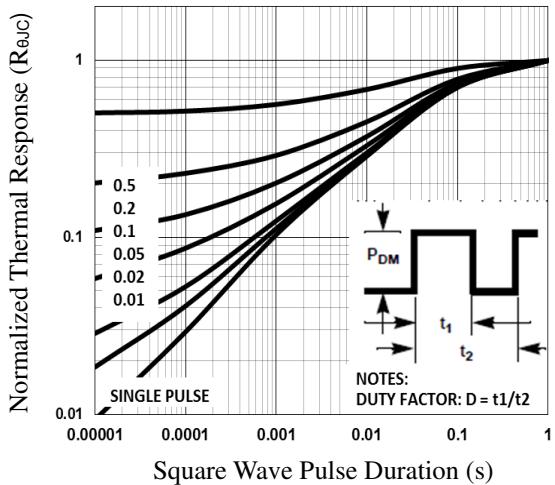
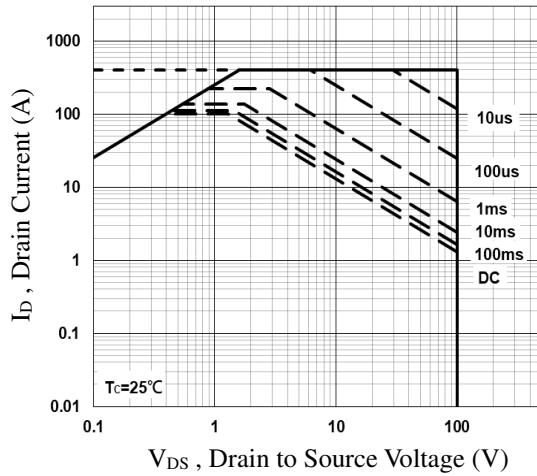
Q <sub>g</sub>	Total Gate Charge <sup>3, 4</sup>	V <sub>DS</sub> =64V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A	---	88	132	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3, 4</sup>		---	10.2	15	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3, 4</sup>		---	24	32	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3, 4</sup>	V <sub>DD</sub> =40V, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω I <sub>D</sub> =1A	---	20	40	ns
T <sub>r</sub>	Rise Time <sup>3, 4</sup>		---	13	26	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>3, 4</sup>		---	36	72	
T <sub>f</sub>	Fall Time <sup>3, 4</sup>		---	18	36	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, F=1MHz	---	5160	10200	pF
C <sub>oss</sub>	Output Capacitance		---	1346	2700	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	40	80	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	1.65	---	Ω

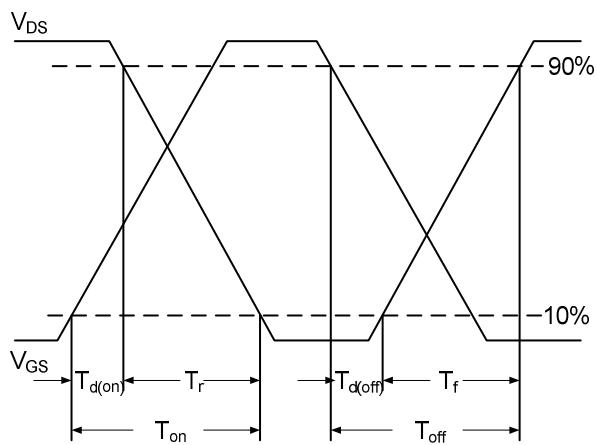
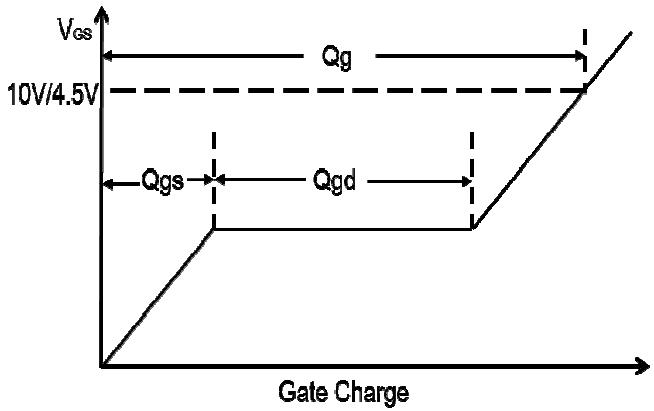
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>s</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	100	A
I <sub>SM</sub>	Pulsed Source Current		---	---	200	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>s</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V

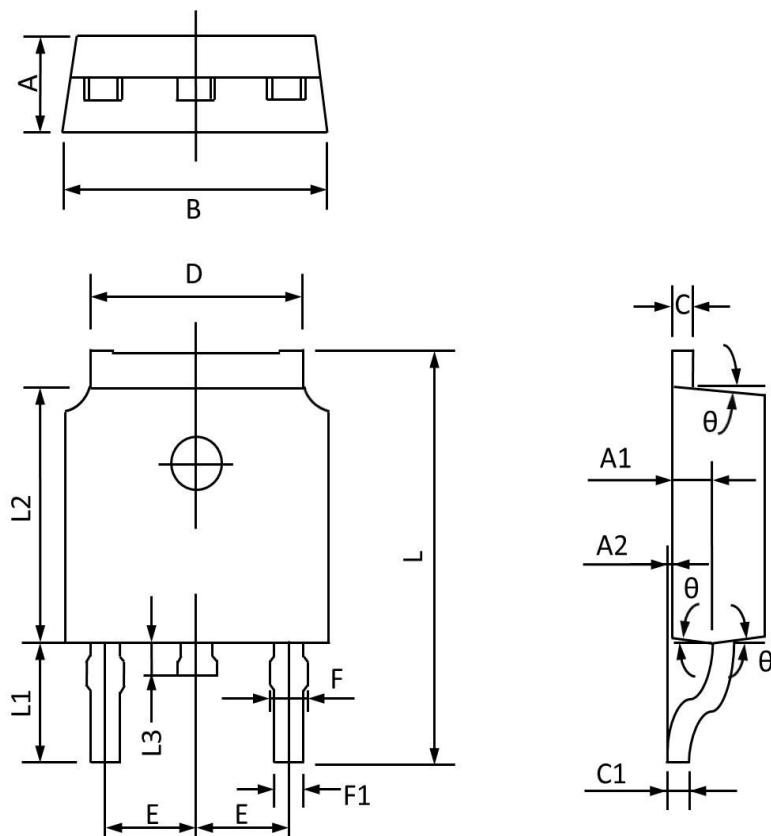
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=70A., R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**

**Fig.2 Normalized RDSON vs. T<sub>j</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**

**Fig.4 Gate Charge Characteristics**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

## TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.400	2.200	0.094	0.087
A1	1.110	0.910	0.044	0.036
A2	0.150	0.000	0.006	0.000
B	6.800	6.400	0.268	0.252
C	0.580	0.450	0.023	0.018
C1	0.580	0.460	0.023	0.018
D	5.500	5.100	0.217	0.201
E	2.386	2.186	0.094	0.086
F	0.940	0.600	0.037	0.024
F1	0.860	0.500	0.034	0.020
L	10.400	9.400	0.409	0.370
L1	3.000	2.400	0.118	0.094
L2	6.200	5.400	0.244	0.213
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°