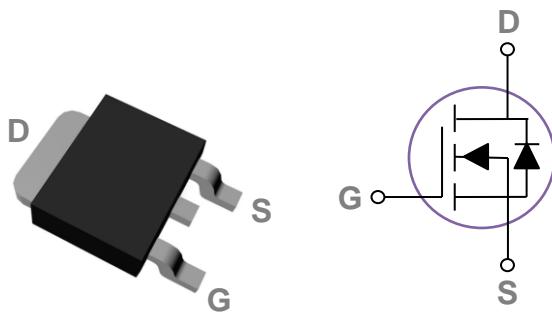


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO252 Pin Configuration



BVDSS	RDS(ON)	ID
60V	2.2mΩ	160A

Features

- 60V, 160A, RDS(ON) = 2.2mΩ @ VGS = 10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

Absolute Maximum Ratings T_c=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{Gs}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _c =25°C)	160	A
	Drain Current – Continuous (T _c =100°C)	102	A
I _{DM}	Drain Current – Pulsed ¹	640	A
EAS	Single Pulse Avalanche Energy ²	451	mJ
IAS	Single Pulse Avalanche Current ²	95	A
P _D	Power Dissipation (T _c =25°C)	133	W
	Power Dissipation – Derate above 25°C	1.06	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	0.94	°C/W



60V N-Channel MOSFETs

PDD6990B

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	60	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=85^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=20\text{A}$	---	1.8	2.2	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=10\text{A}$	---	2.7	3.5	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	1.2	1.6	2.5	V

Dynamic and switching Characteristics

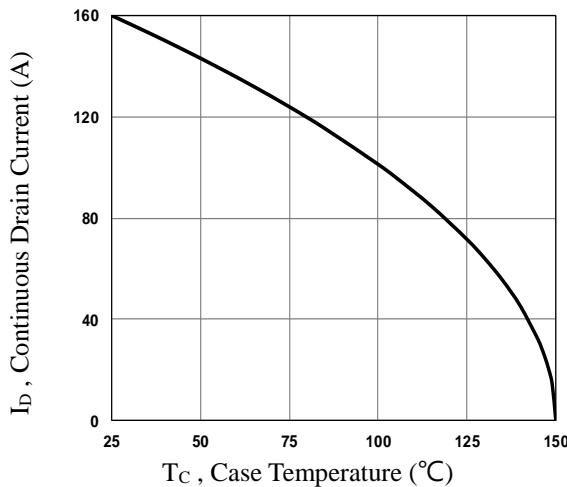
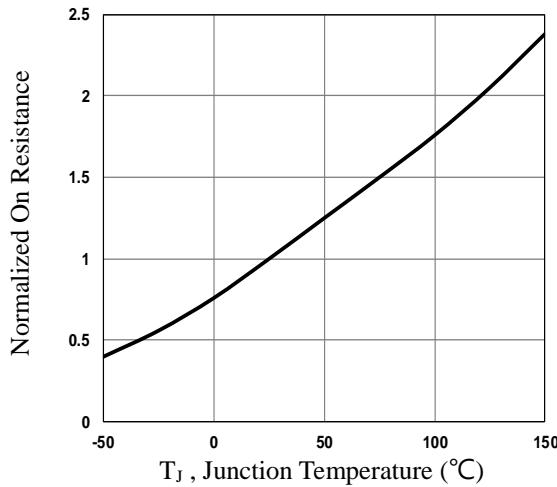
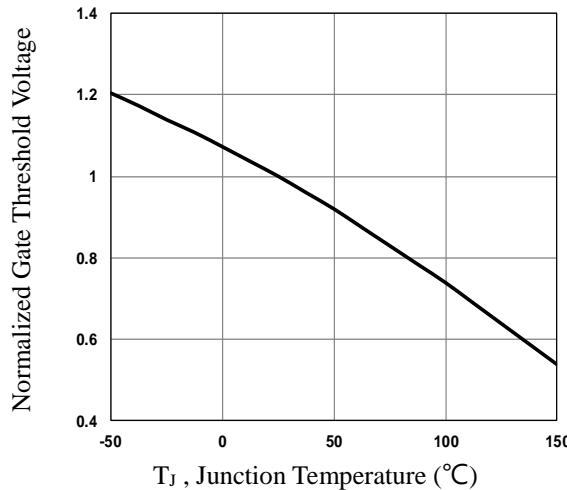
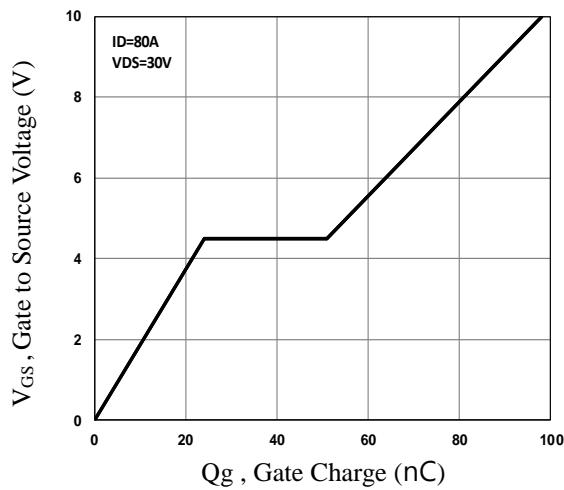
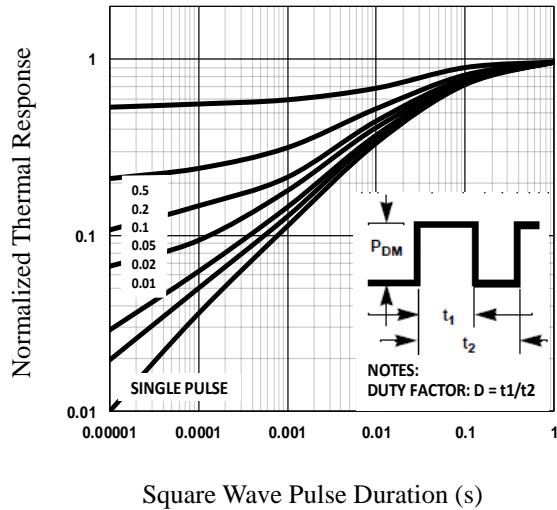
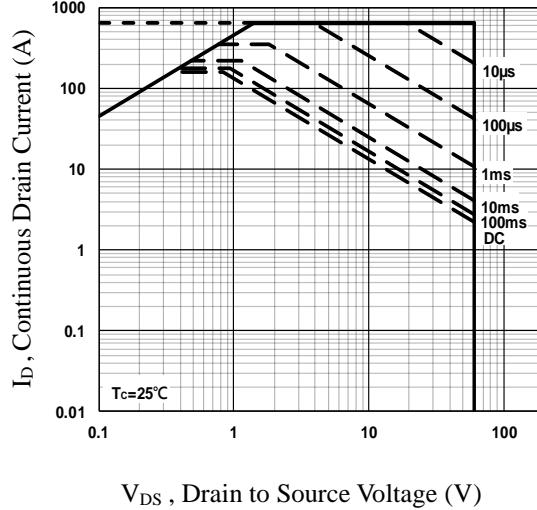
Q_g	Total Gate Charge ^{3, 4}	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=80\text{A}$	---	98	140	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	24	36	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	27	40	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{3, 4}	$V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_{\text{G}}=3.3\Omega$	---	19	38	ns
T_r	Rise Time ^{3, 4}		---	12	24	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{3, 4}		---	62	124	
T_f	Fall Time ^{3, 4}		---	130	260	
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	4930	7400	pF
C_{oss}	Output Capacitance		---	1540	2310	
C_{rss}	Reverse Transfer Capacitance		---	20	30	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	1.2	---	Ω

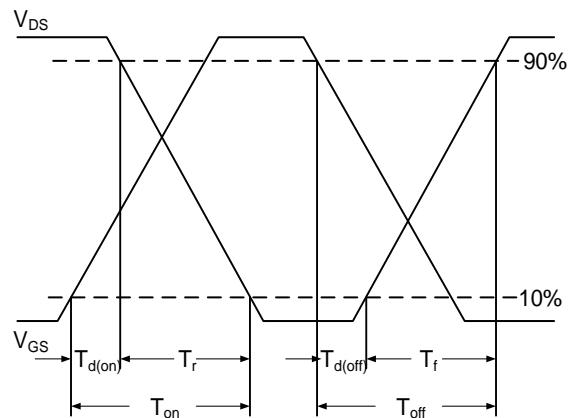
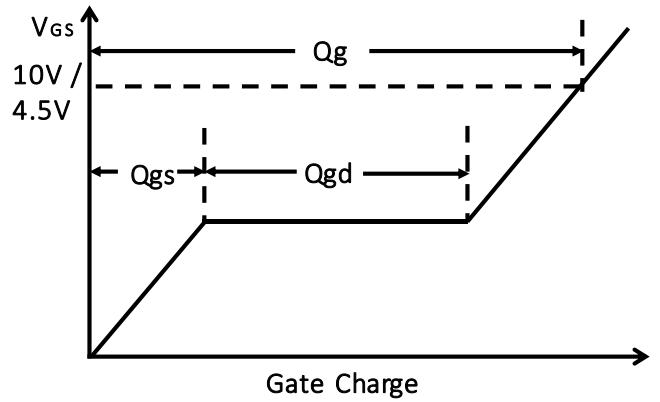
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	160	A
			---	---	320	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V
T_{rr}	Reverse Recovery Time	$V_R=30\text{V}$, $I_s=10\text{A}$	---	52	---	ns
			---	90	---	nC
Q_{rr}	Reverse Recovery Charge	$dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	---	---	

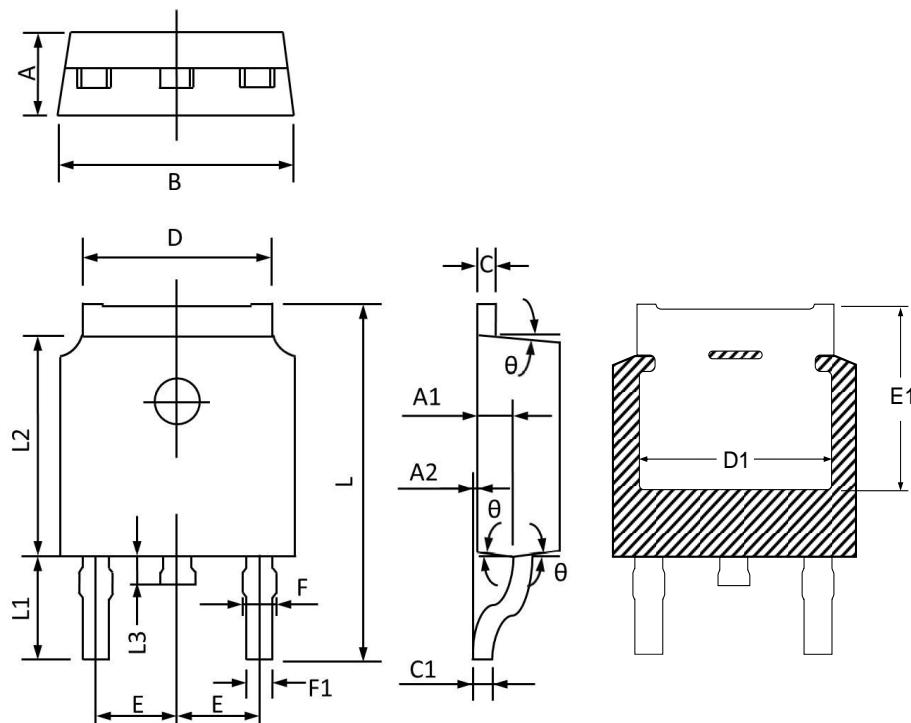
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.1\text{mH}$, $I_{\text{AS}}=95\text{A}$, $R_{\text{G}}=25\Omega$, Starting $T_J=25^\circ\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_c

Fig.2 Normalized RDSON vs. T_j

Fig.3 Normalized V_{th} vs. T_j

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area

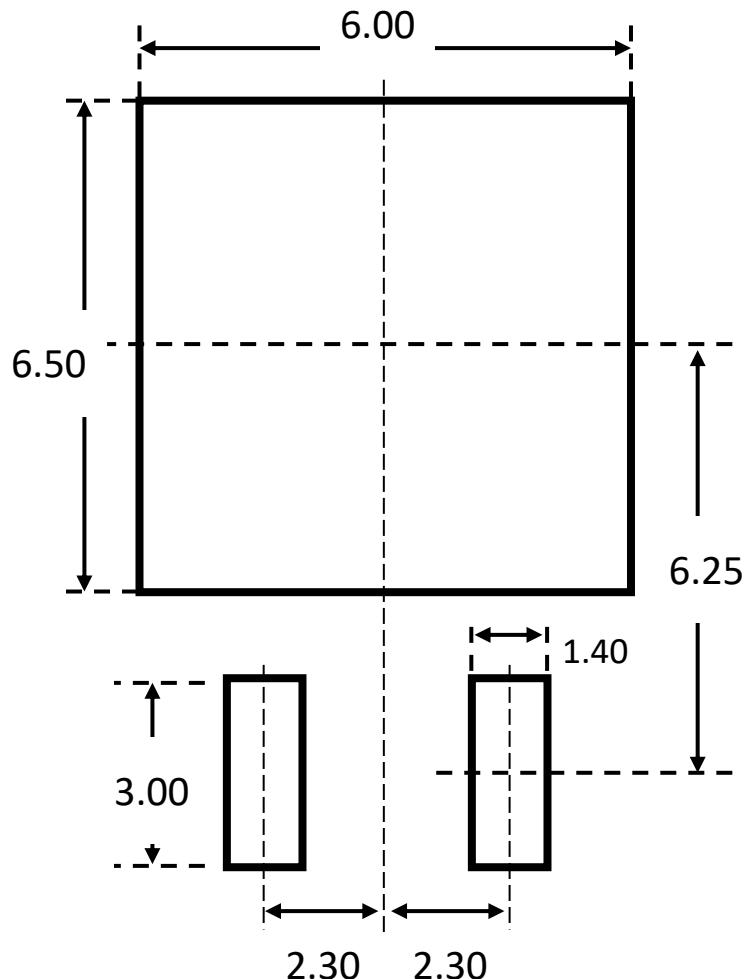

Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.450	2.150	0.096	0.085
A1	1.200	0.900	0.047	0.035
A2	0.250	0.000	0.010	0.000
B	6.800	6.300	0.268	0.248
C	0.600	0.350	0.024	0.014
C1	0.600	0.380	0.024	0.015
D	5.500	5.100	0.217	0.201
D1	5.400	4.950	0.212	0.195
E	2.400	2.000	0.094	0.079
E1	5.650	4.950	0.222	0.194
F	1.150	0.600	0.045	0.024
F1	0.900	0.500	0.035	0.020
L	10.400	9.400	0.409	0.370
L1	3.100	2.400	0.122	0.094
L2	6.300	5.300	0.248	0.209
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°

TO252 RECOMMENDED LAND PATTERN



unit : mm