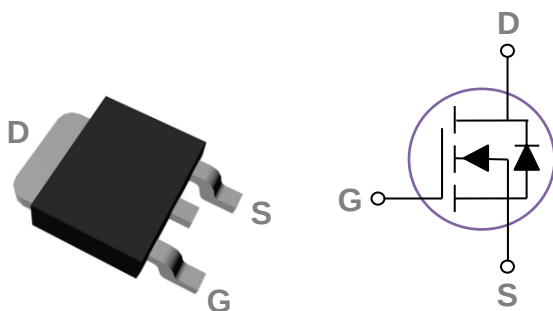


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO252 Pin Configuration



BVDSS	RDS(ON)	ID
65V	3.8mΩ	100A

Features

- 65V,100A, $R_{DS(ON)} = 3.8m\Omega @ V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	65	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	100	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	64	A
I_{DM}	Drain Current – Pulsed ¹	400	A
EAS	Single Pulse Avalanche Energy ²	273	mJ
IAS	Single Pulse Avalanche Current ²	74	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	94	W
	Power Dissipation – Derate above 25°C	0.75	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.33	$^\circ\text{C/W}$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	65	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=60V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=48V, V_{GS}=0V, T_J=85^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=30A$	---	3.2	3.8	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2	2.8	4	V
gfs	Forward Transconductance	$V_{DS}=10V, I_D=3A$	---	13	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=30V, V_{GS}=10V, I_D=50A$	---	49	75	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	11	20	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	18	30	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=30V, V_{GS}=10V, R_G=6\Omega$ $I_D=50A$	---	15	25	ns
T_r	Rise Time ^{3, 4}		---	22	35	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	54	80	
T_f	Fall Time ^{3, 4}		---	84	130	
C_{iss}	Input Capacitance	$V_{DS}=30V, V_{GS}=0V, F=1\text{MHz}$	---	2640	4000	pF
C_{oss}	Output Capacitance		---	830	1250	
C_{rss}	Reverse Transfer Capacitance		---	40	60	
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	---	1.5	---	Ω

Guaranteed Avalanche Energy

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy	$V_{DD}=40V, L=0.1\text{mH}, I_{AS}=52A$	135	---	---	mJ

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	100	A
I_{SM}	Pulsed Source Current		---	---	200	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1	V
t_{rr}	Reverse Recovery Time	$V_R=50V, I_S=10A$	---	70	---	ns
Q_{rr}	Reverse Recovery Charge	$dI/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	105	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=40V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=74A, R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

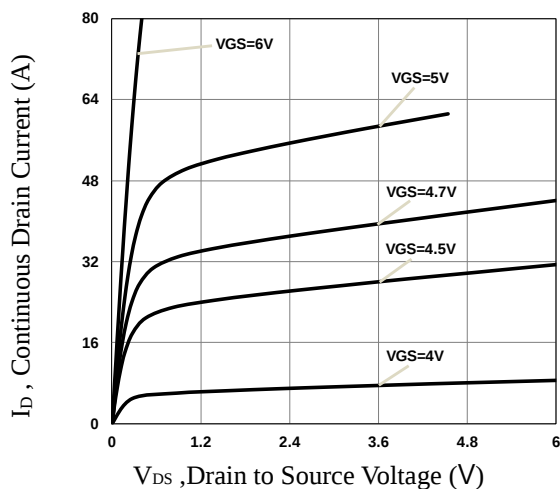


Fig.1 Typical Output Characteristics

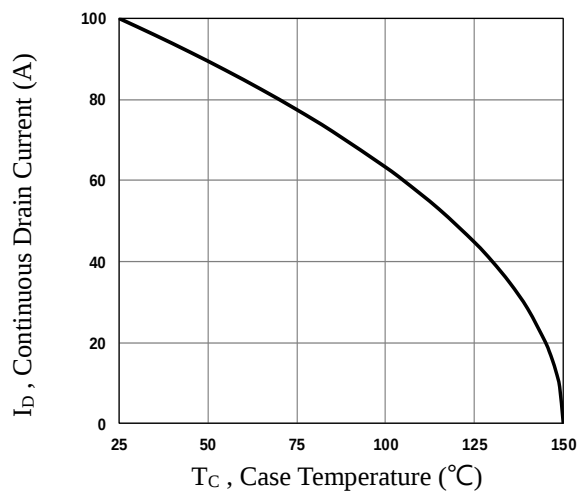


Fig.2 Continuous Drain Current vs. T_c

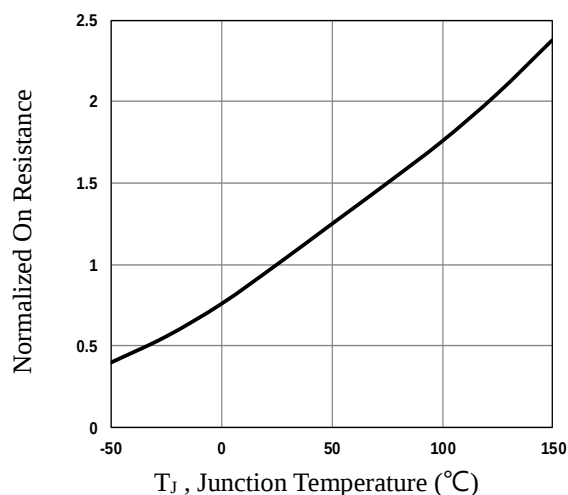


Fig.3 Normalized $R_{DS(on)}$ vs. T_J

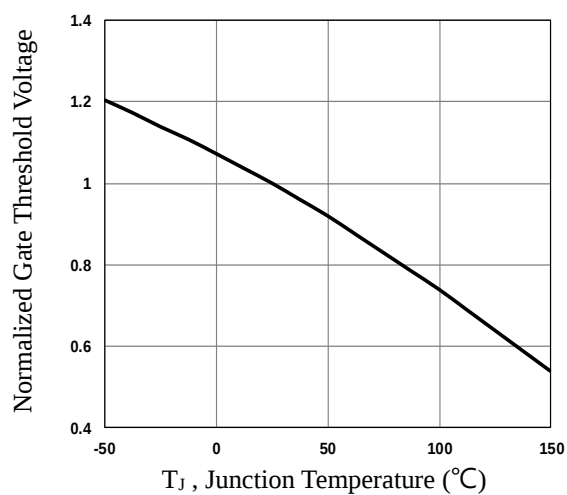


Fig.4 Normalized V_{th} vs. T_J

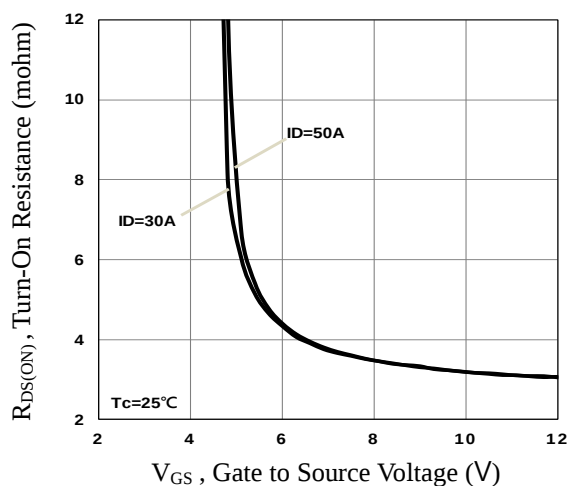


Fig.5 Turn-On Resistance vs. V_{GS}

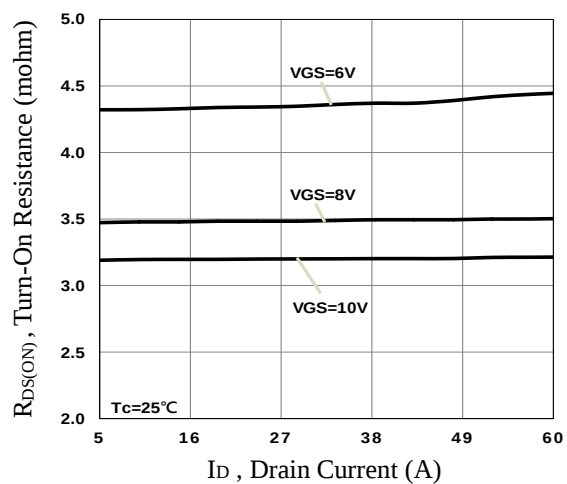
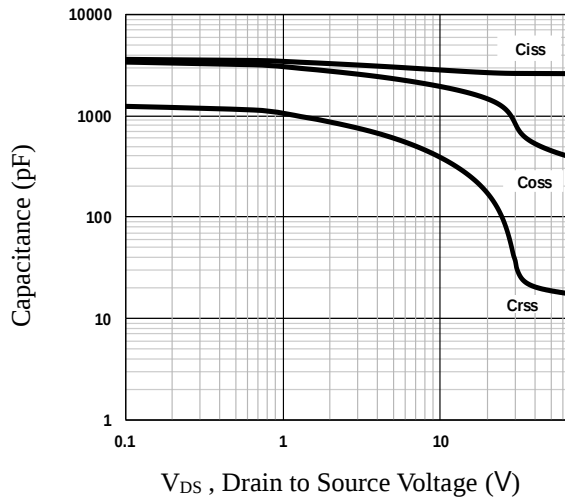
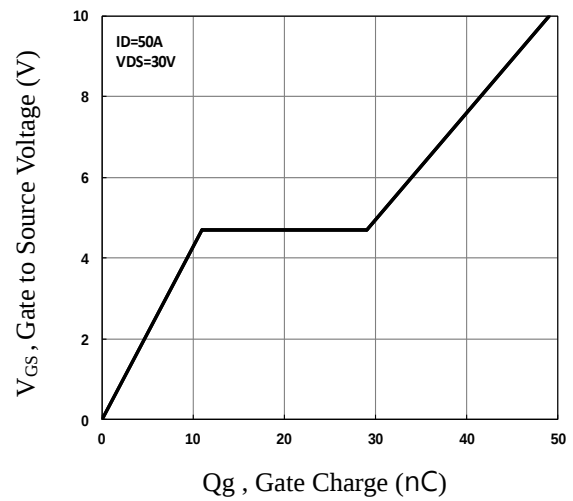
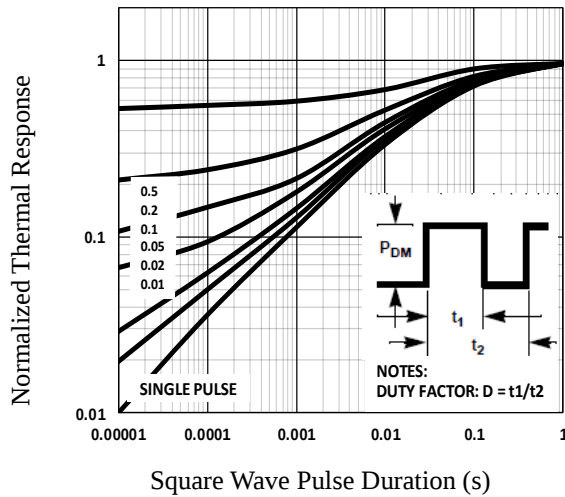
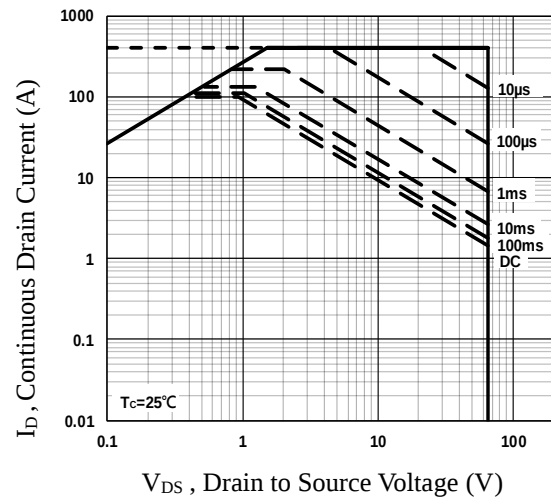
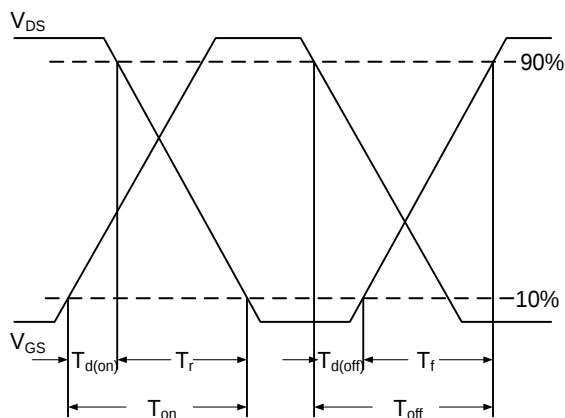
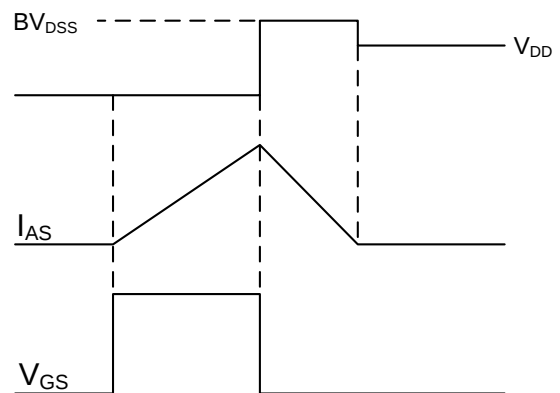
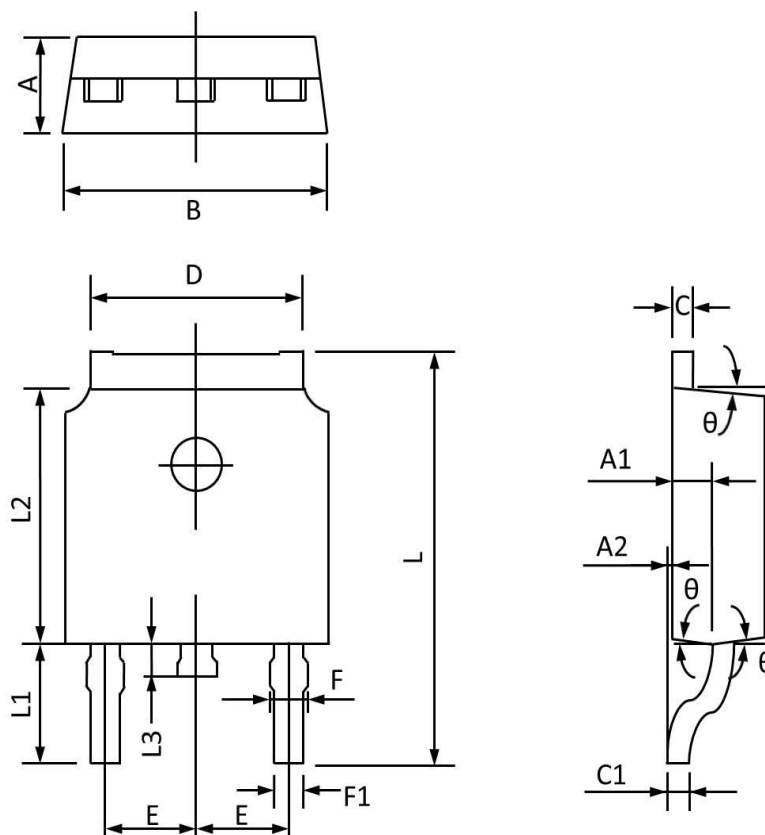


Fig.6 Turn-On Resistance vs. I_D


Fig.7 Capacitance Characteristics

Fig.8 Gate Charge Characteristics

Fig.9 Normalized Transient Impedance

Fig.10 Maximum Safe Operation Area

Fig.11 Switching Time Waveform

Fig.12 EAS Waveform

TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.450	2.150	0.096	0.085
A1	1.200	0.900	0.047	0.035
A2	0.250	0.000	0.010	0.000
B	6.800	6.300	0.268	0.248
C	0.600	0.350	0.024	0.014
C1	0.600	0.380	0.024	0.015
D	5.500	5.100	0.217	0.201
E	2.400	2.000	0.094	0.079
F	1.150	0.600	0.045	0.024
F1	0.900	0.500	0.035	0.020
L	10.400	9.400	0.409	0.370
L1	3.100	2.400	0.122	0.094
L2	6.300	5.300	0.248	0.209
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°