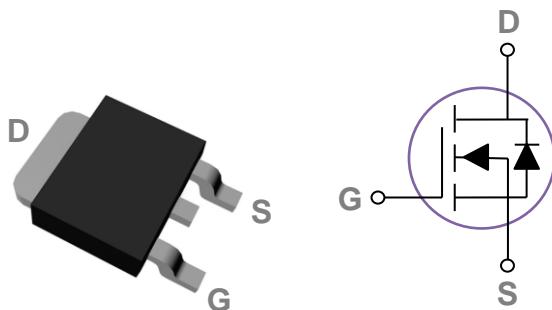


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO252 Pin Configuration



BVDSS	RDS(ON)	ID
65V	3.3mΩ	95A

Features

- 65V,95A, $RDS(ON) = 3.3m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Motor Drive
- Power Tools
- LED Lighting
- Quick Charger

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	65	V
V_{GS}	Gate-Source Voltage	+20/-12	V
I_D	Drain Current – Continuous ($T_c=25^\circ C$)	95	A
	Drain Current – Continuous ($T_c=100^\circ C$)	60	A
I_{DM}	Drain Current – Pulsed ¹	380	A
EAS	Single Pulse Avalanche Energy ²	245	mJ
IAS	Single Pulse Avalanche Current ²	70	A
P_D	Power Dissipation ($T_c=25^\circ C$)	130	W
	Power Dissipation – Derate above 25°C	1.04	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.96	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	65	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$, $I_D=1\text{mA}$	---	0.05	---	$\text{V}/\text{ }^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=60\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=48\text{V}$, $V_{GS}=0\text{V}$, $T_J=85\text{ }^{\circ}\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=20\text{V}$, $V_{DS}=0\text{V}$	---	---	100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$, $I_D=20\text{A}$	---	2.5	3.3	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=10\text{A}$	---	4.6	6	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1	1.6	2.5	V
			---	-5	---	$\text{mV}/\text{ }^{\circ}\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=10\text{V}$, $I_D=5\text{A}$	---	16	---	S

Dynamic and switching Characteristics

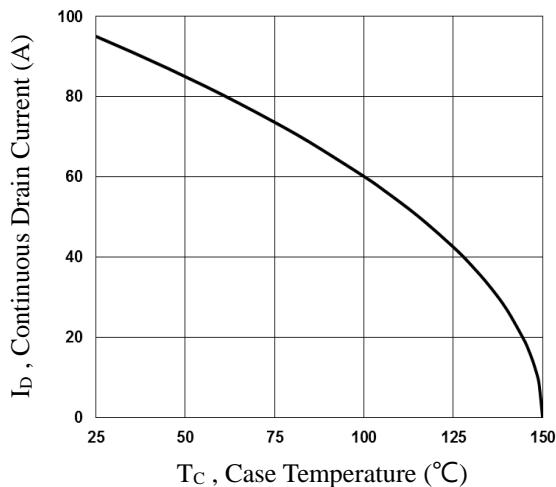
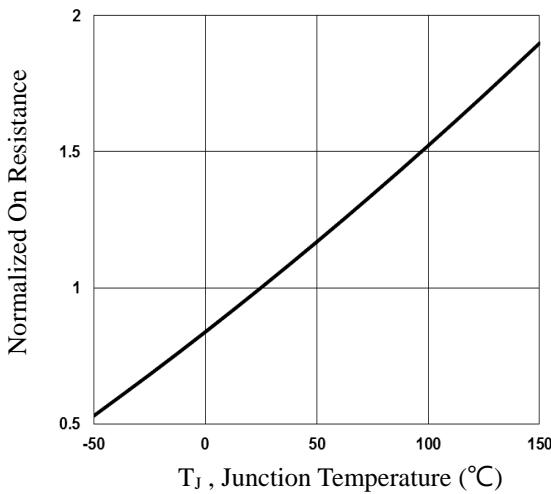
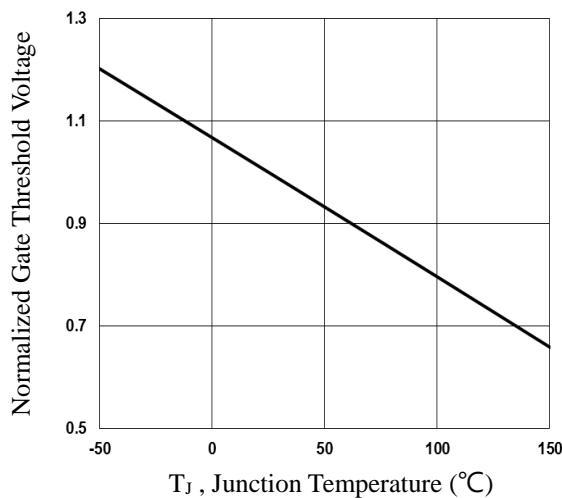
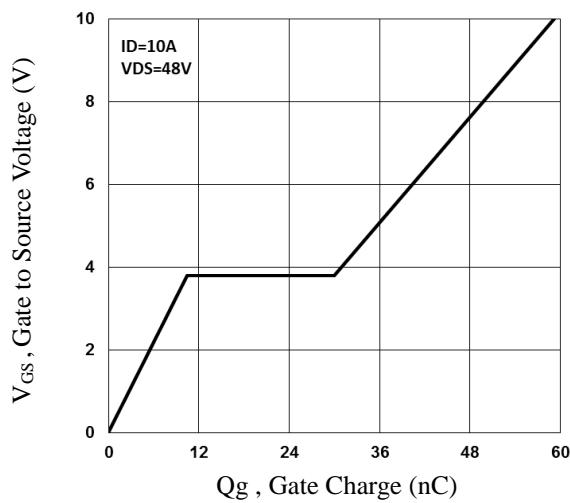
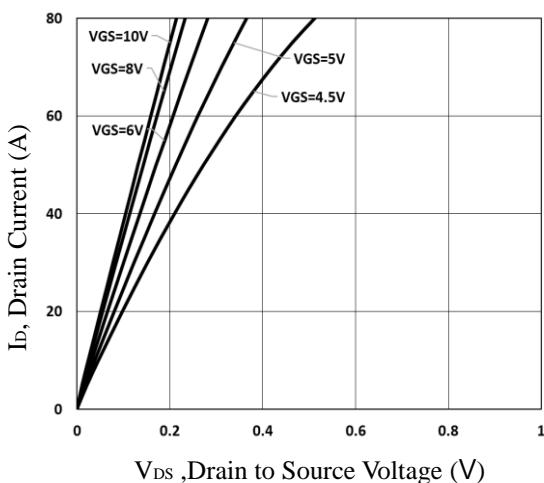
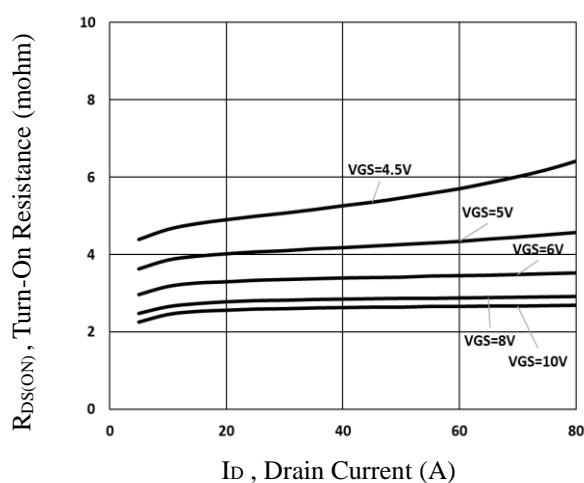
Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=48\text{V}$, $V_{GS}=10\text{V}$, $I_D=10\text{A}$	---	59	120	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	10.4	20	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	19.6	38	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=30\text{V}$, $V_{GS}=10\text{V}$, $R_G=6\Omega$ $I_D=1\text{A}$	---	22	44	ns
T_r	Rise Time ^{3, 4}		---	14	28	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	40	80	
T_f	Fall Time ^{3, 4}		---	20	40	
C_{iss}	Input Capacitance	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	5180	10360	pF
C_{oss}	Output Capacitance		---	1390	2780	
C_{rss}	Reverse Transfer Capacitance		---	55	110	
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $F=1\text{MHz}$	---	1.8	3.6	Ω

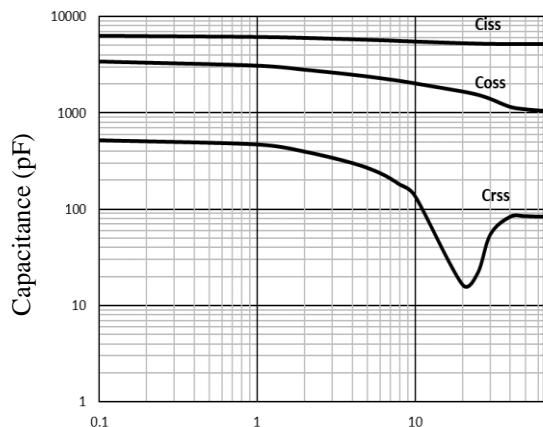
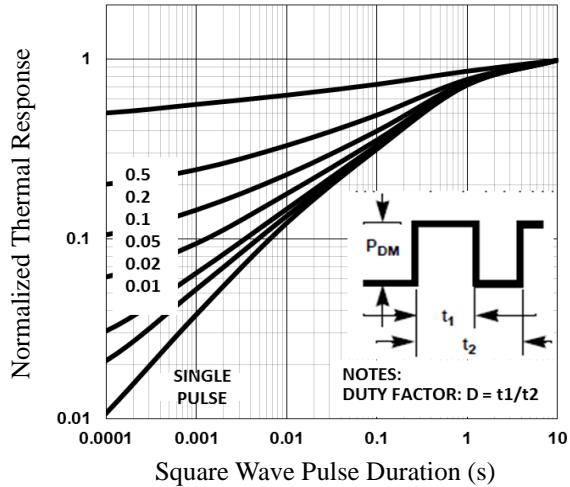
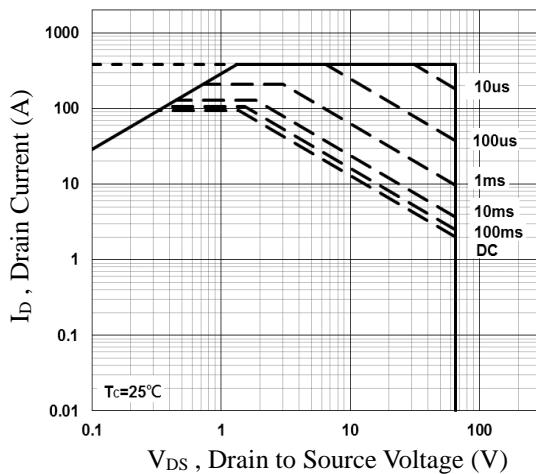
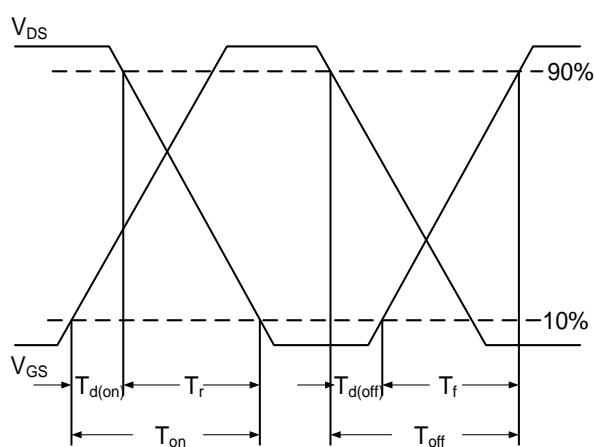
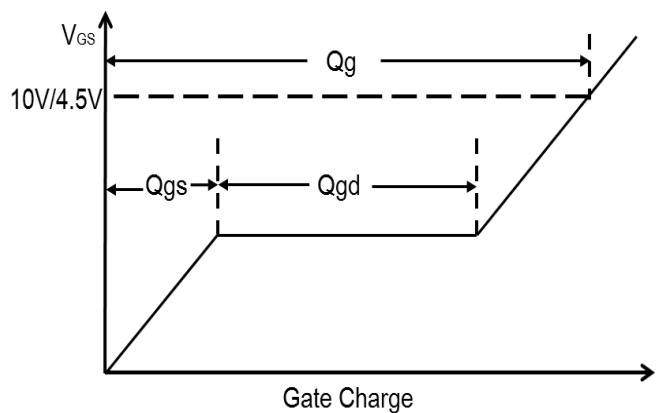
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	95	A
			---	---	190	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V
t_{rr}	Reverse Recovery Time	$V_R=50\text{V}$, $I_s=10\text{A}$ $di/dt=100\text{A}/\mu\text{s}$, $T_J=25\text{ }^{\circ}\text{C}$	---	70	---	ns
			---	140	---	nC

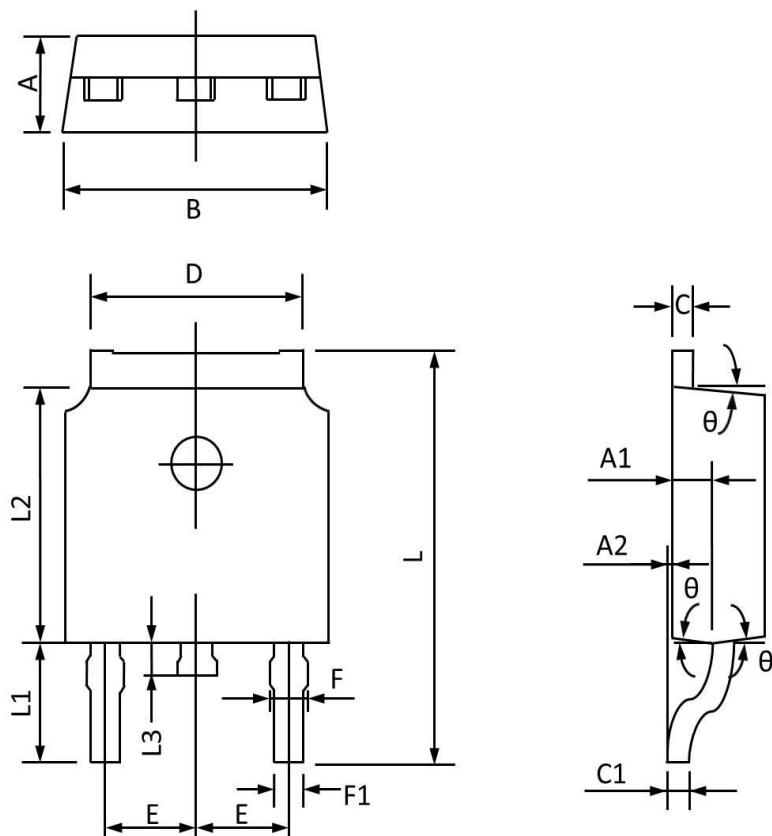
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=70\text{A}$, $R_G=25\Omega$, Starting $T_J=25\text{ }^{\circ}\text{C}$.
3. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. Tc

Fig.2 Normalized RDSON vs. Tj

Fig.3 Normalized Vth vs. Tj

Fig.4 Gate Charge Characteristics

Fig.5 Typical Output Characteristics

Fig.6 Turn-On Resistance vs. Id


Fig.7 Capacitance Characteristics

Fig.8 Normalized Transient Impedance

Fig.9 Maximum Safe Operation Area

Fig.10 Switching Time Waveform

Fig.11 Gate Charge Waveform

TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.450	2.150	0.096	0.085
A1	1.200	0.910	0.047	0.036
A2	0.150	0.000	0.006	0.000
B	6.800	6.300	0.268	0.248
C	0.580	0.350	0.023	0.014
C1	0.550	0.380	0.022	0.015
D	5.500	5.100	0.217	0.201
E	2.390	2.000	0.094	0.079
F	0.940	0.600	0.037	0.024
F1	0.860	0.500	0.034	0.020
L	10.400	9.400	0.409	0.370
L1	3.000	2.400	0.118	0.094
L2	6.200	5.300	0.244	0.209
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°