100V N-Channel MOSFETs

PDD0982

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
100V	13.2mΩ	45A

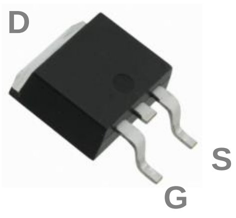
Features

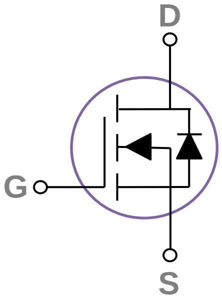
- *100V,45A, RDS(ON) =13.2mΩ@VGS = 10V*
- *Improved dv/dt capability*
- *Fast switching*
- *100% EAS Guaranteed*
- *Green Device Available*

Applications

- *Networking*
- *Load Switch*
- *LED applications*

TO252 Pin Configuration





Absolute Maximum Ratings T_C=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	+20/-12	V
I_D	Drain Current – Continuous (T_C=25℃)	45	A
	Drain Current – Continuous (T_C=100℃)	28.5	A
I_{DM}	Drain Current – Pulsed[1]	180	A
EAS	Single Pulse Avalanche Energy[2]	115	mJ
IAS	Single Pulse Avalanche Current[2]	48	A
P_D	Power Dissipation (T_C=25℃)	78	W
	Power Dissipation – Derate above 25℃	0.624	W/℃
T_{STG}	Storage Temperature Range	-50 to 150	℃
T_J	Operating Junction Temperature Range	-50 to 150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.61	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	100	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.06	---	V/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=100V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=80V , V_{GS}=0V , T_J=125℃	---	---	10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=25A	---	11	13.2	mΩ
		V_{GS}=4.5V , I_D=20A	---	15.4	20	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1	1.5	2.5	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-5.1	---	mV/℃
gfs	Forward Transconductance	V_{DS}=10V , I_D=3A	---	12	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge^{3,4}	V_{DS}=50V , V_{GS}=10V , I_D=10A	---	27.8	55	nC
Q_{gs}	Gate-Source Charge^{3,4}		---	3.5	7	
Q_{gd}	Gate-Drain Charge^{3,4}		---	8.8	17	
$T_{d(on)}$	Turn-On Delay Time^{3,4}	V_{DD}=50V , V_{GS}=10V , R_G=6Ω I_D=1A	---	14.2	28	ns
T_r	Rise Time^{3,4}		---	20.8	42	
$T_{d(off)}$	Turn-Off Delay Time^{3,4}		---	42	84	
T_f	Fall Time^{3,4}		---	30	60	
C_{iss}	Input Capacitance	V_{DS}=50V , V_{GS}=0V , F=1MHz	---	1640	3280	pF
C_{oss}	Output Capacitance		---	240	480	
C_{rss}	Reverse Transfer Capacitance		---	4	10	
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, F=1MHz	---	1.14	---	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	45	A
I_{SM}	Pulsed Source Current		---	---	90	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1	V
t_{rr}	Reverse Recovery Time^3	I_S=10A , dI/dt=100A/µs T_J=25℃	---	43.5	---	ns
Q_{rr}	Reverse Recovery Charge^3		---	59.6	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=50V,V_{GS}=10V,L=0.1mH,I_{AS}=48A.,R_G=25Ω,Starting T_J=25℃.
3. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
4. Essentially independent of operating temperature.

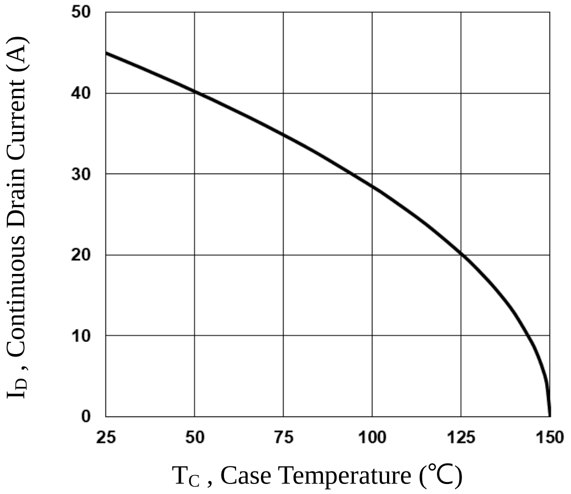


Fig.1 Continuous Drain Current vs. T_C

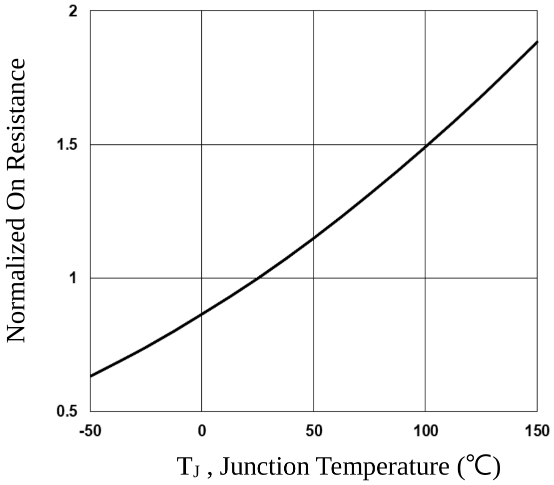


Fig.2 Normalized RDSON vs. T_J

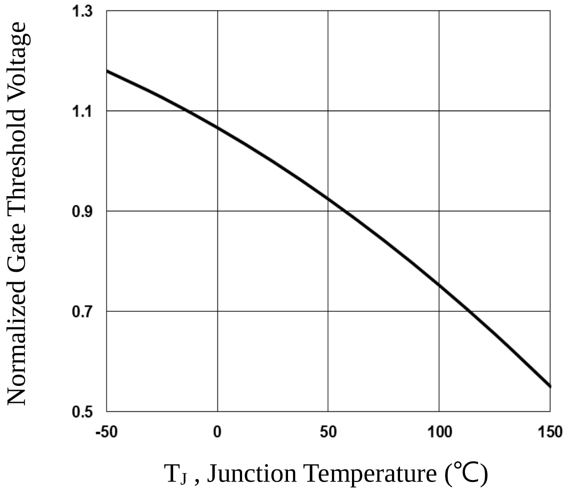


Fig.3 Normalized Vth vs. T_J

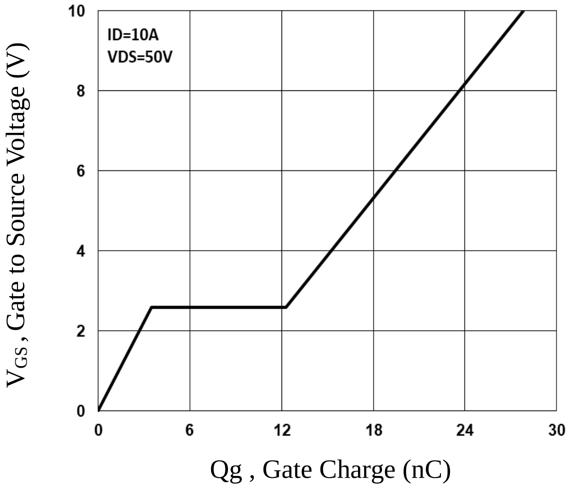


Fig.4 Gate Charge Characteristics

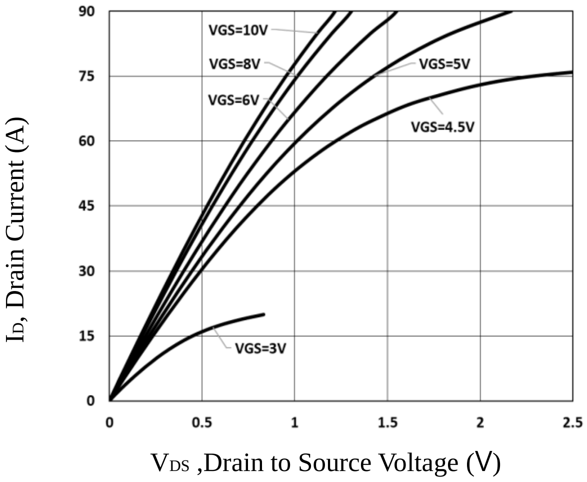


Fig.5 Typical Output Characteristics

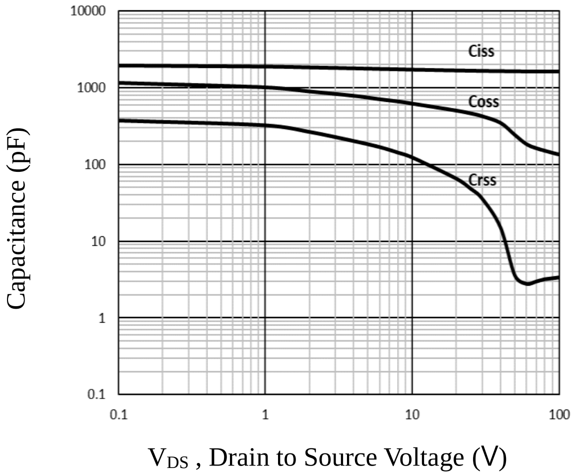


Fig.6 Capacitance Characteristics

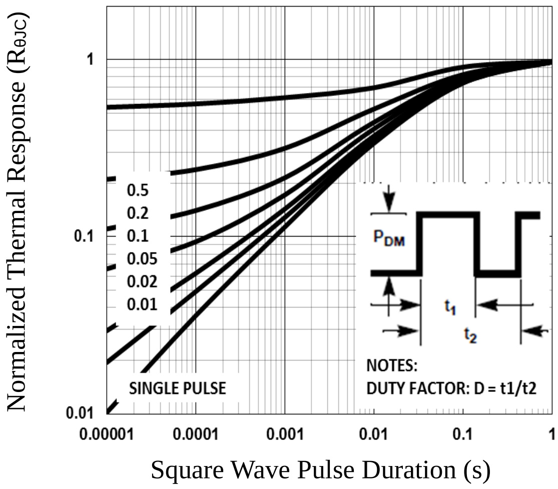


Fig.7 Normalized Transient Impedance

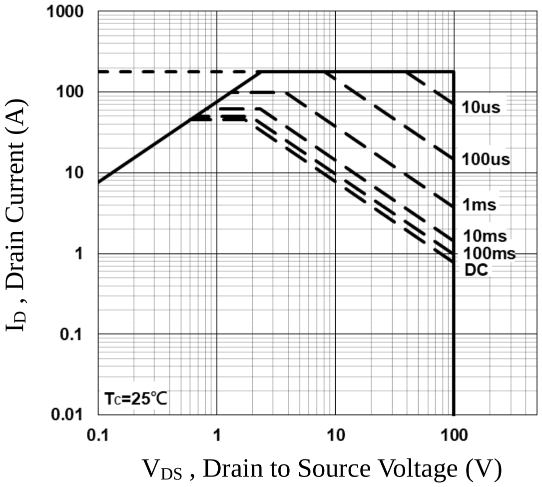


Fig.8 Maximum Safe Operation Area

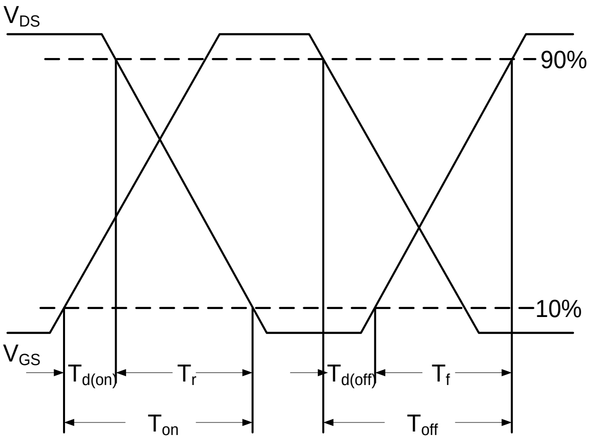


Fig.9 Switching Time Waveform

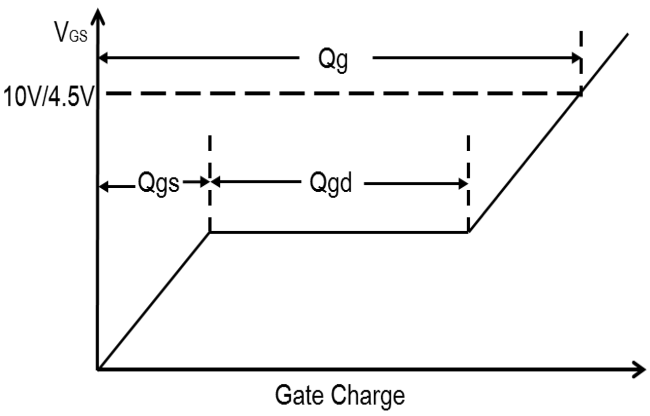


Fig.10 Gate Charge Waveform

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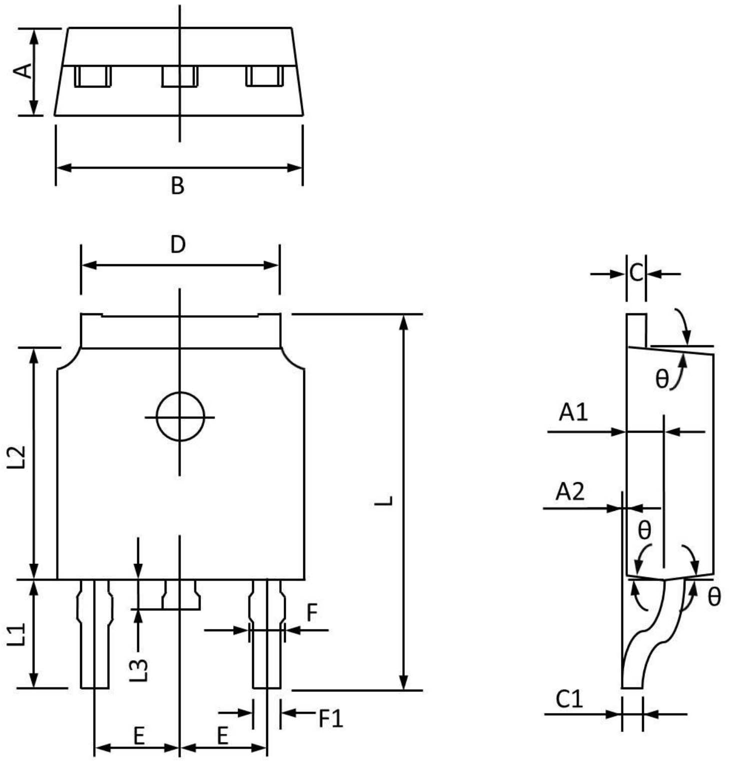
TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.20	2.40	0.087	0.094
A1	0.91	1.11	0.036	0.044
A2	0.00	0.15	0.000	0.006
B	6.50	6.70	0.256	0.264
C	0.46	0.580	0.018	0.230
C1	0.46	0.580	0.018	0.030
D	5.10	5.46	0.201	0.215
E	2.186	2.386	0.086	0.094
F	0.74	0.94	0.029	0.037
F1	0.660	0.860	0.026	0.034
L	9.80	10.40	0.386	0.409
L1	2.9REF		0.114REF	
L2	6.00	6.20	0.236	0.244
L3	0.60	1.00	0.024	0.039
θ	3˚	9˚	3˚	9˚