

General Description

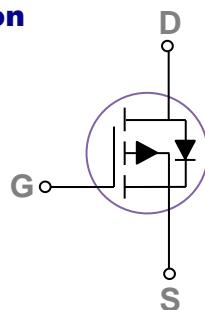
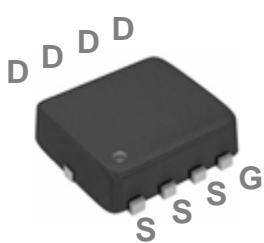
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-40V	14mΩ	-38A

Features

- -40V,-38A, $RDS(ON) = 14m\Omega @ VGS = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

PPAK3x3 Pin Configuration



Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ C$)	-38	A
	Drain Current – Continuous ($T_c=100^\circ C$)	-24	A
I_{DM}	Drain Current – Pulsed ¹	-152	A
EAS	Single Pulse Avalanche Energy ²	130	mJ
IAS	Single Pulse Avalanche Current ²	51	A
P_D	Power Dissipation ($T_c=25^\circ C$)	52	W
	Power Dissipation – Derate above 25°C	0.42	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.4	$^\circ C/W$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-40	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-40\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{\text{DS}}=-32\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-15\text{A}$	---	11.3	14	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-8\text{A}$	---	15.6	21	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.6	-2.5	V
gfs	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_{\text{D}}=-4\text{A}$	---	11	---	S

Dynamic and switching Characteristics

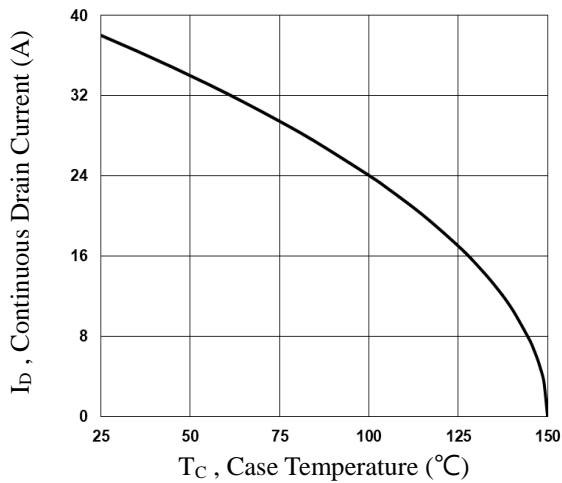
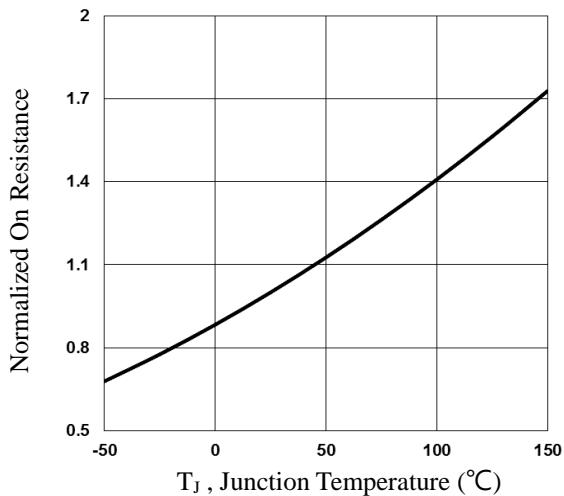
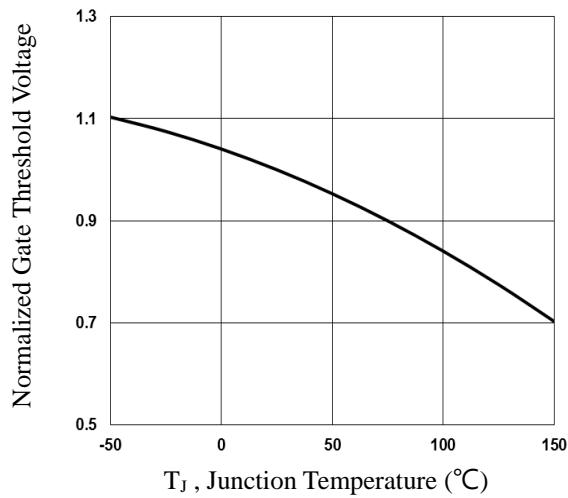
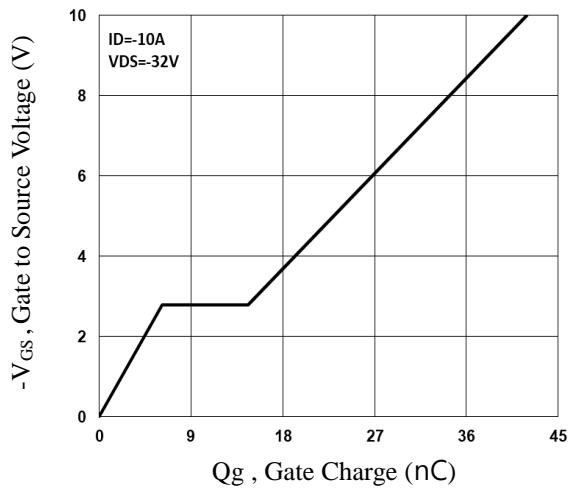
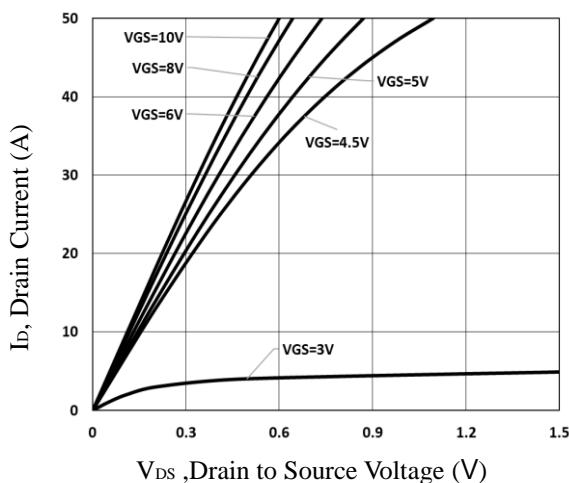
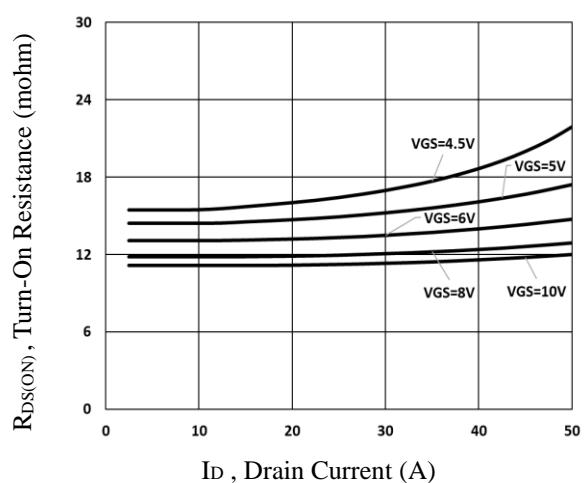
Q_g	Total Gate Charge ^{3, 4}	$V_{\text{DS}}=-32\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-10\text{A}$	---	22.2	40	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	8.2	16	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	8.8	16	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{3, 4}	$V_{\text{DD}}=-20\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_{\text{G}}=6\Omega$	---	23	40	ns
T_r	Rise Time ^{3, 4}		---	10	20	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{3, 4}		---	135	250	
T_f	Fall Time ^{3, 4}		---	46	90	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-25\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	2757	4000	pF
C_{oss}	Output Capacitance		---	240	360	
C_{rss}	Reverse Transfer Capacitance		---	137	200	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-38	A
			---	---	-76	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.1\text{mH}$, $I_{\text{AS}}=51\text{A}$, $R_{\text{G}}=25\Omega$, Starting $T_J=25^\circ\text{C}$.
3. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_c

Fig.2 Normalized RD_{ON} vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Typical Output Characteristics

Fig.6 Turn-On Resistance vs. I_D

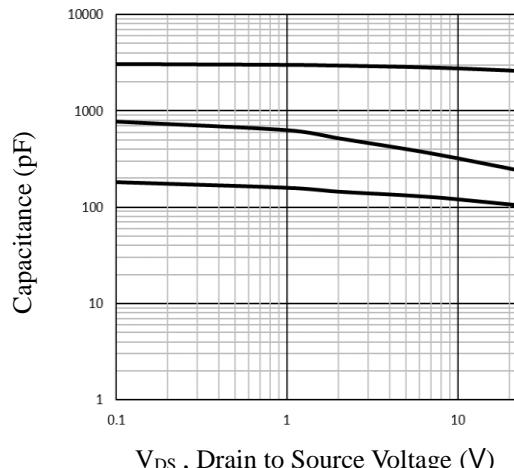


Fig.7 Capacitance Characteristics

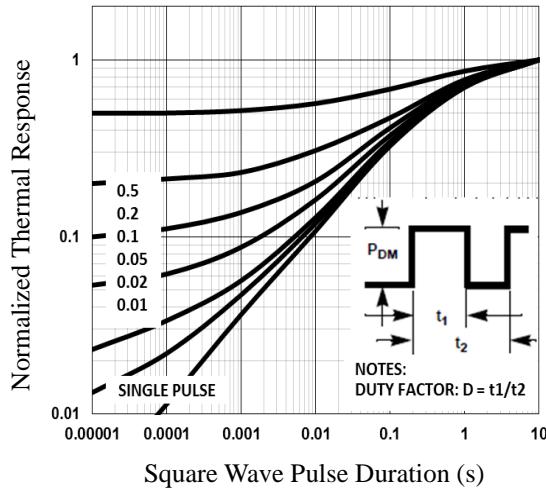


Fig.8 Normalized Transient Impedance

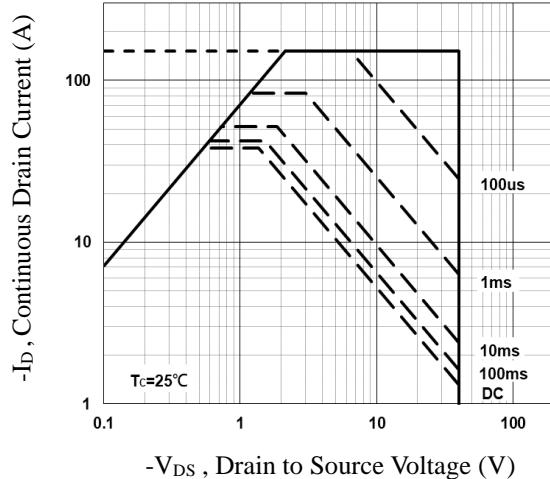


Fig.9 Maximum Safe Operation Area

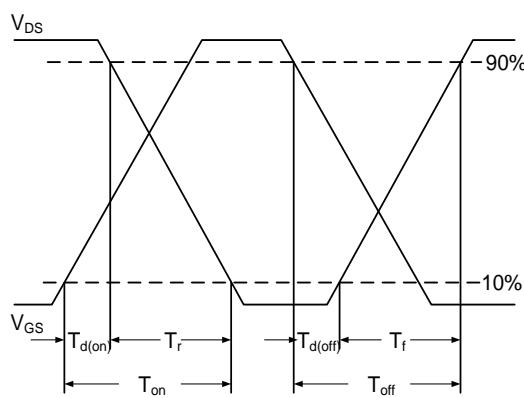


Fig.10 Switching Time Waveform

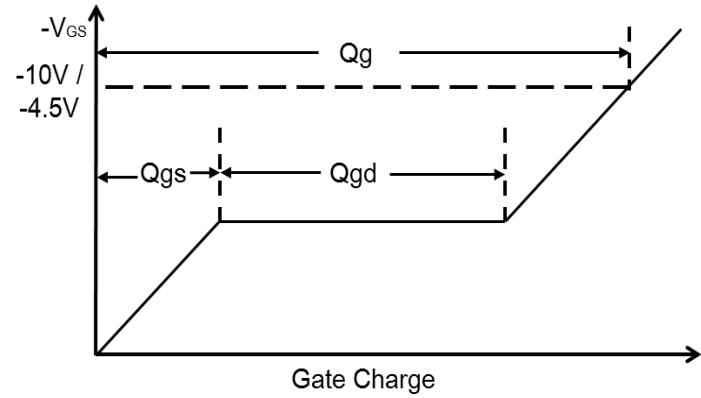
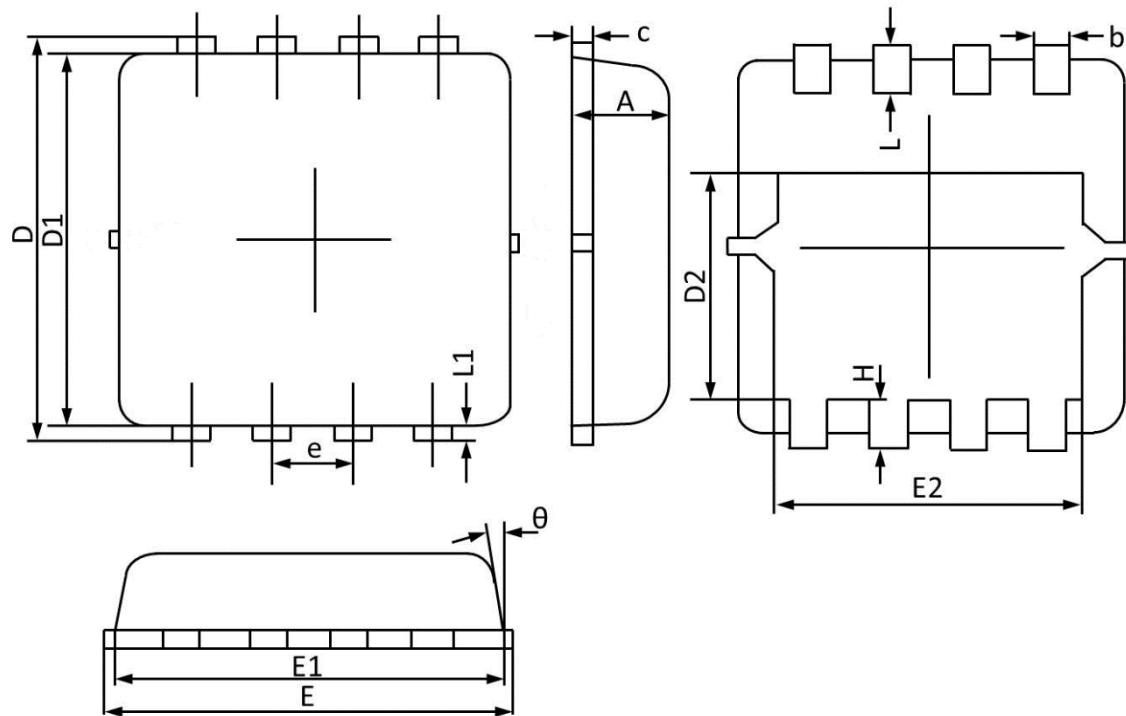


Fig.11 Gate Charge Waveform

PPAK3x3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
c	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
e	0.65BSC		0.026BSC	
H	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
θ	14°	6°	14°	6°

PPAK3X3 RECOMMENDED LAND PATTERN