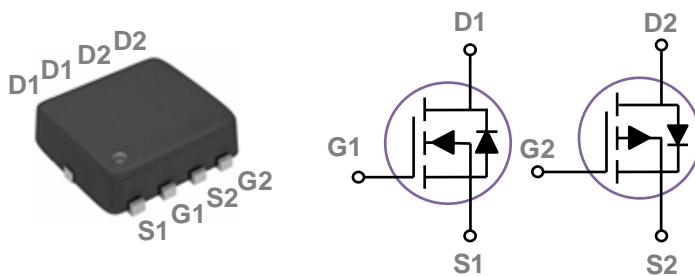


General Description

These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

PPAK3X3 Dual Pin Configuration



BVDSS	RDSON	ID
30V	20mΩ	12A
-30V	18mΩ	-16A

Features

- Fast switching
- Green Device Available
- Suit for 4.5V Gate Drive Applications

Applications

- DC Fan
- Motor Drive Applications
- Networking
- Half / Full Bridge Topology

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating		Units
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	12	-16	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	7.6	-10.1	A
I_{DM}	Drain Current – Pulsed ¹	48	-64	A
EAS	Single Pulse Avalanche Energy ^{2,6}	32	63	mJ
IAS	Single Pulse Avalanche Current ²	8	35	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	20		W
	Power Dissipation – Derate above 25°C	0.16		$\text{W}/^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150		$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150		$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62.5	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	6.4	$^\circ\text{C}/\text{W}$

N-CH Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^\circ\text{C}$	---	---	1	μA
		$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$, $I_D=8\text{A}$	---	15	20	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=5\text{A}$	---	21	30	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1.2	1.5	2.5	V
			---	-4	---	$\text{mV}/\text{ }^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=5\text{V}$, $I_D=6\text{A}$	---	13	---	S

Dynamic and switching Characteristics

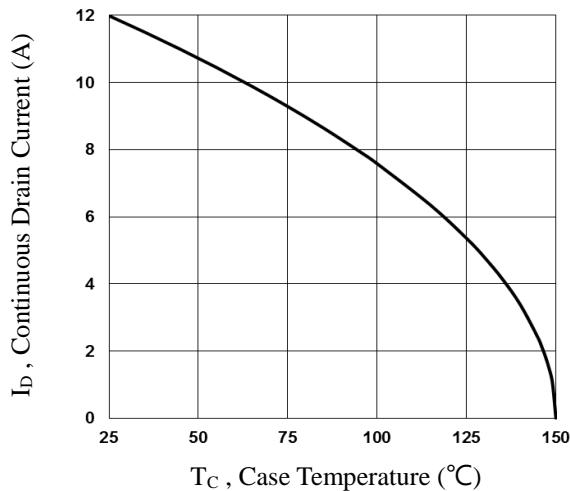
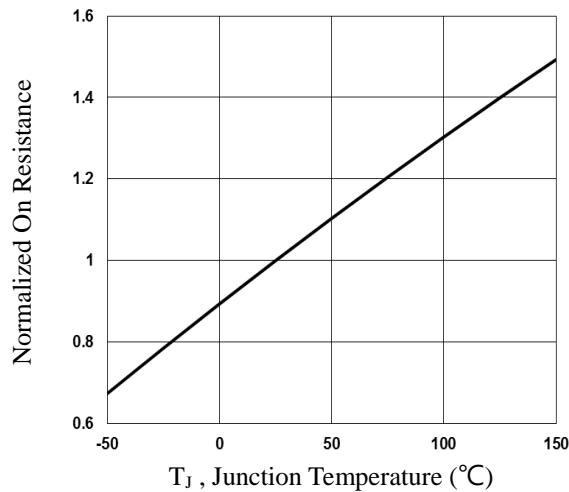
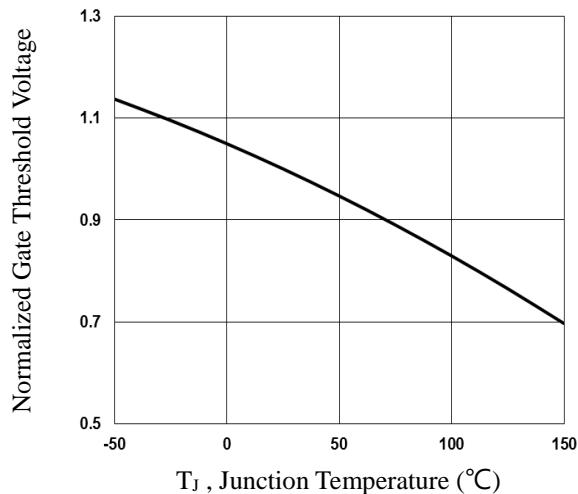
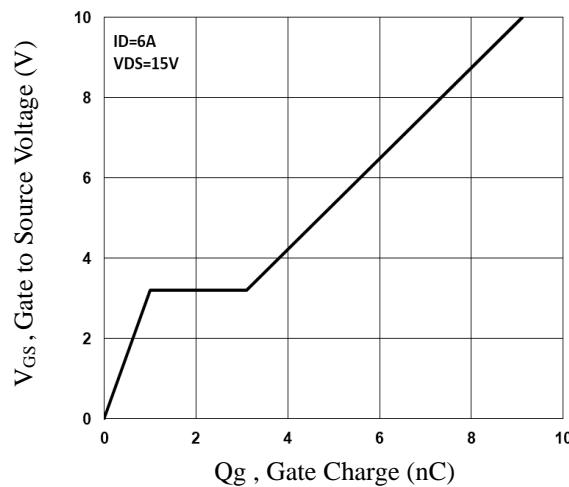
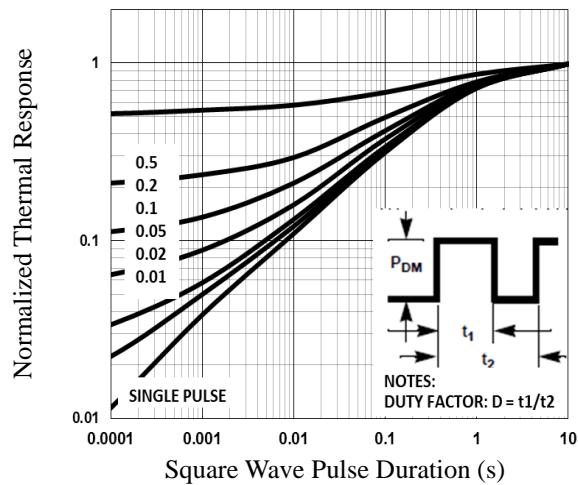
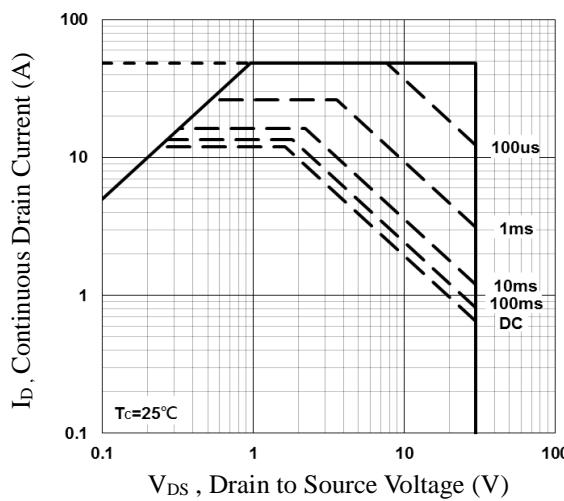
Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=15\text{V}$, $V_{GS}=10\text{V}$, $I_D=6\text{A}$	---	9.1	18	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	1	2	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	2.1	4	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=15\text{V}$, $V_{GS}=10\text{V}$, $R_G=6\Omega$ $I_D=1\text{A}$	---	2.8	5	ns
T_r	Rise Time ^{3, 4}		---	7.2	14	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	15.8	30	
T_f	Fall Time ^{3, 4}		---	4.6	9	
C_{iss}	Input Capacitance	$V_{DS}=25\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	345	500	pF
C_{oss}	Output Capacitance		---	55	80	
C_{rss}	Reverse Transfer Capacitance		---	32	55	
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $F=1\text{MHz}$	---	3.2	6.4	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	12	A
			---	---	24	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25\text{ }^\circ\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=1\text{mH}$, $I_{AS}=8\text{A}$, $R_G=25\Omega$, Starting $T_J=25\text{ }^\circ\text{C}$.
3. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. TC

Fig.2 Normalized RDSON vs. TJ

Fig.3 Normalized Vth vs. TJ

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Response

Fig.6 Maximum Safe Operation Area

P-CH Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	-30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$, $I_D=-1\text{mA}$	---	-0.03	---	$\text{V}/\text{ }^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-30\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	μA
		$V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$, $I_D=-8\text{A}$	---	14.5	18	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-5\text{A}$	---	23	30	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu\text{A}$	-1.2	-1.6	-2.5	V
			---	4	---	$\text{mV}/\text{ }^{\circ}\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=-10\text{V}$, $I_D=-3\text{A}$	---	6.8	---	S

Dynamic and switching Characteristics

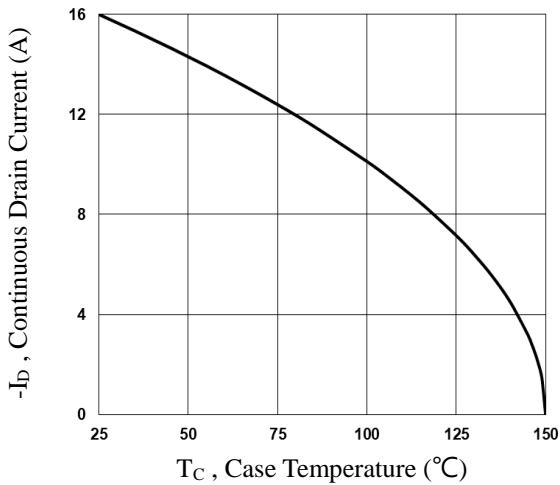
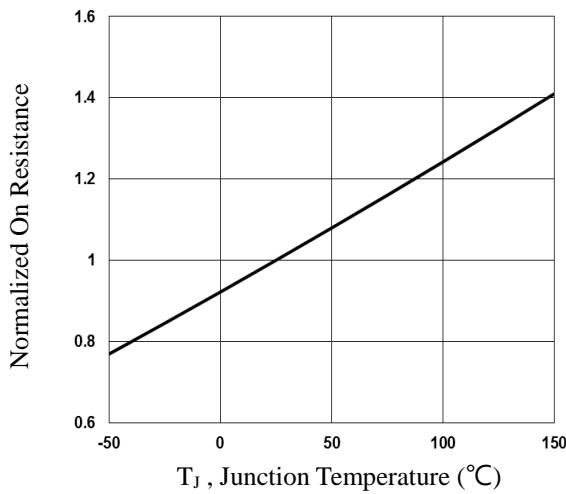
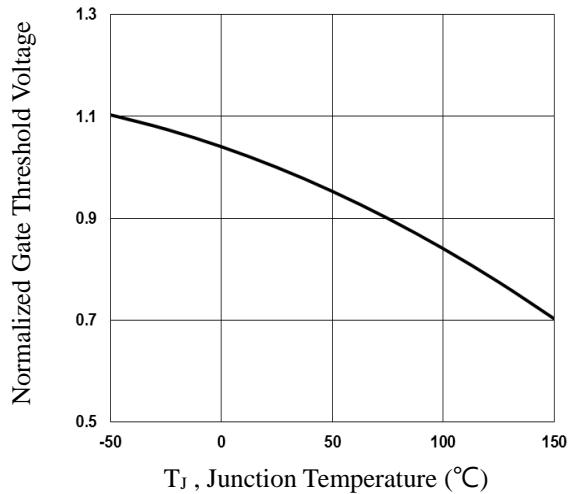
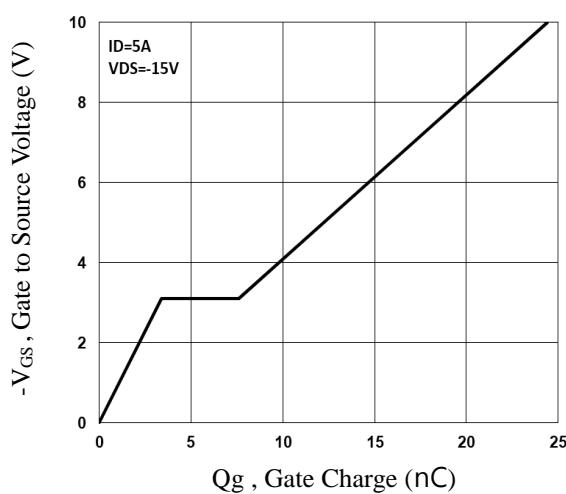
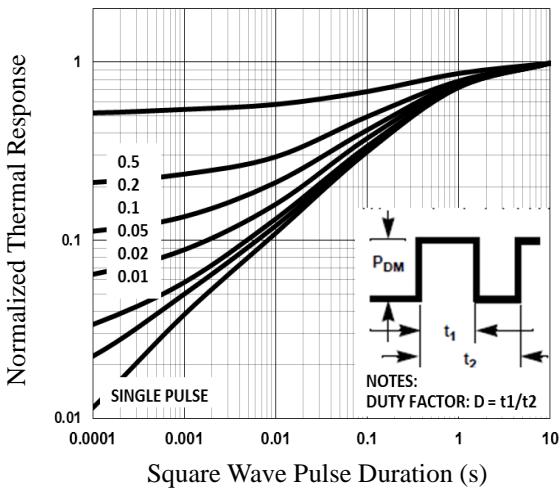
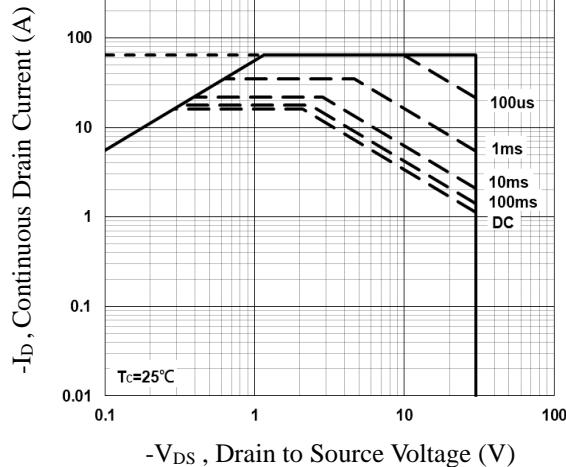
Q_g	Total Gate Charge ^{7,8}	$V_{DS}=-15\text{V}$, $V_{GS}=-10\text{V}$, $I_D=-5\text{A}$	---	24.4	48	nC
Q_{gs}	Gate-Source Charge ^{7,8}		---	3.4	6	
Q_{gd}	Gate-Drain Charge ^{7,8}		---	4.2	8	
$T_{d(on)}$	Turn-On Delay Time ^{7,8}	$V_{DD}=-15\text{V}$, $V_{GS}=-10\text{V}$, $R_G=6\Omega$ $I_D=-1\text{A}$	---	5.8	11	ns
T_r	Rise Time ^{7,8}		---	18.8	36	
$T_{d(off)}$	Turn-Off Delay Time ^{7,8}		---	46.9	90	
T_f	Fall Time ^{7,8}		---	12.3	23	
C_{iss}	Input Capacitance	$V_{DS}=-15\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	1250	2500	pF
C_{oss}	Output Capacitance		---	160	320	
C_{rss}	Reverse Transfer Capacitance		---	90	180	

Drain-Source Diode Characteristics and Maximum Ratings

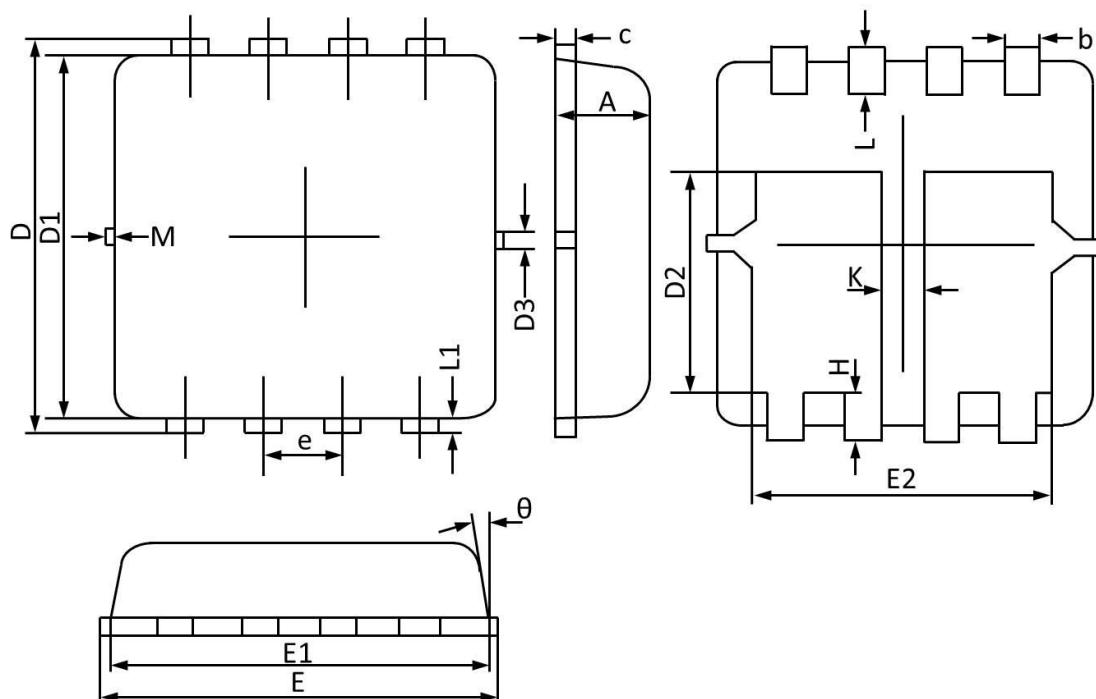
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-16	A
I_{SM}	Pulsed Source Current		---	---	-32	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	V

Note :

5. Repetitive Rating : Pulsed width limited by maximum junction temperature.
6. $V_{DD}=-25\text{V}$, $V_{GS}=-10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=-35\text{A}$, $R_G=25\Omega$, Starting $T_J=25\text{ }^{\circ}\text{C}$
7. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
8. Essentially independent of operating temperature.


Fig.7 Continuous Drain Current vs. T_c

Fig.8 Normalized RD_{ON} vs. T_J

Fig.9 Normalized V_{th} vs. T_J

Fig.10 Gate Charge Waveform

Fig.11 Normalized Transient Impedance

Fig.12 Maximum Safe Operation Area

PPAK3x3 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.670	0.880	0.026	0.035
b	0.250	0.350	0.010	0.014
c	0.100	0.250	0.004	0.010
D	3.150	3.550	0.124	0.140
D1	3.000	3.300	0.118	0.130
D2	1.500	2.000	0.059	0.079
D3	0.130	0.200	0.005	0.008
E	3.100	3.500	0.122	0.138
E1	3.000	3.200	0.118	0.126
E2	2.350	2.600	0.093	0.102
e	0.650 BSC		0.026 BSC	
H	0.300	0.500	0.012	0.020
L	0.300	0.500	0.012	0.020
L1	0.130 REF		0.005 REF	
K	0.300 REF		0.012 REF	
θ	0°	12°	0°	12°
M	0.150 REF		0.006 REF	

PPAK3X3 Dual RECOMMENDED LAND PATTERN