

General Description

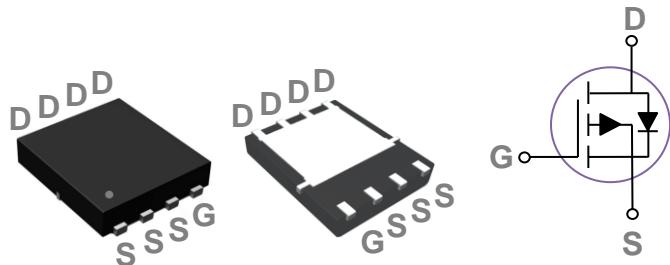
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
-20V	2.3mΩ	-90A

Features

- -20V,-90A, RDS(ON) = $2.3m\Omega$ @VGS = -10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

PPAK5X6 Pin Configuration



Applications

- Notebook
- Load Switch
- Networking
- Hand-Held Instruments

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
V _{Gs}	Gate-Source Voltage	± 12	V
I _D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	-90	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	-54	A
I _{DM}	Drain Current – Pulsed ¹	-360	A
P _D	Power Dissipation ($T_c=25^\circ\text{C}$)	41.67	W
	Power Dissipation – Derate above 25°C	0.33	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	3	°C/W

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	-20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$, $I_D=-1\text{mA}$	---	-0.008	---	V/C
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-20\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	μA
		$V_{DS}=-16\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	-30	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 12\text{V}$, $V_{DS}=0\text{V}$	---	---	± 500	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$, $I_D=-20\text{A}$	---	1.8	2.3	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-20\text{A}$	---	2.1	2.6	
		$V_{GS}=-2.5\text{V}$, $I_D=-20\text{A}$	---	2.7	3.6	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu\text{A}$	-0.4	-0.6	-1.0	V
ΔV_{GS}	$V_{GS(\text{th})}$ Temperature Coefficient		---	-3.44	---	mV/C
g_{fs}	Forward Transconductance	$V_{DS}=-10\text{V}$, $I_S=-3\text{A}$	---	30	---	S

Dynamic and switching Characteristics

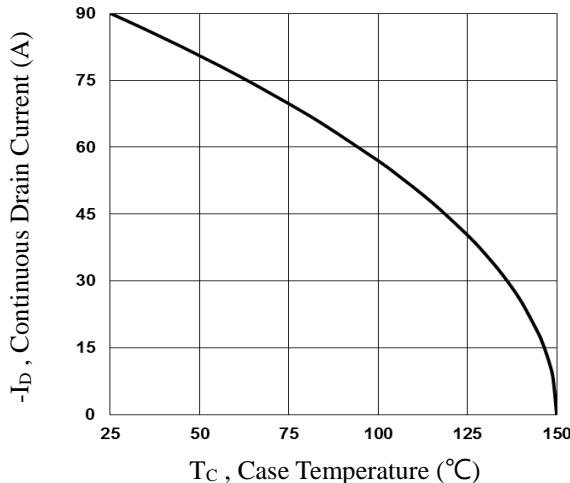
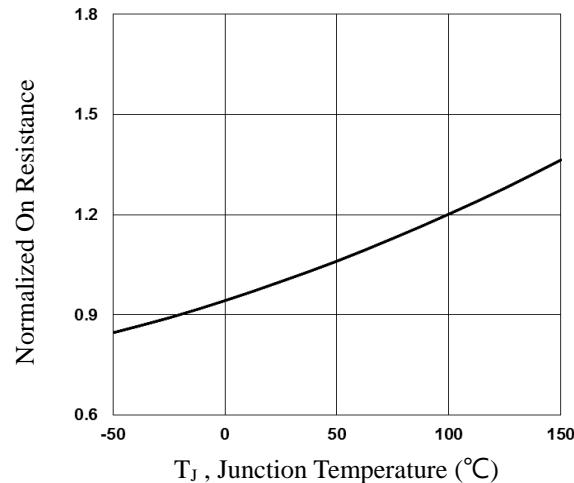
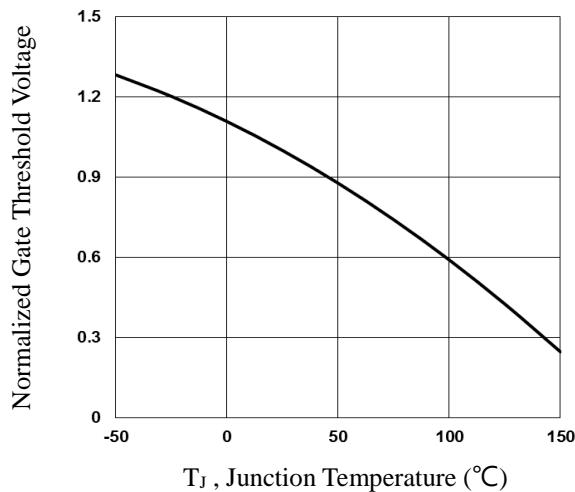
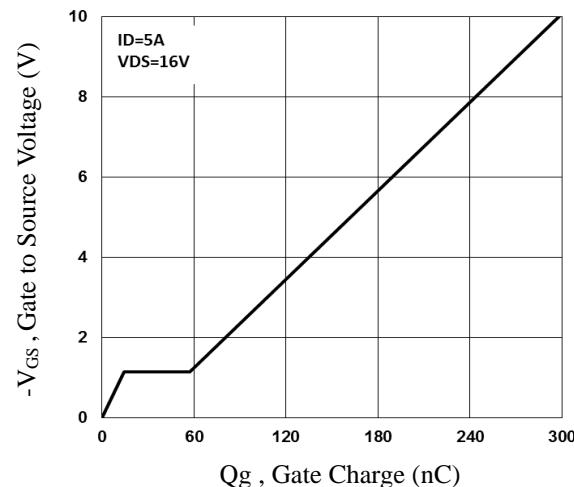
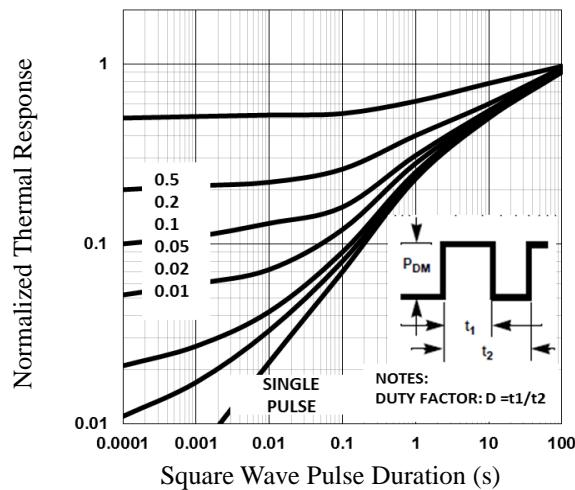
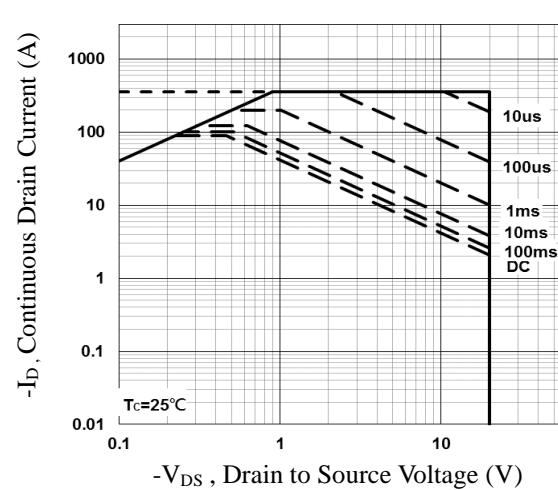
Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=-16\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-5\text{A}$	---	149	225	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	14.4	22	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	42.8	65	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=-15\text{V}$, $V_{GS}=-4.5\text{V}$, $R_G=25\Omega$	---	21.2	42	nS
T_r	Rise Time ^{2, 3}		---	20.6	40	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	26	52	
T_f	Fall Time ^{2, 3}		---	400	600	
C_{iss}	Input Capacitance	$V_{DS}=-15\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	14000	21000	pF
C_{oss}	Output Capacitance		---	1670	2500	
C_{rss}	Reverse Transfer Capacitance		---	730	1100	
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $F=1\text{MHz}$	---	2.6	---	Ω

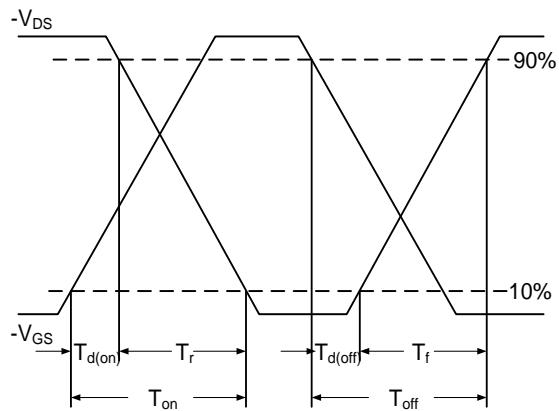
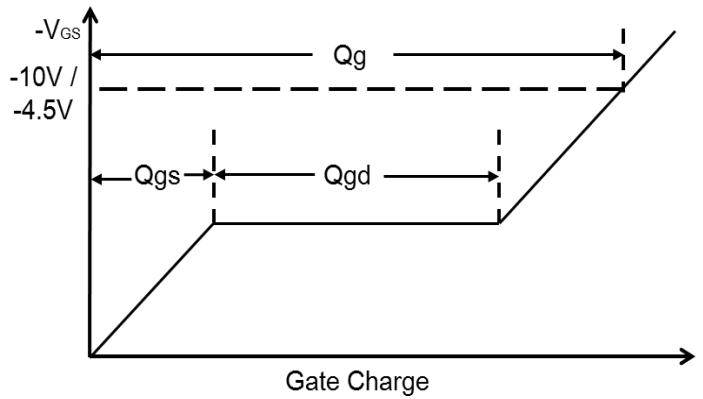
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-90	A
I_{SM}	Pulsed Source Current		---	---	-180	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	-1	V

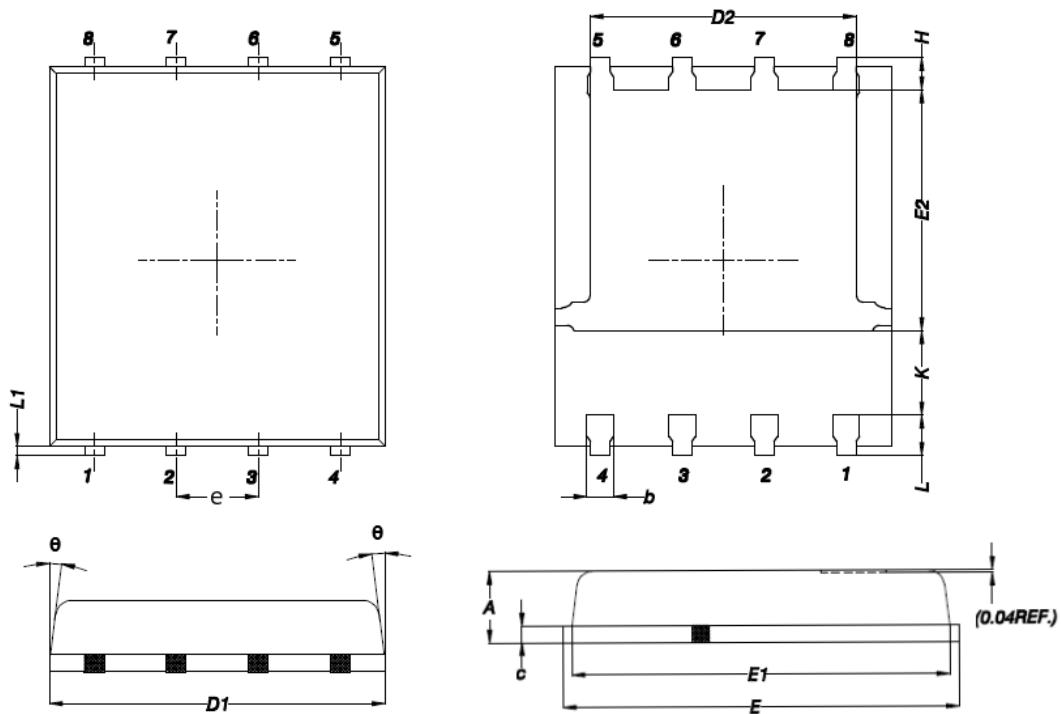
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_C

Fig.2 Normalized R_{DSON} vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Response

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

PPAK5x6 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.200	0.850	0.047	0.031
b	0.510	0.300	0.020	0.012
C	0.300	0.200	0.012	0.008
D1	5.400	4.800	0.212	0.189
D2	4.310	3.610	0.170	0.142
E	6.300	5.850	0.248	0.230
E1	5.960	5.450	0.235	0.215
E2	3.920	3.300	0.154	0.130
e	1.27BSC		0.05BSC	
H	0.650	0.380	0.026	0.015
K	---	1.100	---	0.043
L	0.710	0.380	0.028	0.015
L1	0.250	0.050	0.009	0.002
θ	12°	0°	12°	0°