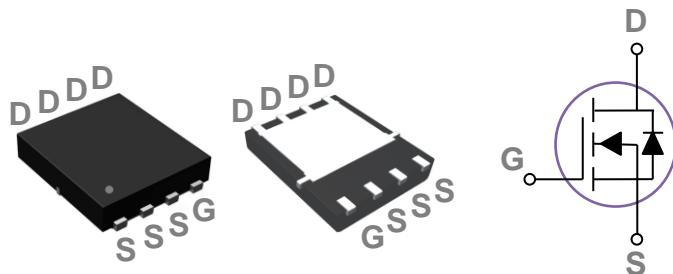


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### PPAK5X6 Pin Configuration



BVDSS	RDS(ON)	ID
100V	6.2mΩ	90A

### Features

- 100V,90A, RDS(ON) = $6.2\text{m}\Omega$ @VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>Ds</sub>	Drain-Source Voltage	100	V
V <sub>Gs</sub>	Gate-Source Voltage	$\pm 20$	V
I <sub>D</sub>	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	90	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	57	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	360	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	205	mJ
I <sub>AS</sub>	Single Pulse Avalanche Current <sup>2</sup>	64	A
P <sub>D</sub>	Power Dissipation ( $T_c=25^\circ\text{C}$ )	134	W
	Power Dissipation – Derate above 25°C	1.08	W/ $^\circ\text{C}$
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C/W}$
R <sub>θJC</sub>	Thermal Resistance Junction to Case	---	0.93	$^\circ\text{C/W}$

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	100	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=80\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=80\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=85\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$ , $I_D=15\text{A}$	---	5.2	6.2	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=8\text{A}$	---	6.8	8.8	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	1.2	1.6	2.5	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=3\text{A}$	---	14	---	S

**Dynamic and switching Characteristics**

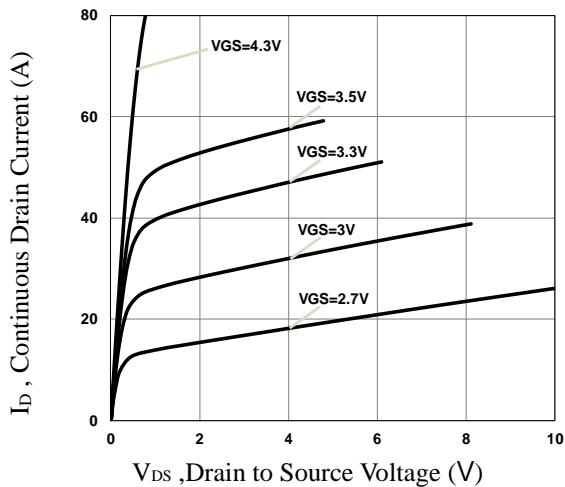
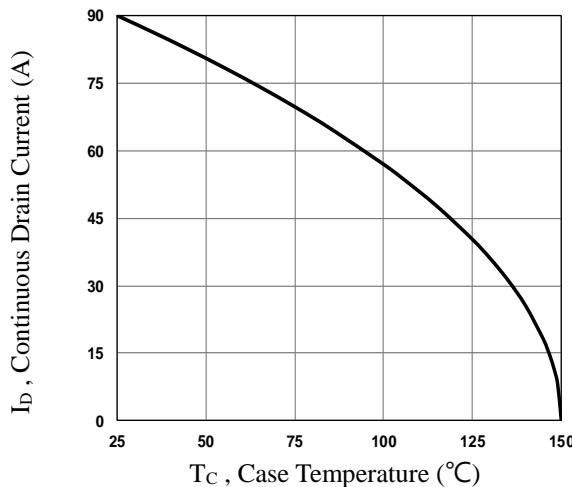
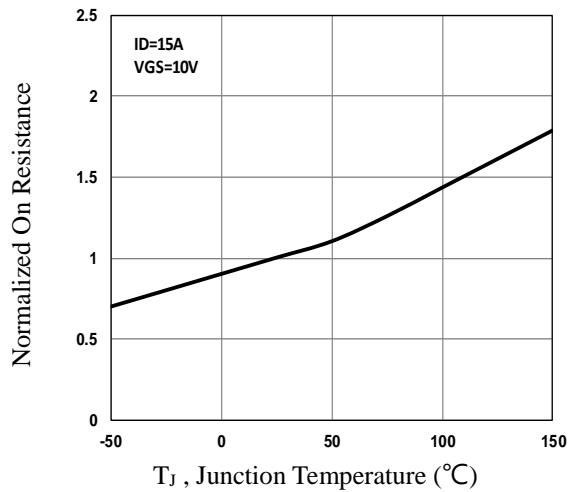
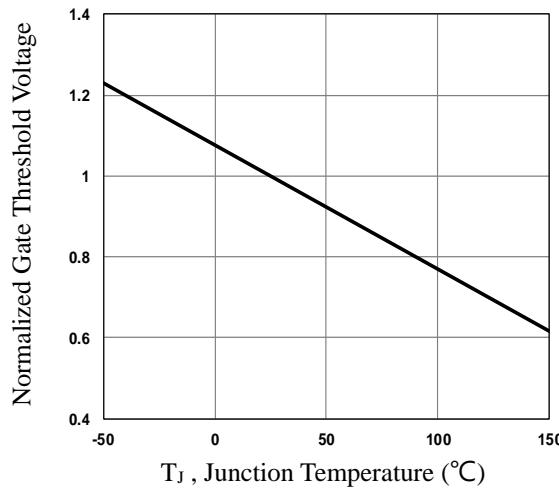
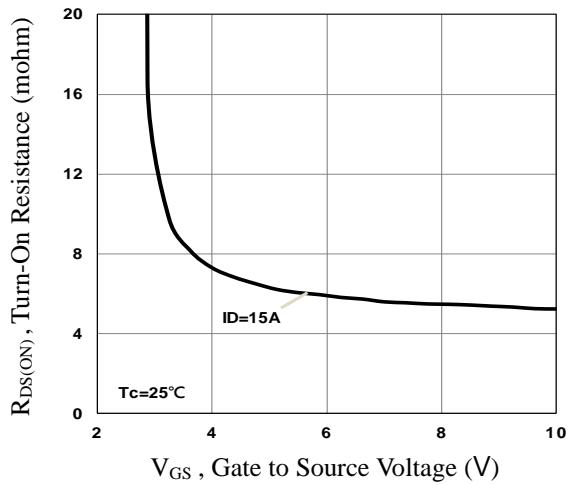
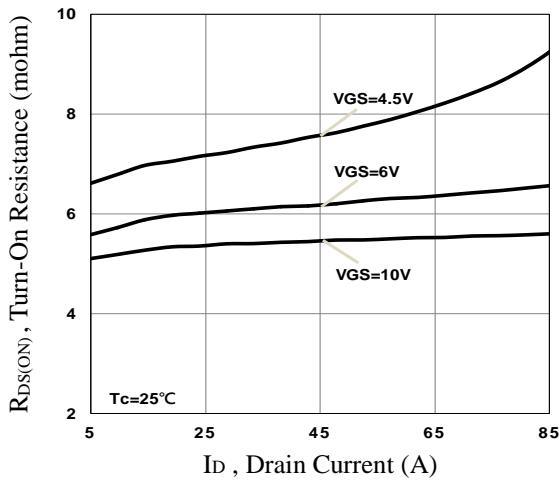
$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=50\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=45\text{A}$	---	37	55	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	5.3	8	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	8.7	13	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=50\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$ $I_D=45\text{A}$	---	15	23	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	28	42	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	45	68	
$T_f$	Fall Time <sup>3, 4</sup>		---	32	48	
$C_{iss}$	Input Capacitance	$V_{DS}=50\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	2420	3620	pF
$C_{oss}$	Output Capacitance		---	320	480	
$C_{rss}$	Reverse Transfer Capacitance		---	10	15	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	1.5	---	$\Omega$

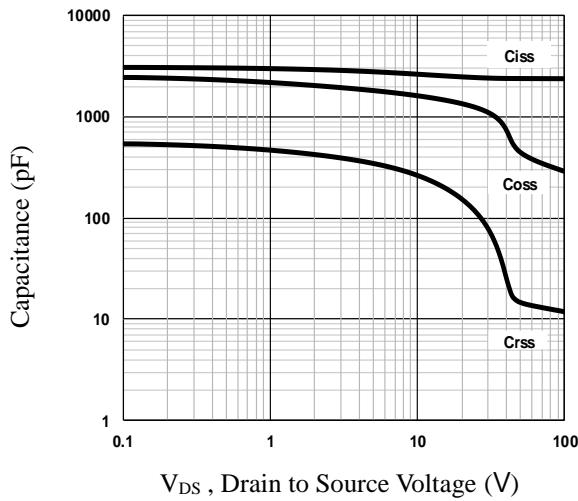
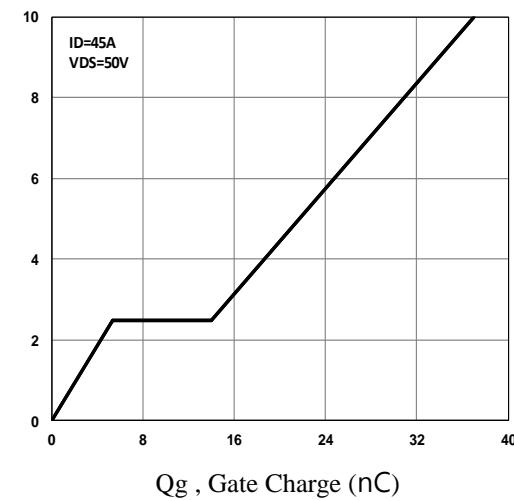
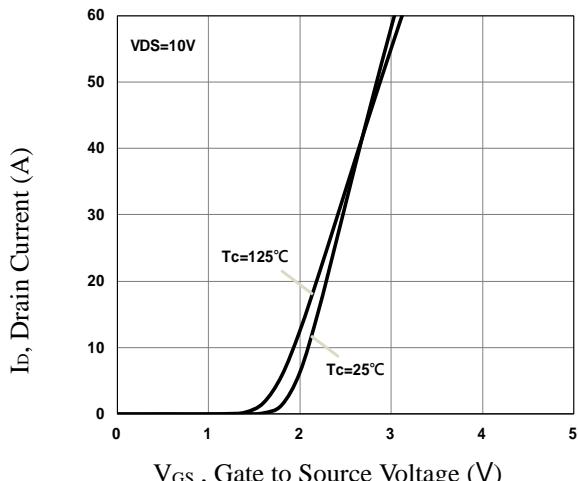
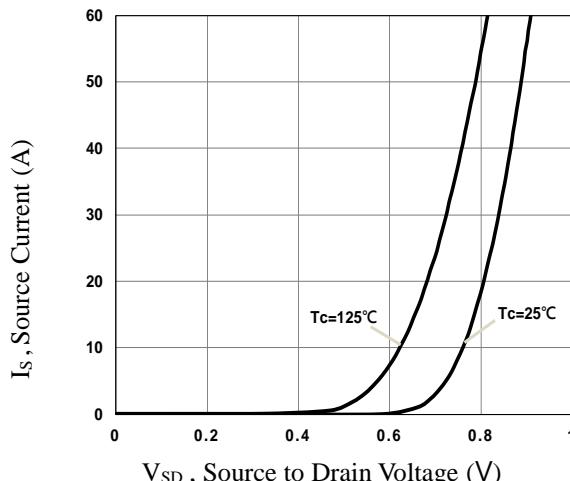
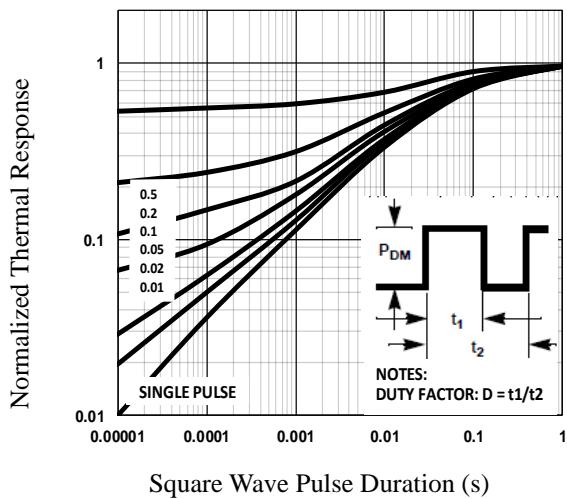
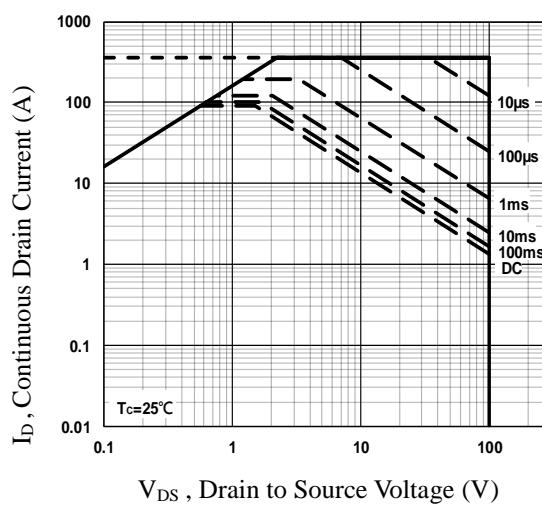
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	90	A
			---	---	180	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V
$t_{rr}$	Reverse Recovery Time	$V_R=100\text{V}$ , $I_R=10\text{A}$	---	220	---	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100\text{A}/\mu\text{s}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	435	---	nC

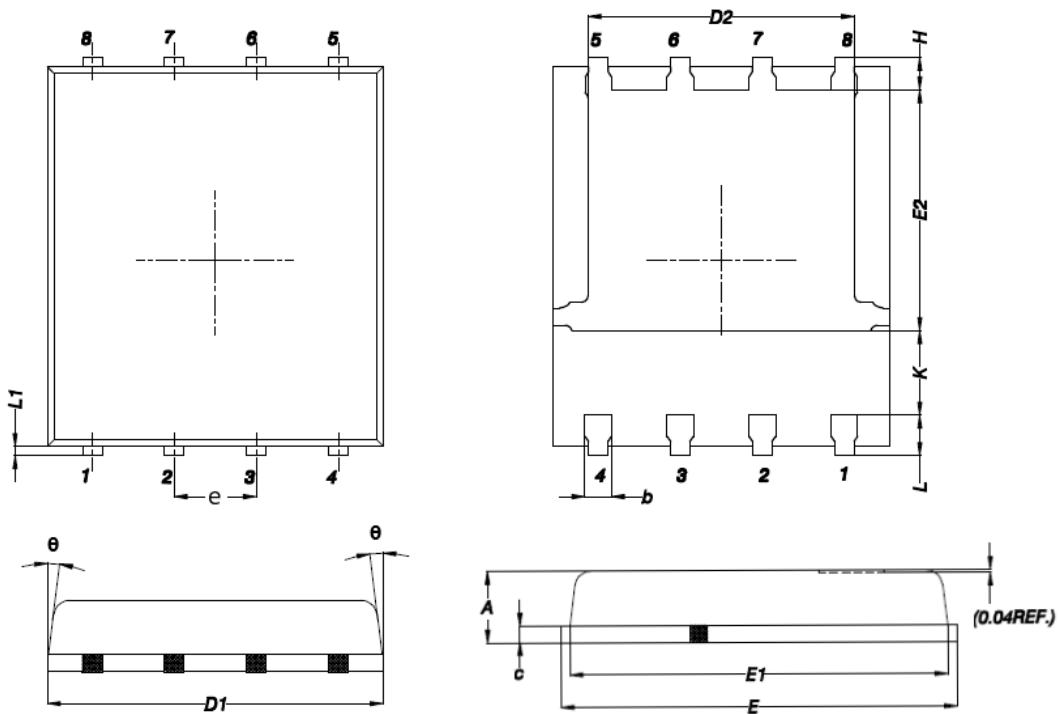
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=50\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=64\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25\text{ }^{\circ}\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Typical Output Characteristics**

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized  $R_{DSON}$  vs.  $T_j$** 

**Fig.4 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.5 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.6 Turn-On Resistance vs.  $I_D$**


**Fig.7 Capacitance Characteristics**

**Fig.8 Gate Charge Characteristics**

**Fig.9 Transfer Characteristics**

**Fig.10 Source Current vs.  $V_{SD}$** 

**Fig.11 Normalized Transient**

**Fig.12 Maximum Safe Operation Area**

## PPAK5x6 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.200	0.850	0.047	0.031
b	0.510	0.300	0.020	0.012
C	0.300	0.200	0.012	0.008
D1	5.400	4.800	0.212	0.189
D2	4.310	3.610	0.170	0.142
E	6.300	5.850	0.248	0.230
E1	5.960	5.450	0.235	0.215
E2	3.920	3.300	0.154	0.130
e	1.27BSC		0.05BSC	
H	0.650	0.380	0.026	0.015
K	---	1.100	---	0.043
L	0.710	0.380	0.028	0.015
L1	0.250	0.050	0.009	0.002
θ	12°	0°	12°	0°

## PPAK5X6 RECOMMENDED LAND PATTERN

