

### General Description

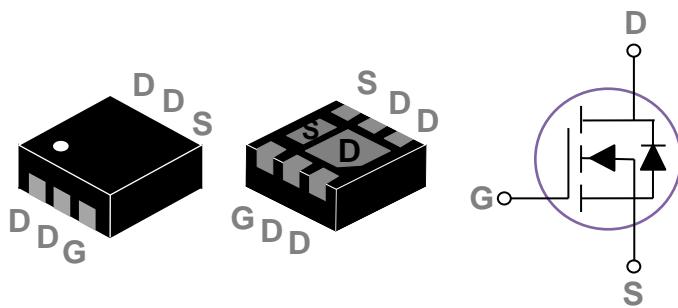
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
65V	17.4mΩ	7A

### Features

- 65V,7A,  $RDS(ON) = 17.4m\Omega$  @ $VGS = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

### DFN2x2-6L 2EP Pin Configuration



### Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	65	V
$V_{GS}$	Gate-Source Voltage	+20/-12	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ C$ )	7	A
	Drain Current – Continuous ( $T_A=70^\circ C$ )	5.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	28	A
$P_D$	Power Dissipation ( $T_A=25^\circ C$ )	2	W
	Power Dissipation – Derate above 25°C	0.016	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W



65V N-Channel MOSFETs

PDB6988L-5

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	65	---	---	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25\text{ }^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=85\text{ }^\circ\text{C}$	---	---	10	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	100	nA

**On Characteristics**

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=2\text{A}$	---	14.5	17.4	mΩ
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=1.5\text{A}$	---	24	31	mΩ
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	1.2	1.6	2.5	V
$\text{gfs}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=1\text{A}$	---	3	---	S

**Dynamic and switching Characteristics**

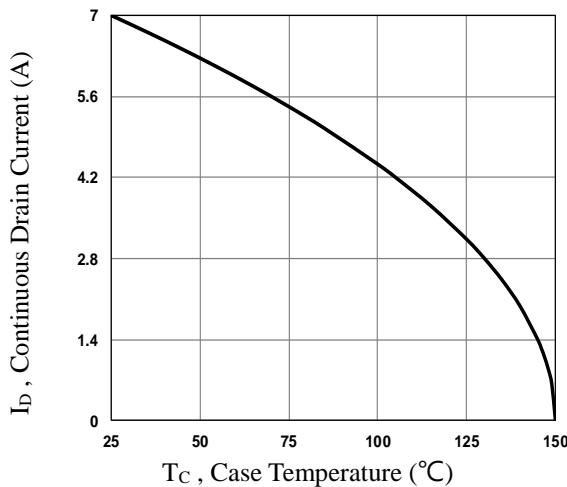
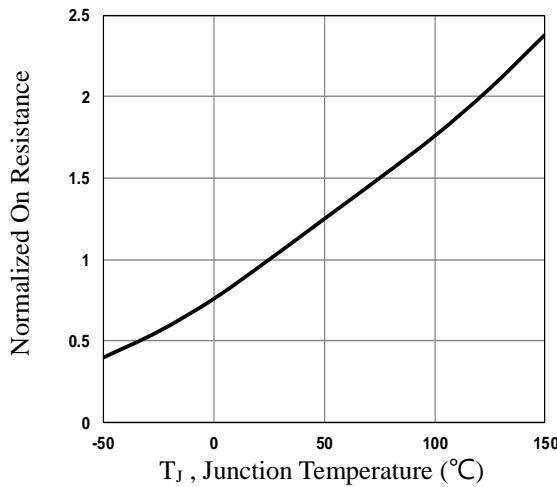
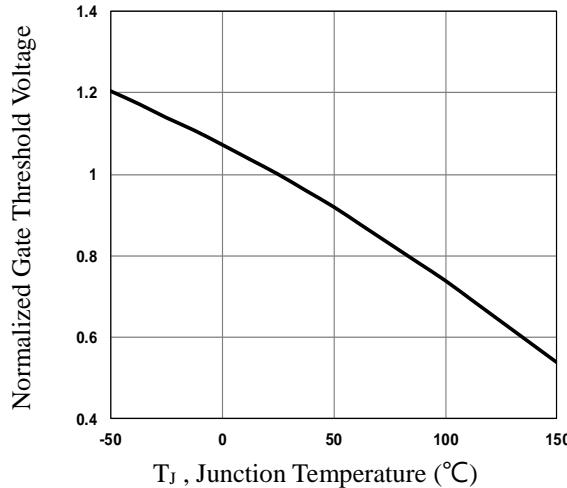
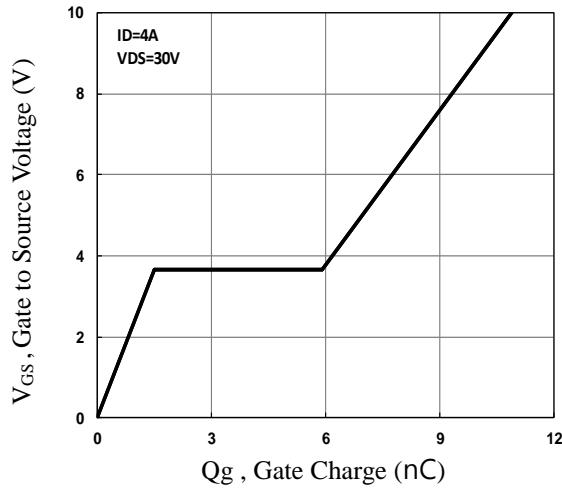
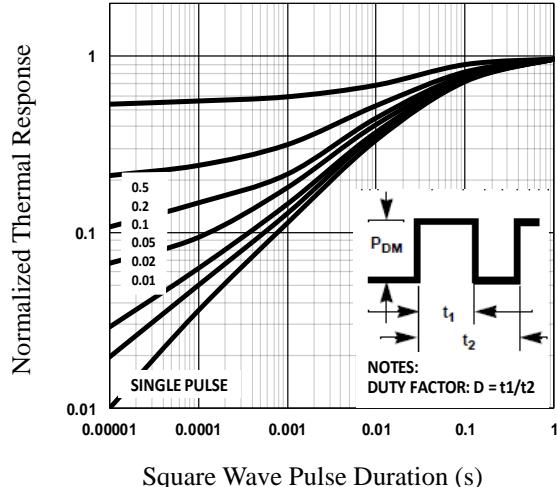
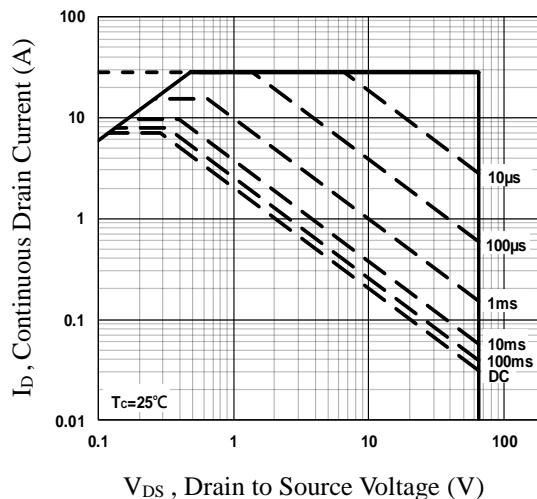
$\text{Q}_g$	Total Gate Charge <sup>2, 3</sup>	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=4\text{A}$	---	10.9	22	nC
$\text{Q}_{\text{gs}}$	Gate-Source Charge <sup>2, 3</sup>		---	1.5	3	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge <sup>2, 3</sup>		---	4.4	9	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{\text{DD}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=6\Omega$ $I_D=4\text{A}$	---	8	16	ns
$\text{T}_r$	Rise Time <sup>2, 3</sup>		---	12	24	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time <sup>2, 3</sup>		---	25	50	
$\text{T}_f$	Fall Time <sup>2, 3</sup>		---	18	36	
$\text{C}_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	653	1300	pF
$\text{C}_{\text{oss}}$	Output Capacitance		---	192	380	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	27	60	
$\text{R}_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $f=1\text{MHz}$	---	0.8	---	Ω

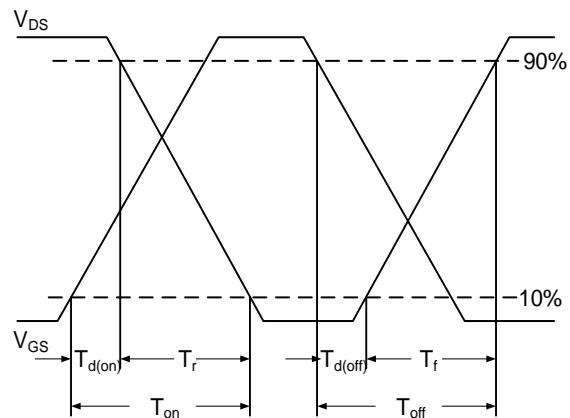
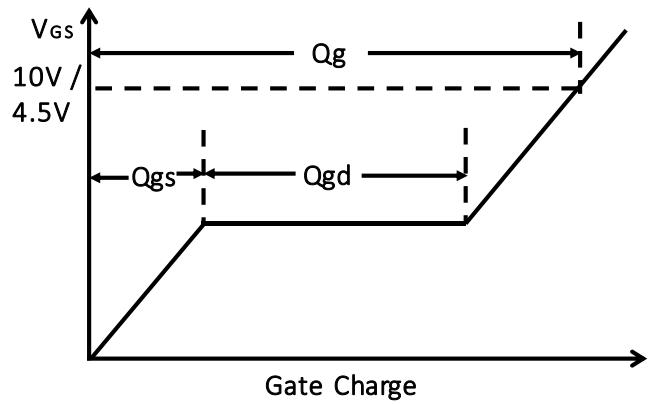
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{I}_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	7	A
			---	---	14	A
$\text{I}_{\text{SM}}$	Pulsed Source Current <sup>3</sup>					
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>3</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^\circ\text{C}$	---	---	1	V
$\text{t}_{\text{rr}}$	Reverse Recovery Time	$V_R=50\text{V}$ , $I_s=4\text{A}$	---	30	---	ns
$\text{Q}_{\text{rr}}$	Reverse Recovery Charge	$di/dt=100\text{A}/\mu\text{s}$ , $T_J=25\text{ }^\circ\text{C}$	---	15	---	nC

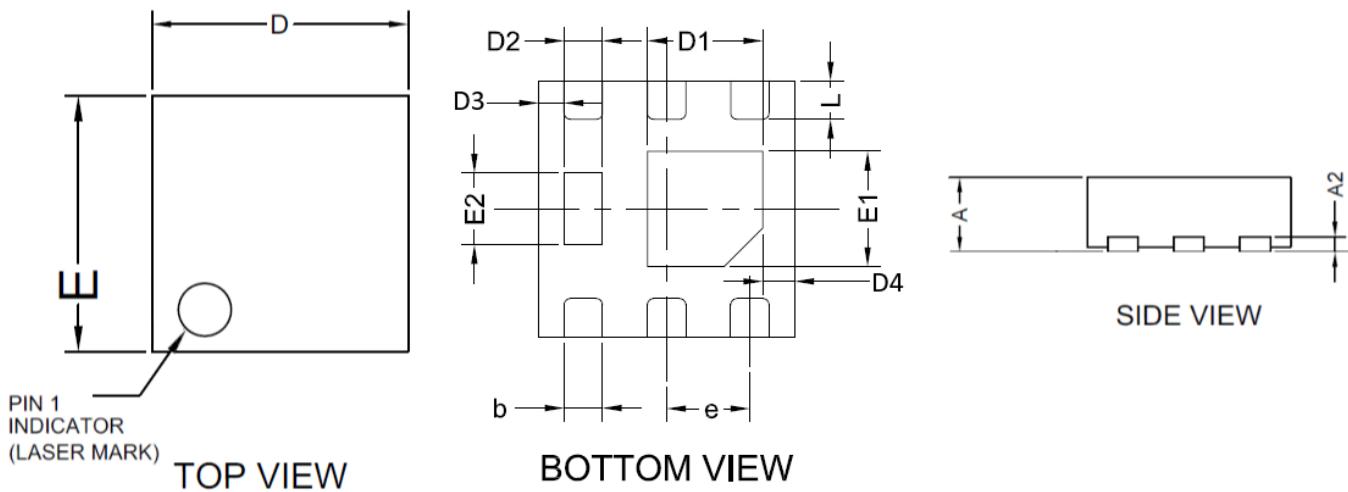
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_c$** 

**Fig.2 Normalized RD<sub>SON</sub> vs.  $T_J$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 Gate Charge Waveform**

## DFN2x2-6L 2EP PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.800	0.500	0.031	0.019
A2	0.250	0.145	0.010	0.006
b	0.350	0.250	0.014	0.010
D	2.100	1.900	0.083	0.075
D1	1.050	0.800	0.041	0.031
D2	0.430	0.250	0.017	0.010
D3	0.200BSC		0.008BSC	
D4	0.200BSC		0.008BSC	
E	2.100	1.900	0.083	0.075
E1	1.250	0.800	0.049	0.031
E2	0.750	0.460	0.029	0.018
e	0.650BSC		0.026BSC	
L	0.350	0.225	0.014	0.009

## RECOMMENDED LAND PATTERN

**DFN2X2-6L 2EP**

