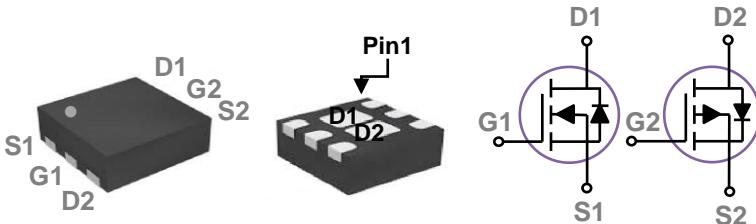


### General Description

These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### DFN2X2 Dual 2EP Pin Configuration



BVDSS	RDSON	ID
20V	40mΩ	3.8A
-20V	100mΩ	-2.5A

### Features

- Fast switching
- Green Device Available
- Suit for 1.8V Gate Drive Applications

### Applications

- Notebook
- Load Switch
- Networking
- Hand-held Instruments

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating		Units
$V_{DS}$	Drain-Source Voltage	20	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 10$	$\pm 10$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ\text{C}$ )	3.8	-2.5	A
	Drain Current – Continuous ( $T_A=100^\circ\text{C}$ )	2.3	-1.5	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	15.2	-10	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	1.25	1.25	W
	Power Dissipation – Derate above 25°C	0.01	0.01	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	100	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	15	°C/W



20V N+P Dual Channel MOSFETs

PDB2116S

**N-CH Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.02	---	$\text{V}/^{\circ}\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=20\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=16\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 10\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=4.5\text{V}$ , $I_D=3\text{A}$	---	30	40	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$ , $I_D=2\text{A}$	---	42	55	$\text{m}\Omega$
		$V_{GS}=1.8\text{V}$ , $I_D=1.5\text{A}$	---	55	70	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	0.3	0.6	1	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	-2	---	$\text{mV}/^{\circ}\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=2\text{A}$	---	4.4	---	S

**Dynamic and switching Characteristics**

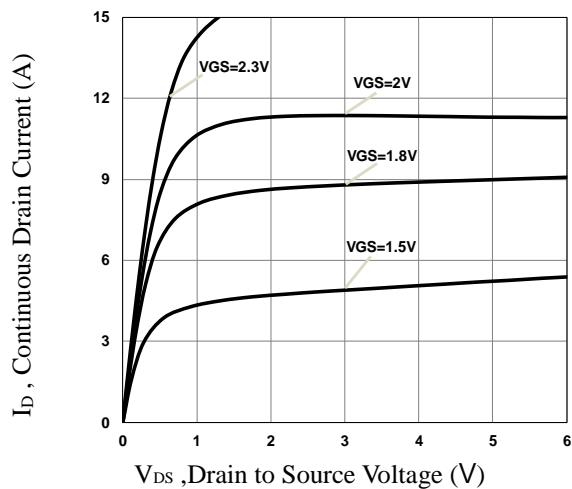
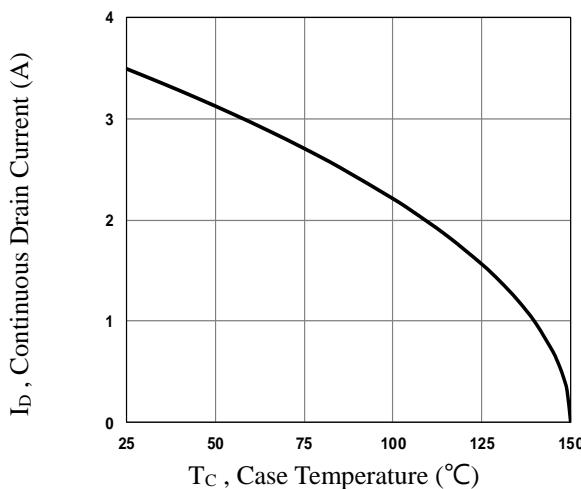
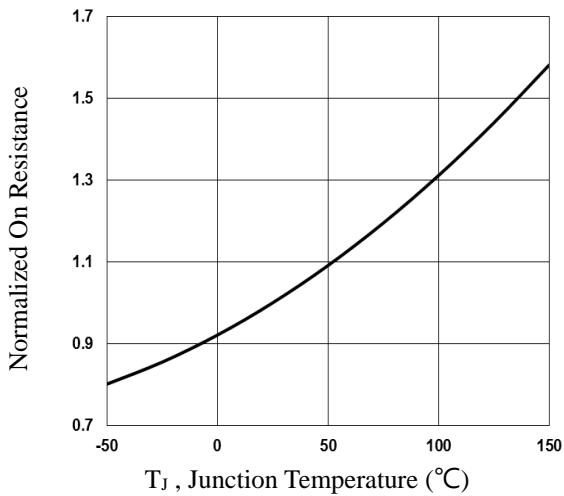
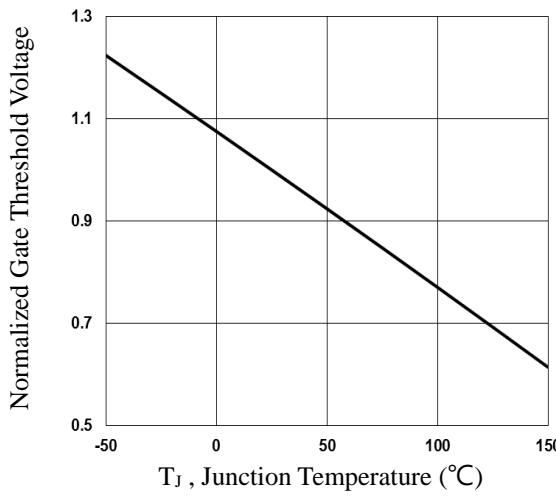
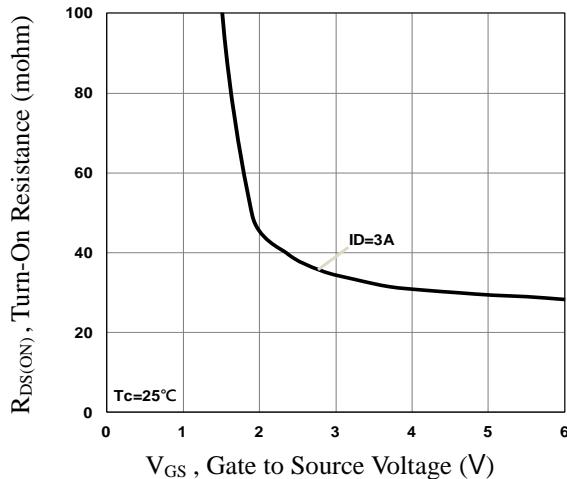
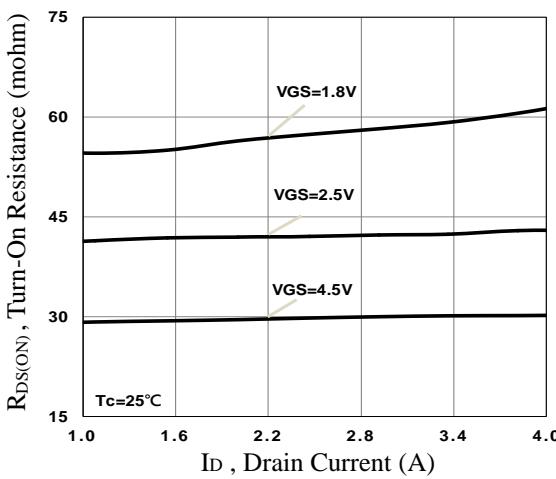
$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{DS}=10\text{V}$ , $V_{GS}=4.5\text{V}$ , $I_D=3\text{A}$	---	5.8	10	nC
$Q_{gs}$	Gate-Source Charge <sup>2, 3</sup>		---	0.6	1.5	
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>		---	1.5	3	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{DD}=10\text{V}$ , $V_{GS}=4.5\text{V}$ , $R_G=25\Omega$ $I_D=1\text{A}$	---	2.9	6	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	8.4	16	
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>		---	19.2	38	
$T_f$	Fall Time <sup>2, 3</sup>		---	5.6	12	
$C_{iss}$	Input Capacitance	$V_{DS}=15\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	315	600	pF
$C_{oss}$	Output Capacitance		---	50	80	
$C_{rss}$	Reverse Transfer Capacitance		---	40	60	

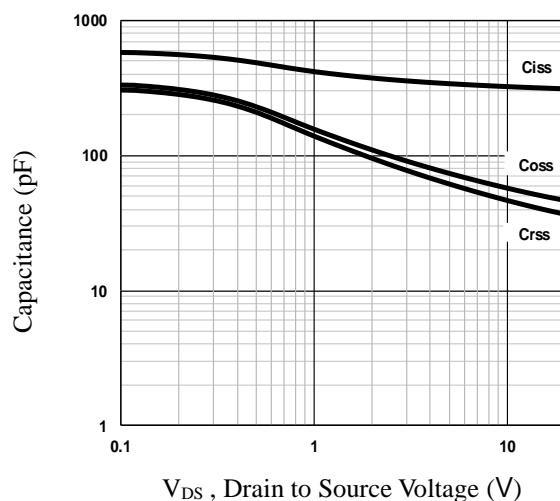
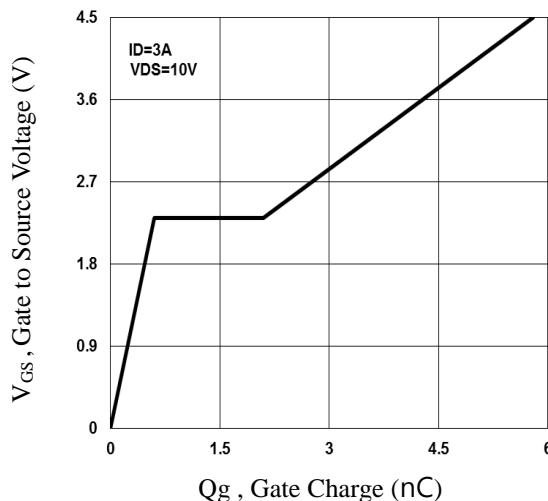
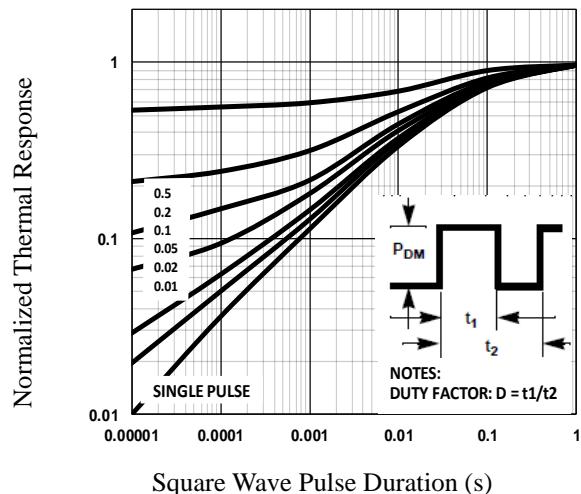
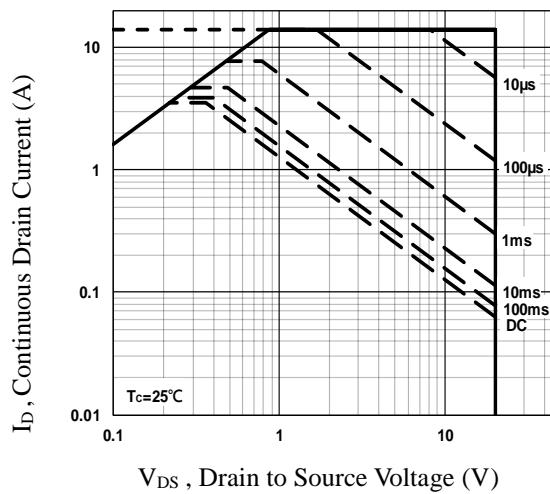
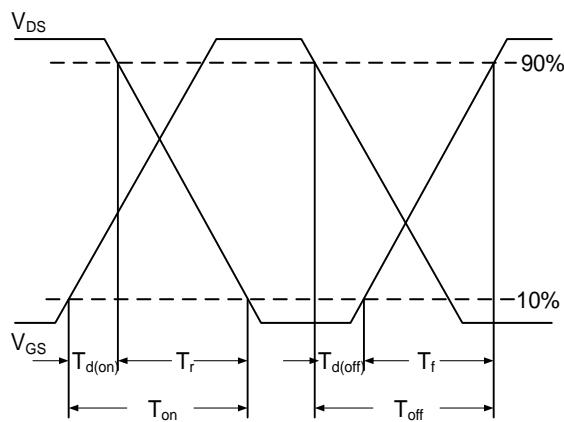
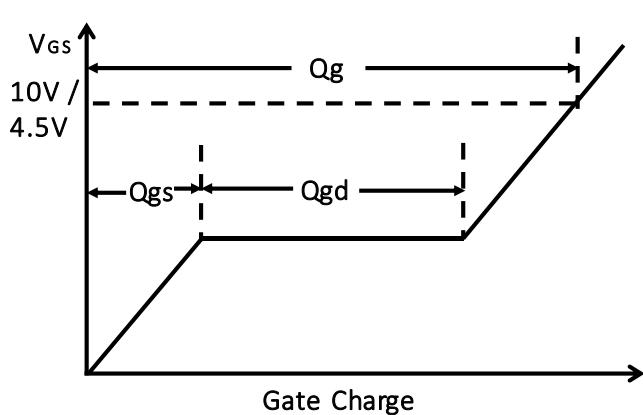
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	3.8	A
$I_{SM}$	Pulsed Source Current		---	---	7.6	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Essentially independent of operating temperature.


**Fig.1 Typical Output Characteristics**

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized  $R_{DSON}$  vs.  $T_J$** 

**Fig.4 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.5 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.6 Turn-On Resistance vs.  $I_D$**


**Fig.7 Capacitance Characteristics**

**Fig.8 Gate Charge Characteristics**

**Fig.9 Normalized Transient Impedance**

**Fig.10 Maximum Safe Operation Area**

**Fig.11 Switching Time Waveform**

**Fig.12 Gate Charge Waveform**



20V N+P Dual Channel MOSFETs

PDB2116S

**P-CH Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-20	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=-1\text{mA}$	---	-0.01	---	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-20\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{\text{DS}}=-16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	-10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 10\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-3\text{A}$	---	82	100	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}$ , $I_D=-2\text{A}$	---	125	140	$\text{m}\Omega$
		$V_{\text{GS}}=-1.8\text{V}$ , $I_D=-1\text{A}$	---	197	230	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=-250\mu\text{A}$	-0.3	-0.6	-1.0	V
$\Delta V_{\text{GS(th)}}$			---	3	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$ , $I_D=-1\text{A}$	---	2.2	---	S

**Dynamic and switching Characteristics**

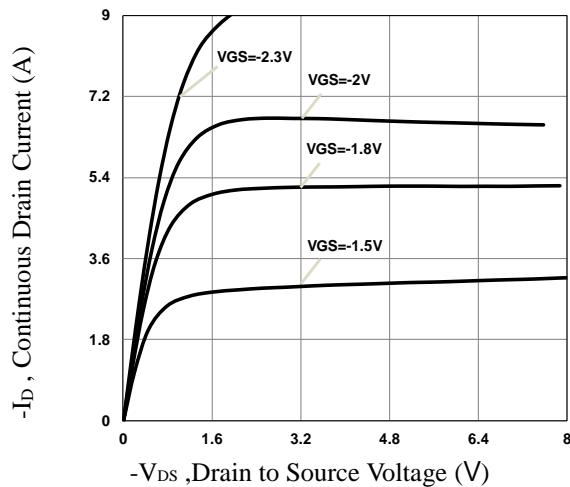
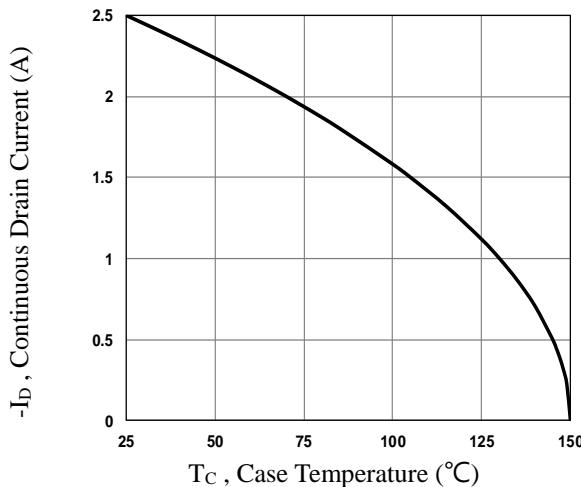
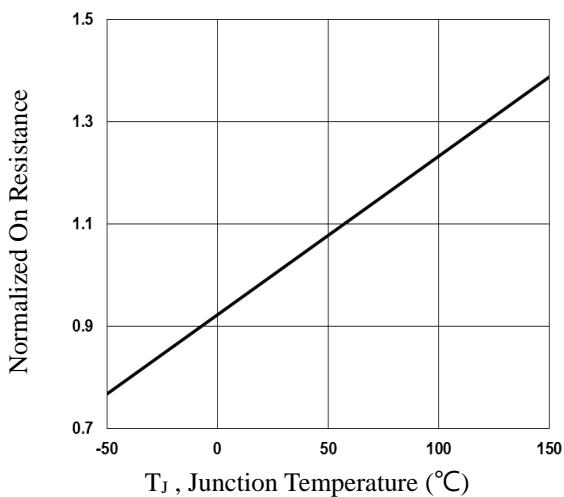
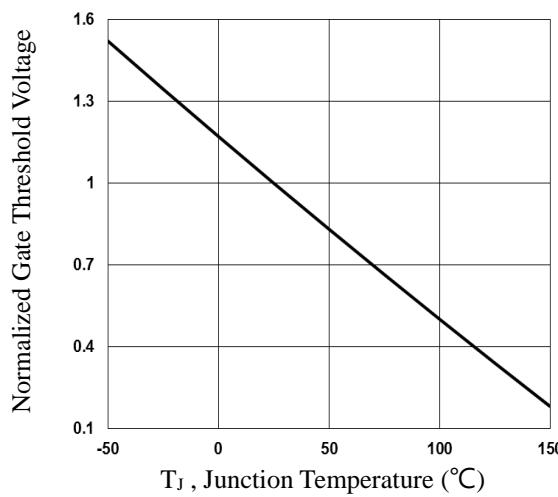
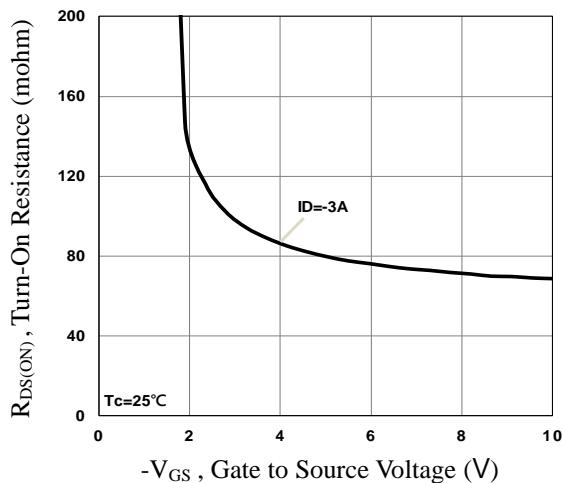
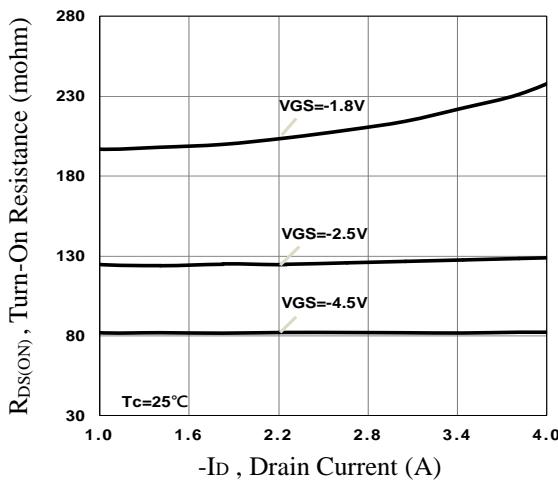
$Q_g$	Total Gate Charge <sup>4, 5</sup>	$V_{\text{DS}}=-10\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $I_D=-2\text{A}$	---	4.8	10	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>4, 5</sup>		---	0.5	1	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>4, 5</sup>		---	1.9	4	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>4, 5</sup>	$V_{\text{DD}}=-10\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $R_G=25\Omega$ $I_D=-1\text{A}$	---	3.5	7	ns
$T_r$	Rise Time <sup>4, 5</sup>		---	12.6	24	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>4, 5</sup>		---	32.6	62	
$T_f$	Fall Time <sup>4, 5</sup>		---	8.4	16	
$C_{\text{iss}}$	Input Capacitance		---	350	510	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=-15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	65	95	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	50	75	

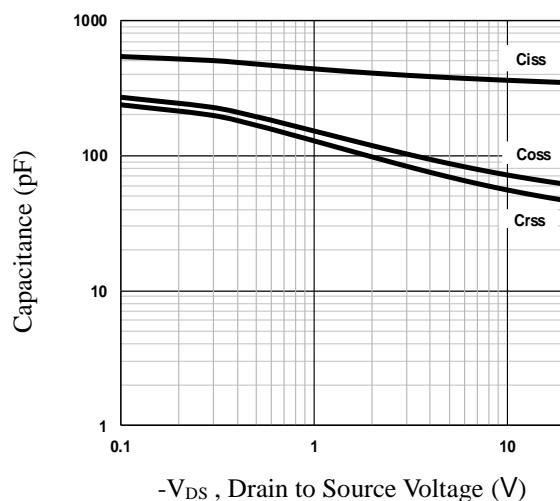
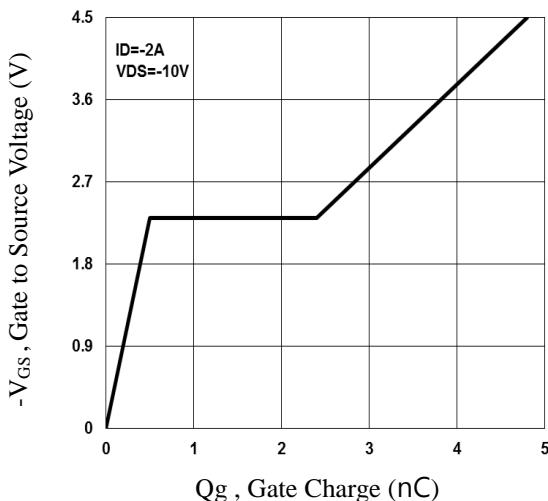
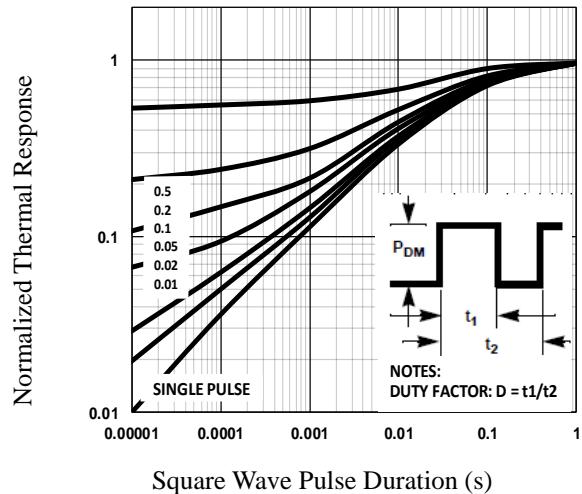
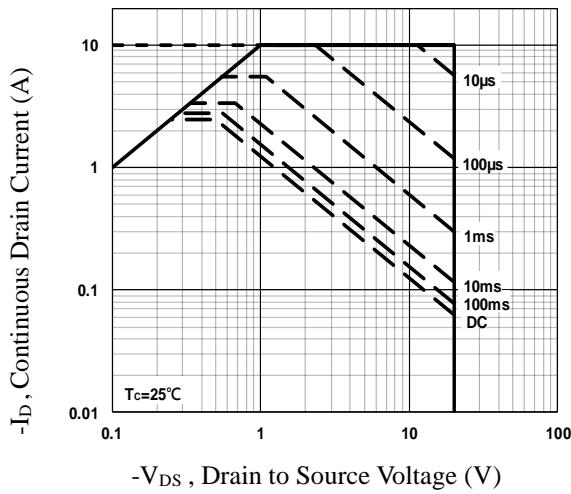
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-2.5	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-5	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1	V

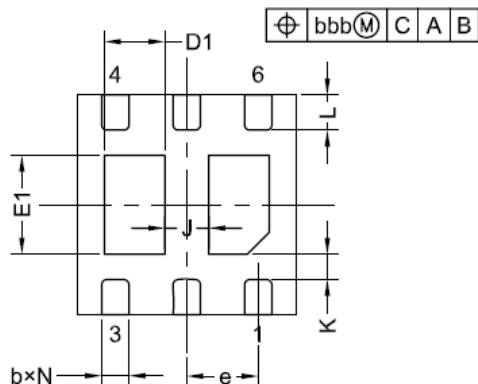
Note :

4. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 5. Essentially independent of operating temperature.

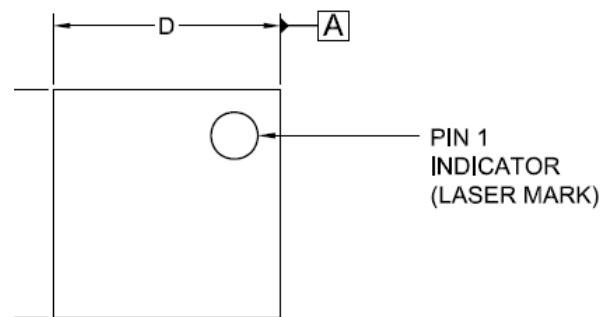

**Fig.13 Typical Output Characteristics**

**Fig.14 Continuous Drain Current vs.  $T_c$** 

**Fig.15 Normalized  $R_{DS(ON)}$  vs.  $T_J$** 

**Fig.16 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.17 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.18 Turn-On Resistance vs.  $I_D$**


**Fig.19 Capacitance Characteristics**

**Fig.20 Gate Charge Characteristics**

**Fig.21 Normalized Transient Impedance**

**Fig.22 Maximum Safe Operation Area**

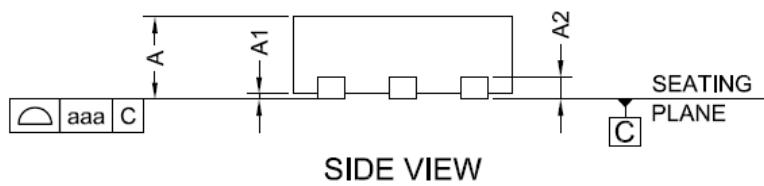
## PPAK2X2 Dual 2EP PACKAGE INFORMATION



BOTTOM VIEW



TOP VIEW



SIDE VIEW

COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2		0.203	
b	0.20	0.25	0.30
D	1.95	2.00	2.05
D1	0.50	0.55	0.60
E	1.95	2.00	2.05
E1	0.85	0.90	0.95
e		0.65BSC	
L	0.27	0.32	0.37
J		0.40BSC	
K		0.20MIN	
N		6	
aaa		0.08	
bbb		0.10	