

General Description

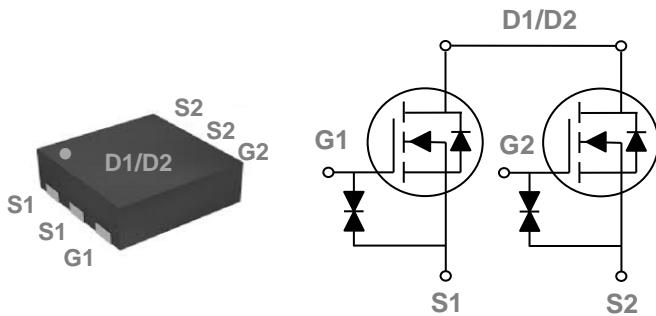
These dual N Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
12V	23mΩ	6A

Features

- 12V, 6A, RDS(ON) = 23mΩ @ VGS = 4.5V
- Fast switching
- Green Device Available
- G-S ESD Protection Diode Embedded

DFN2X2-6L Dual 1EP Pin Configuration



Applications

- Notebook
- Load Switch
- Networking
- Hand-held Instruments

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	12	V
V_{GS}	Gate-Source Voltage	± 8	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	6	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	3.8	A
I_{DM}	Drain Current – Pulsed ¹	36	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	1.25	W
	Power Dissipation – Derate above 25°C	0.01	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	100	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise) noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	12	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =12V , V _{GS} =0V , T _J =25°C	---	---	1	uA
		V _{DS} =10V , V _{GS} =0V , T _J =125°C	---	---	10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±8V , V _{DS} =0V	---	---	±10	uA

On Characteristics

R _{Ds(on)}	Static Drain-Source On-Resistance	V _{GS} =4.5V , I _D =3A	15	19	23	mΩ
		V _{GS} =4.0V , I _D =3A	16	21	25	mΩ
		V _{GS} =3.8V , I _D =2A	17	22	27	mΩ
		V _{GS} =3.1V , I _D =2A	18	24	30	mΩ
		V _{GS} =2.5V , I _D =2A	19	26	34	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	0.3	0.6	1	V
gfs	Forward Transconductance	V _{DS} =10V , I _D =1A	---	3.4	---	S

Dynamic and switching Characteristics

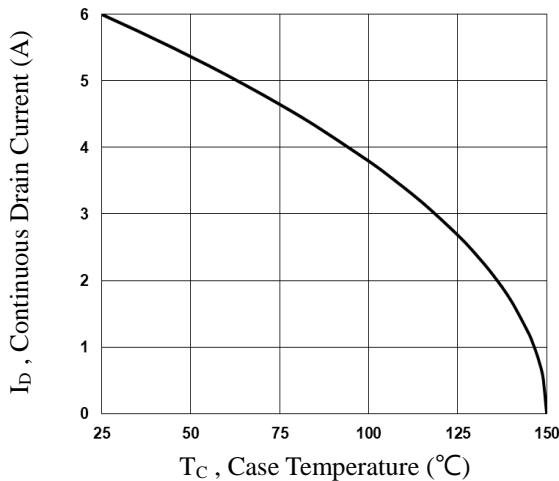
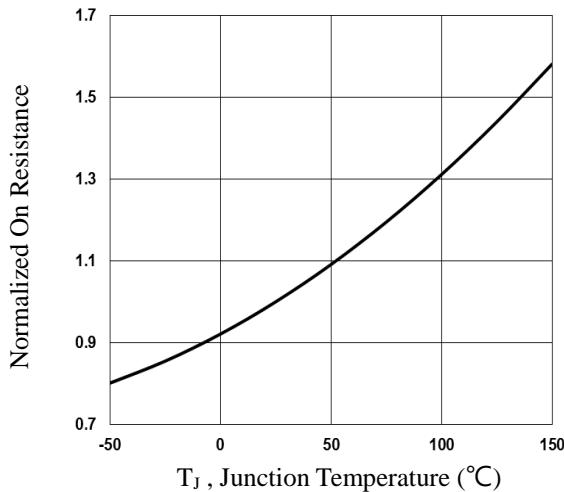
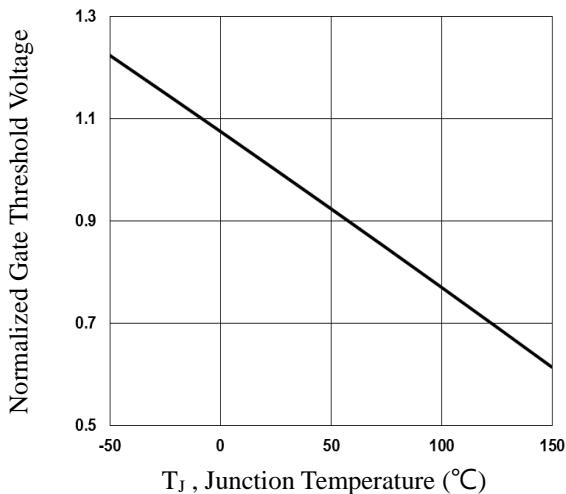
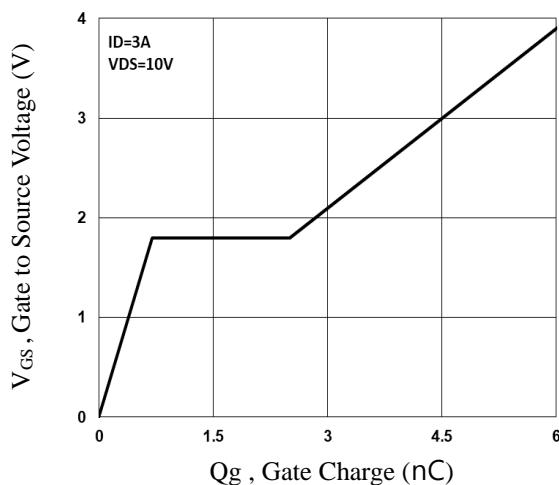
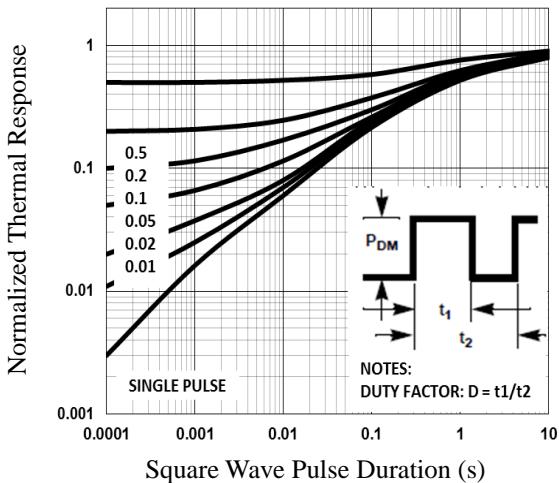
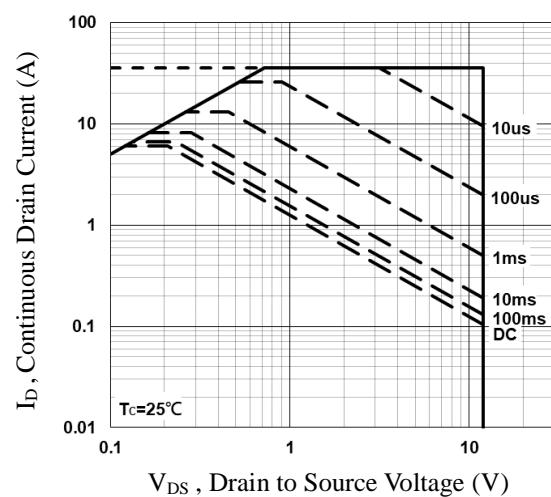
Q _g	Total Gate Charge ^{2, 3}	V _{DS} =10V , V _{GS} =4.5V , I _D =3A	---	7	---	nC
Q _{gs}	Gate-Source Charge ^{2, 3}		---	0.7	---	
Q _{gd}	Gate-Drain Charge ^{2, 3}		---	1.8	---	
T _{d(on)}	Turn-On Delay Time ^{2, 3}	V _{DD} =10V , V _{GS} =4.5V , R _G =25Ω I _D =1A	---	2.9	---	ns
T _r	Rise Time ^{2, 3}		---	8.4	---	
T _{d(off)}	Turn-Off Delay Time ^{2, 3}		---	19.2	---	
T _f	Fall Time ^{2, 3}		---	5.6	---	
C _{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , F=1MHz	---	370	---	pF
C _{oss}	Output Capacitance		---	60	---	
C _{rss}	Reverse Transfer Capacitance		---	48	---	

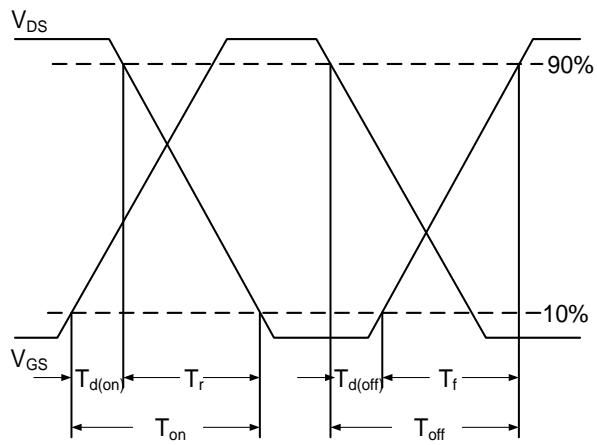
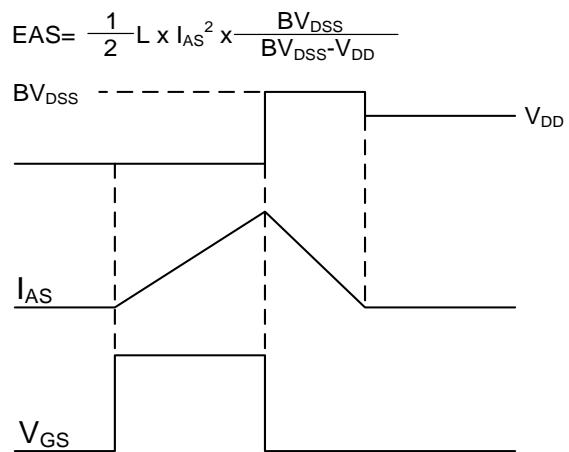
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _s	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	6	A
I _{SM}	Pulsed Source Current		---	---	12	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _s =1A , T _J =25°C	---	---	1	V

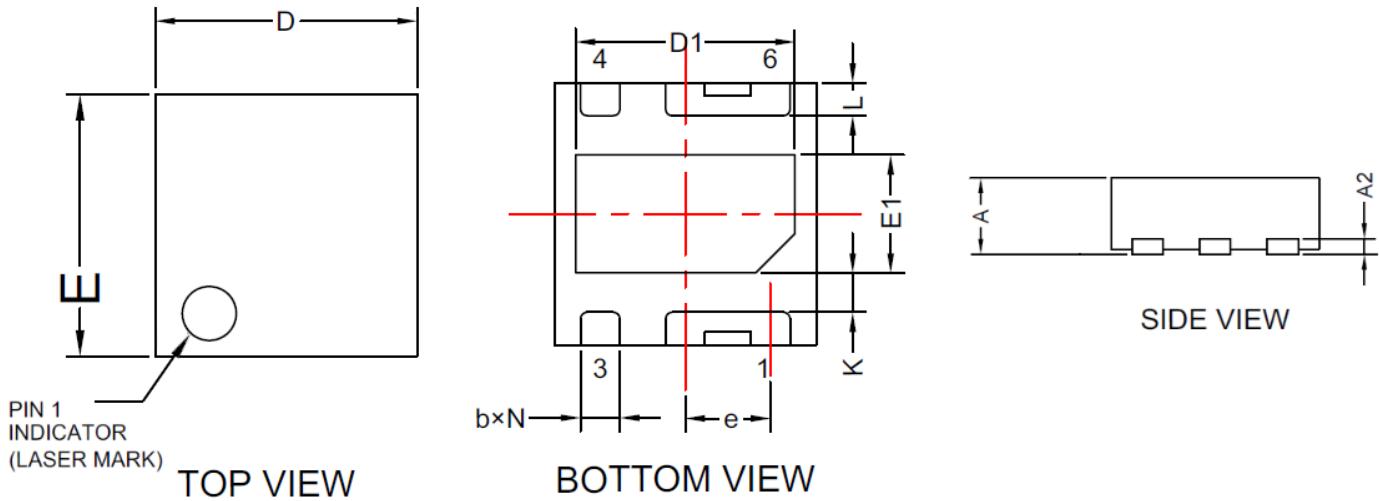
Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_c

Fig.2 Normalized RDSON vs. T_j

Fig.3 Normalized V_{th} vs. T_j

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 EAS Waveform

DFN2X2-6L Dual 1EP PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.600	0.500	0.024	0.019
A2	0.150REF		0.006REF	
b	0.350	0.250	0.014	0.010
D	2.050	1.950	0.081	0.076
D1	1.730	1.630	0.068	0.064
E	2.050	1.950	0.081	0.076
E1	0.950	0.850	0.037	0.033
e	0.650BSC		0.026BSC	
K	---	0.200	---	0.008
L	0.350	0.250	0.014	0.010
N	6pin		6pin	